

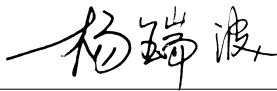
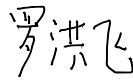
PRODUCT SPECIFICATION

CDTECH Model: **S070QWS176ED-DR09**

CUSTOMER Model: **-**

Description: **7.0 " TFT-LCD Module with RTP**

Version: **1.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2024.12.11	2024.12.11	2024.12.11

CUSTOMER APPROVAL	SIGNATURE	DATE



Record of Revisions

[illegible]



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1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	7.0	inch
Number of Pixels	1024 (H) RGB x 600 (V)	pixels
Display Mode	Normally Black	-
Viewing Direction	Free	-
Interface	LVDS	-
Display Colors	16.7M	colors
Outline Dimension	165.00 (H) x 100.00 (V) x 6.80 (D)	mm
Active Area	154.21 (H) x 85.92 (V)	mm
Pixel Pitch	0.1505 (H) x 0.1432 (V)	mm
Driver IC	EK79001HN+EK73215BCGA	-
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C

Note1:Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VDD	-0.3	5.0	V	Note 1

Note 1:Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

3. Electrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

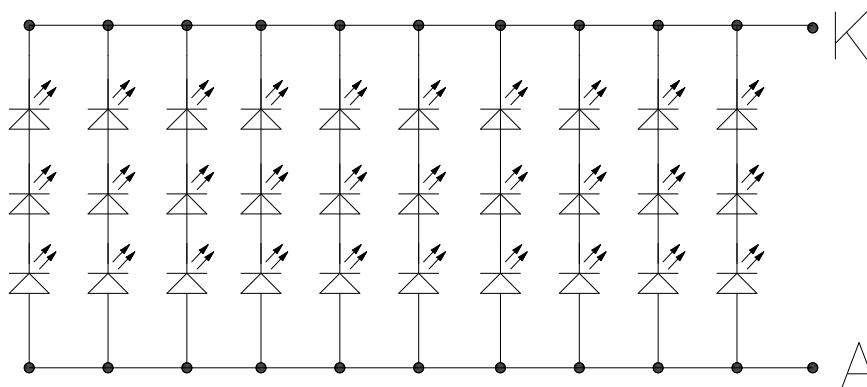
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VDD	3.0	3.3	3.6	V	
Analog supply current	I _{VDD}	-	TBD	-	mA	VDD=3.3V
Power supply for LCD	AVDD	8.0	11.0	13.5	V	
	VGH	16.5	17.0	17.7	V	
	VGL	-7.5	-7.0	-6.5	V	
	VCOM	3.7	4.2	4.7	V	
Logic input voltage	VIH	0.7*VDD	-	VDD	V	
	VIL	GND	-	0.3*VDD	V	

3.2 Recommended Driving Condition for Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I _F	-	400	-	mA	
Driving Voltage	V _F	8.1	-	10.2	V	
Power consumption	W _{BL}	3.24	-	4.08	W	
LED Life-Time	N/A	-	50,000	-	Hours	Ta=25°C Note 1

Note 1:LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2:LED circuit :



4. Interface Pin Assignment

4.1 LCM Pin Assignment

No.	Symbol	Description
1	VCOM	Common Voltage
2	VDD	Power supply
3	VDD	Power supply
4	NC	No connection
5	RESET	Global reset pin
6	STBYB	Standby mode Normally pulled high STBYB=1,normal operation STBYB=0,timing contrller,source driver will turn off,all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	-LVDS differential clock input
18	RXCLKIN+	+LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No connection
24	NC	No connection
25	GND	Ground
26	NC	No connection
27	DIMO	Backlight dimmer signal for external controller. DIMO ="0", Turn off external backlight controller DIMO ="1", Logical control signal to turn on external backlight controller
28	SELB(HSD)	Horizontal Sync input for TTL mode. Negative polarity. (In LVDS interface connected HSD to FPC for pin setting HSD="L":8 bit HSD="H":6 bit)
29	AVDD	Power for Analog Circuit
30	GND	Ground

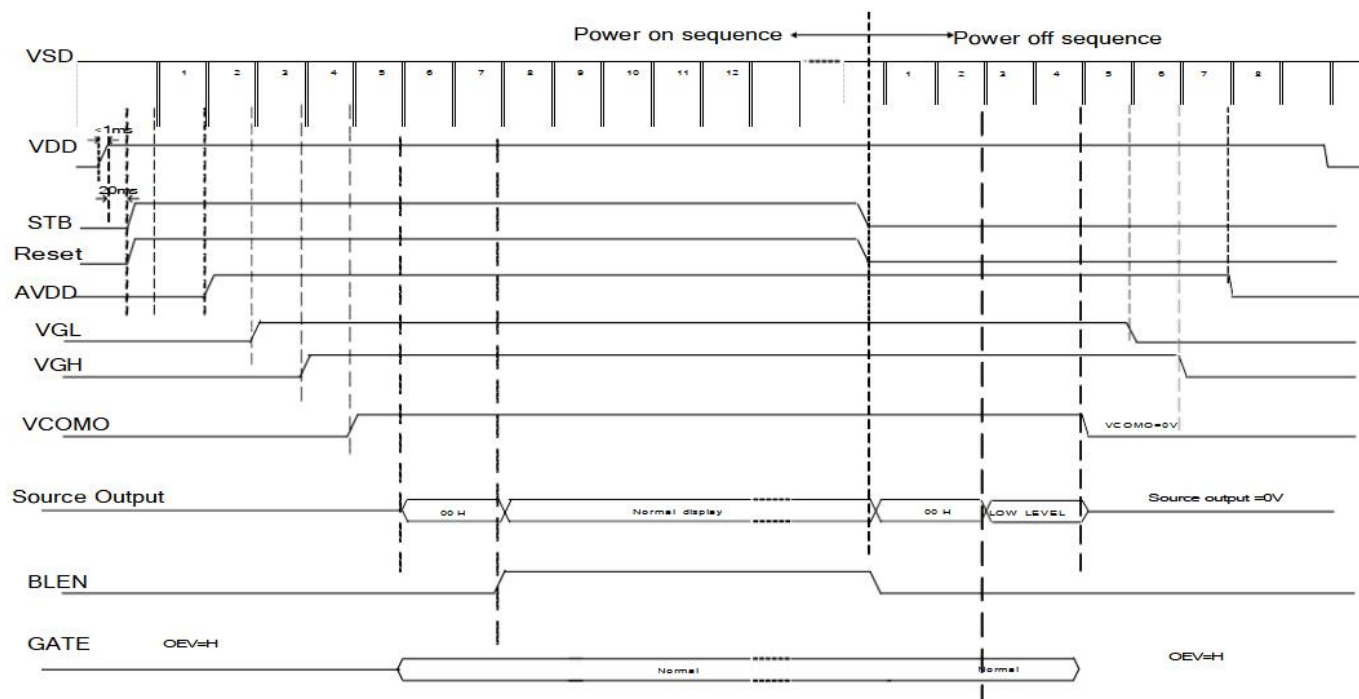
31	LED-	Power for LED backlight (Cathode)
32	LED-	Power for LED backlight (Cathode)
33	L/R	Source Right or Left sequence control. Normally pull high.
34	U/D	Gate Up or Down scan control. Normally pull low.
35	VGL	Gate off Voltage
36	NC	No connection
37	NC	No connection
38	VGH	Gate on Voltage
39	LED+	Power for LED backlight (Anode)
40	LED+	Power for LED backlight (Anode)

4.2 RTP Pin Assignment

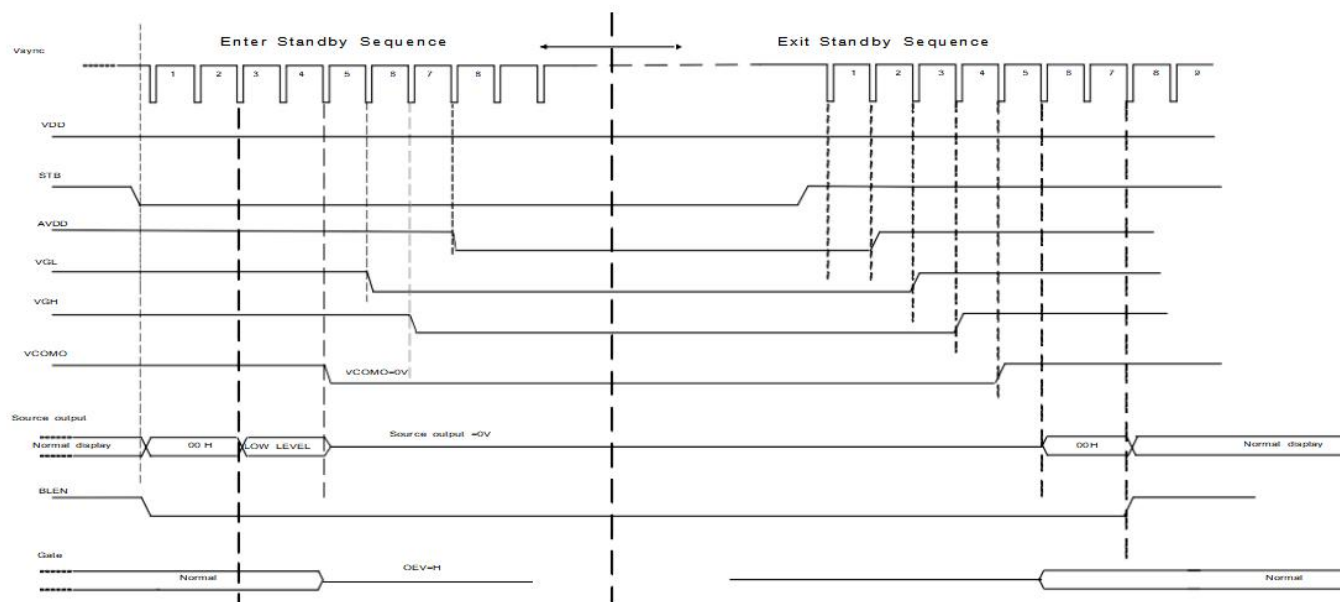
No.	Symbol	Description
1	XL	The left side signal of TP
2	YD	The down side signal of TP
3	XR	The right side signal of TP
4	YU	The up side signal of TP

5. Interface Characteristics

5.1 Power On/Off Sequence



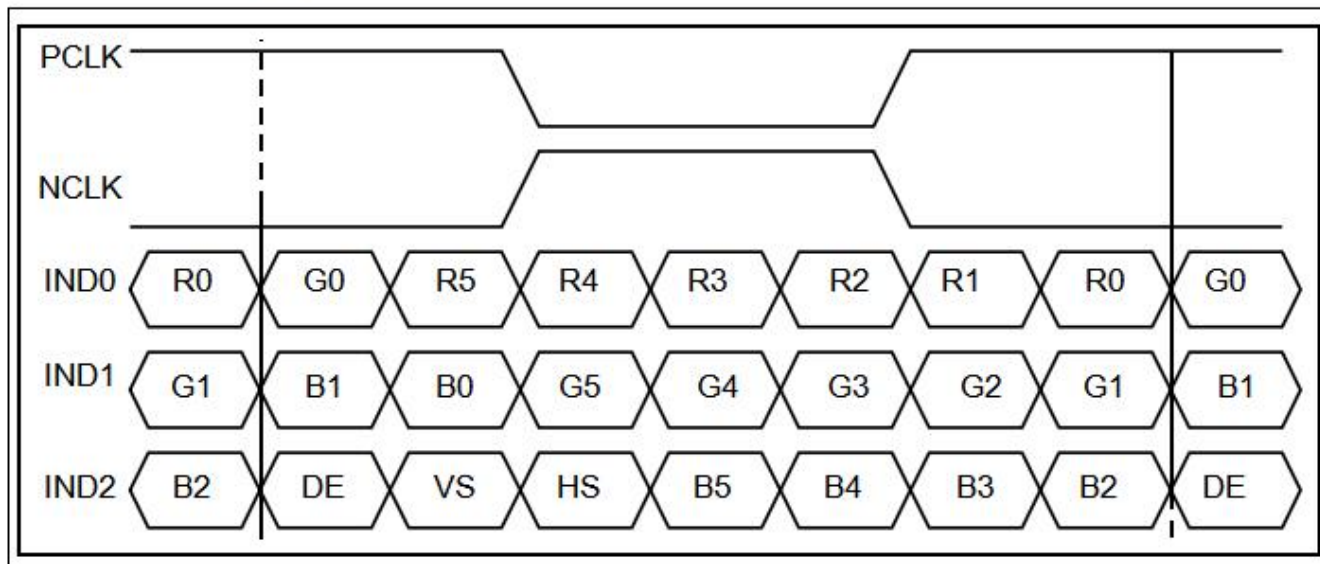
Power On/Off timing chart



Enter and Exit Standby Mode timing chart

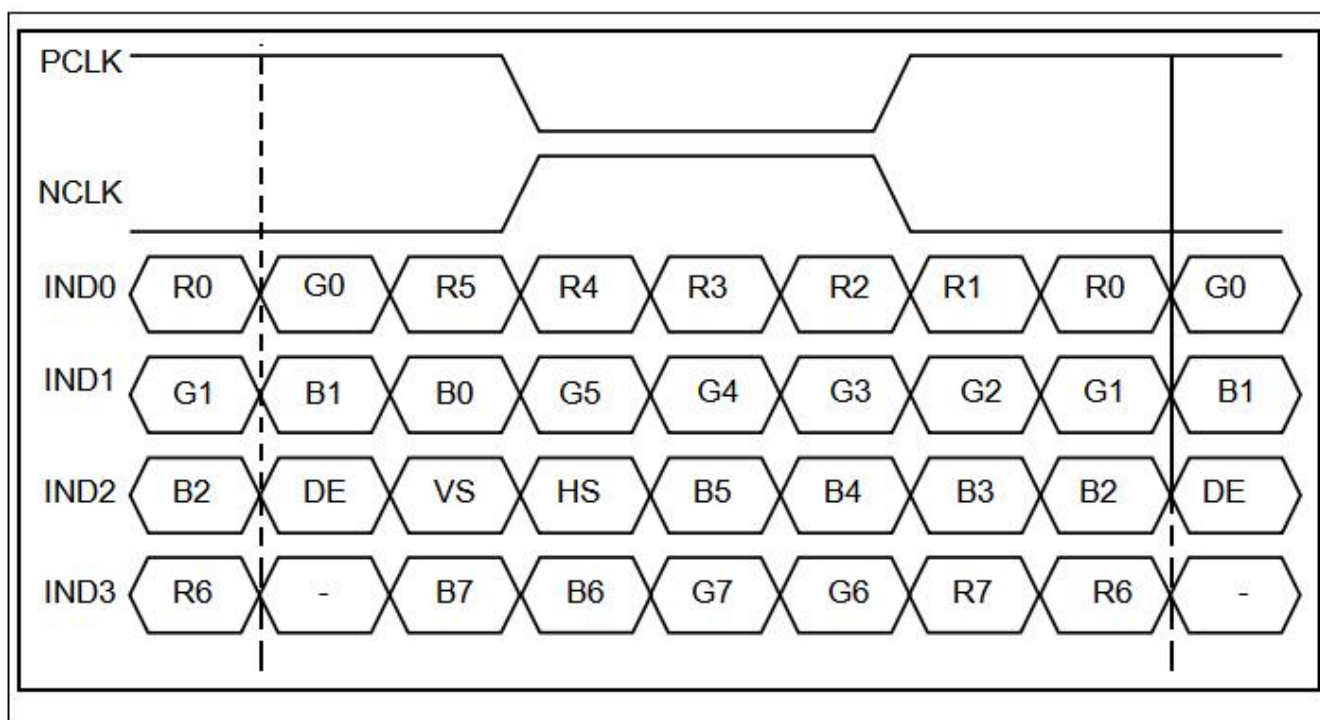
5.2 Data Input Format for LVDS

6-bit LVDS input(HSD="H")



6-bit LVDS Input Timing chart

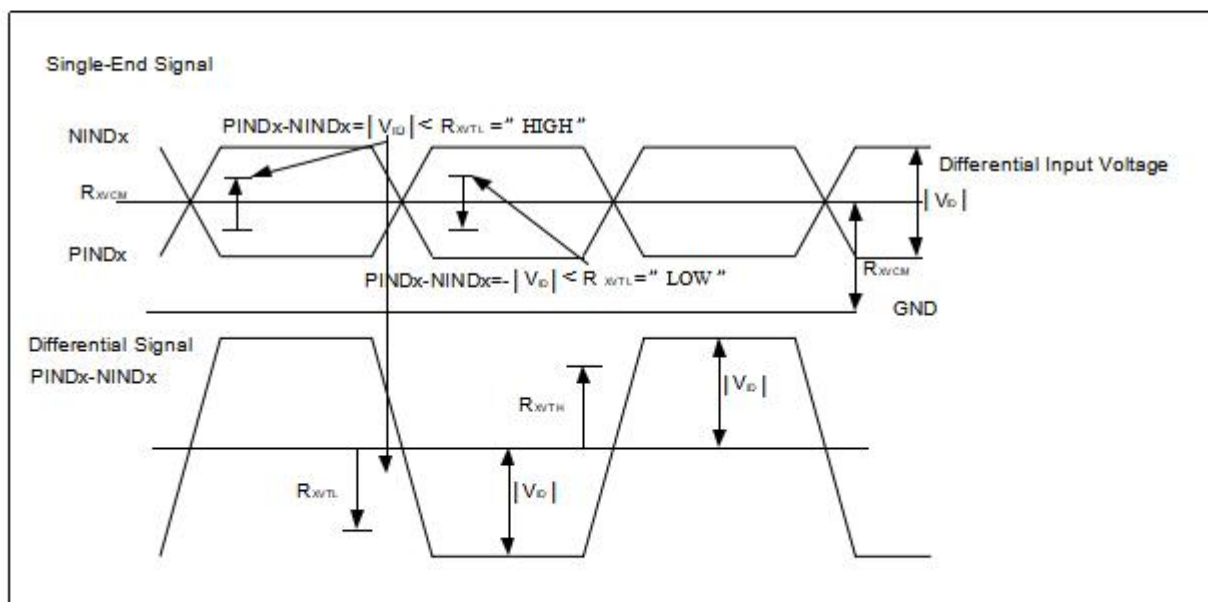
8-bit LVDS input(HSD="L")



5.3 LVDS mode

LVDS DC characteristic

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Differential input high threshold voltage	R_{xVTH}			+0.1V	V	$R_{xVCM}=1.2V$
Differential input low threshold voltage	R_{xVTL}	-0.1			V	
Input voltage range(single-end)	R_{xVIN}	0		2.4	V	
Differential input common mode voltage	R_{xVCM}	$ V_{ID} /2$		$2.4 - V_{ID} /2$	V	
Differential input voltage	$ V_{ID} $	0.2		0.6	V	
Differential input leakage current	R_{xVTH}	-10		+10	V	
LVDS Digital Operating Current	I_{ddlvds}	-	40(TBD)	50	mA	$F_{dk}=65Mhz$, $V_{DD}=3.3V$
LVDS Digital Standby Current	I_{stlvds}	-	10(TBD)	50	μA	Clock & all functions are stop

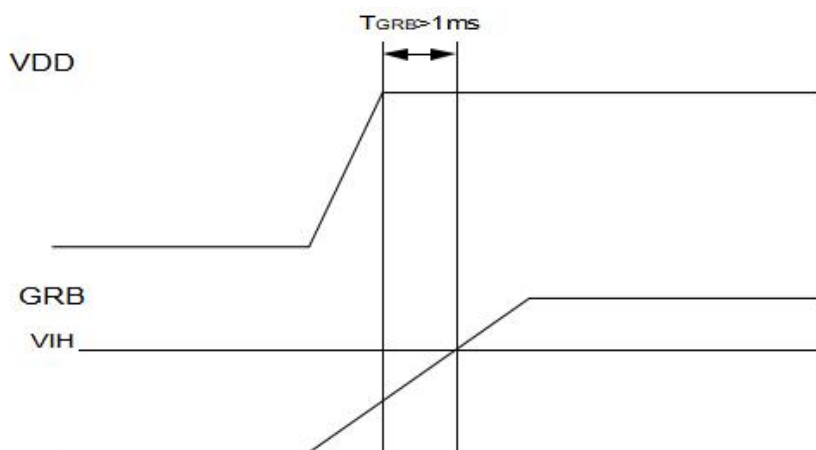


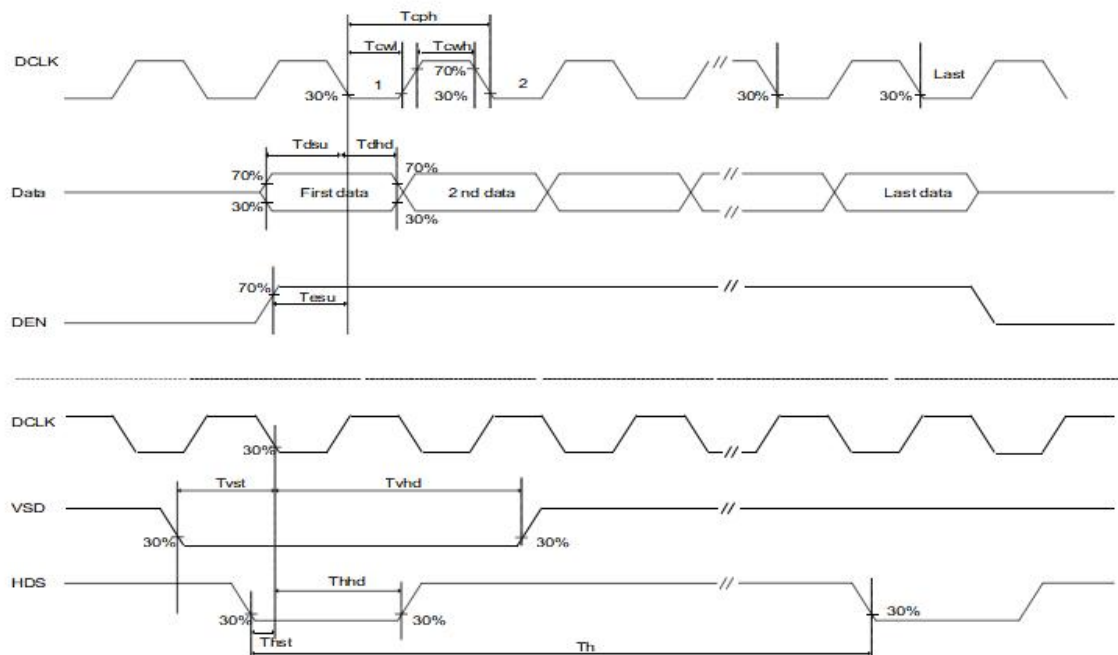
LVDS DC Characteristic

5.3 AC Electrical Characteristics

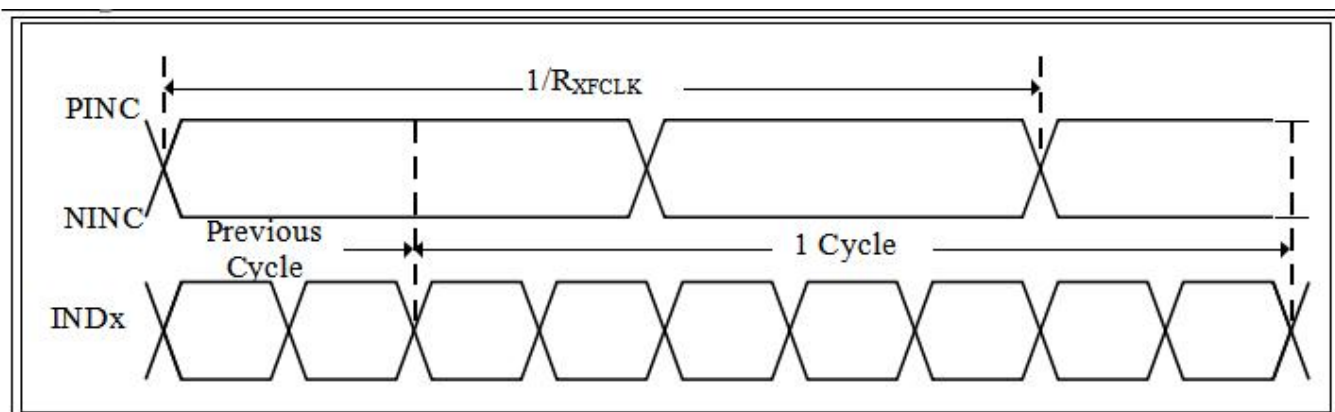
LVDS mode

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Clock Frequency	RxFCLK		20	-	71	MHz
Input data skew margin	TRSKM	$ V_{ID} =400\text{mV}$ $R_{xVCM}=1.2\text{V}$ $R_{xFCLK}=71\text{MHz}$	500			ps
Clock High Time	TLVCH			$4/(7 \cdot R_{xFCLK})$		ns
						ns
Clock Low Time	TLVCL			$3/(7 \cdot R_{xFCLK})$		ns
PLL wake-up-time	TenPLL				150	us

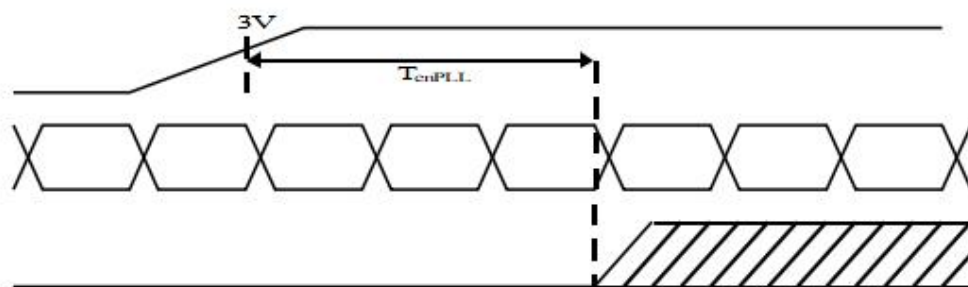




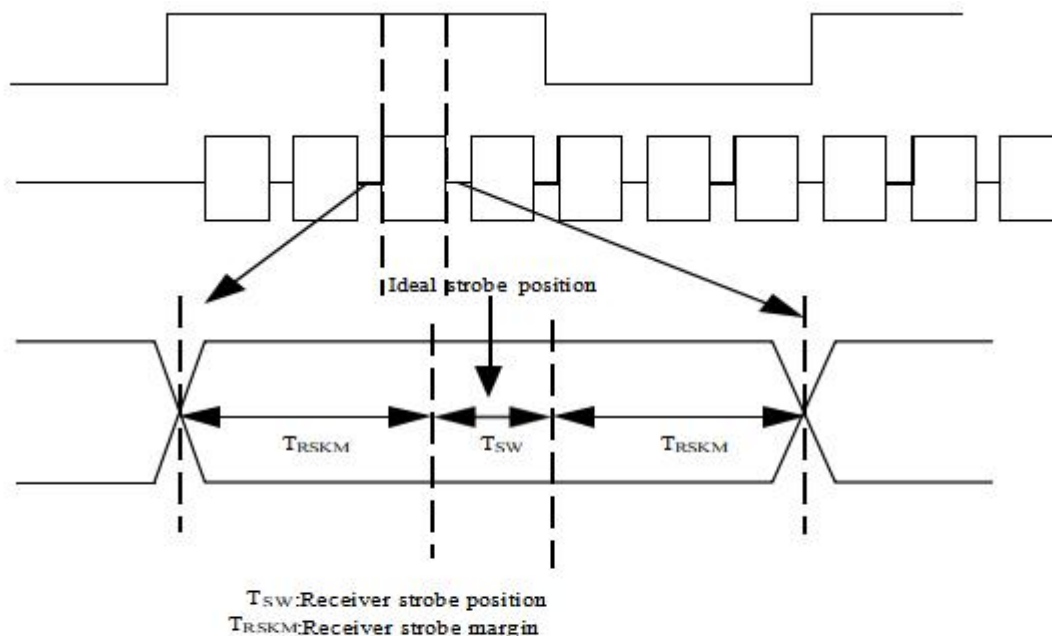
Parallel Input Clock and Data timing



LVDS timing(1)



LVDS timing(2)



LVDS timing(3)

Output Timing Table

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
DCLK frequency	Fclk	-	65	71	MHz	VDD =2.3~3.6V
DCLK cycle time	Tclk	14.1	15.4		ns	
DCLK pulse duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso	-	64	-	DCLK	
Time from HSD to LD	Thld	-	64	-	DCLK	
Time from HSD to STV	Thstv	-	2	-	DCLK	
Time from HSD to CKV	Thckv	-	20	-	DCLK	
Time from HSD to OEV	Thoev	-	4	-	DCLK	
LD pulse width	Twld	-	10	-	DCLK	
CKV pulse width	Twckv	-	66	-	DCLK	
OEV pulse width	Twoev	-	74	-	DCLK	

5.4 Parallel RGB Timing Characteristic

DE mode

DE mode

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

HV mode(1)

HV mode

Horizontal input timing

Parameter		Symbol	Value			Unit
Horizontal display area		thd	1024			DCLK
DCLK frequency@ Frame rate=60hz		fdk	Min.	Typ.	Max.	
			44.9	51.2	63	Mhz
1 Horizontal Line		th	1200	1344	1400	DCLK
HSYNC pulse width	Min.	thpw	1			
	Typ.		—			
	Max.		140			
HSYNC back porch		thbp	160	160	160	
HSYNC front porch		thfp	16	160	216	

HV mode(2)

Vertical input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tpw	1	—	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	$\Phi=90^\circ$ (12 o'clock)	80	-	-	deg	Note2
	θ_B	$\Phi=270^\circ$ (6 o'clock)	80	-	-	deg	Note2
	θ_L	$\Phi=180^\circ$ (9 o'clock)	80	-	-	deg	Note2
	θ_R	$\Phi=0^\circ$ (3 o'clock)	80	-	-	deg	Note2
Response Time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	12	17	msec	Note4
	T_{OFF}		-	12	17	msec	Note4
Contrast Ratio	CR		800	1000	-	-	Note1 Note3
Color Chromaticity	W_X		TBD	TBD	TBD	-	Note1 Note5
	W_Y		TBD	TBD	TBD	-	Note1 Note5
Luminance	L		650	750	-	cd/m ²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		45	50	-	%	-

Note 1:Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

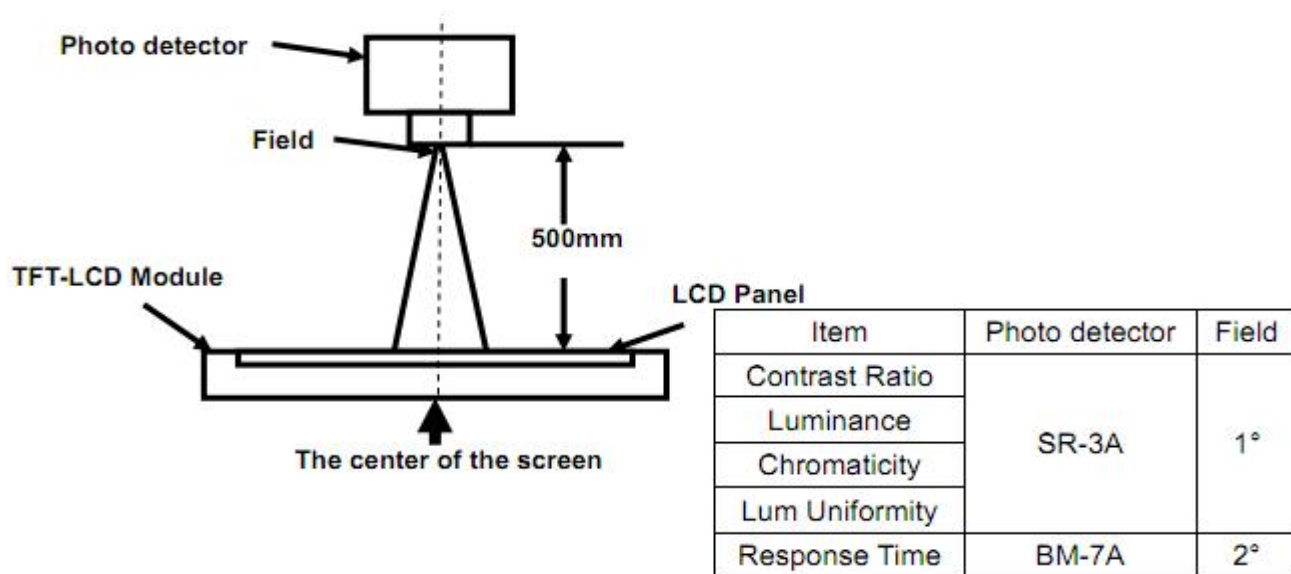


Fig 1

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

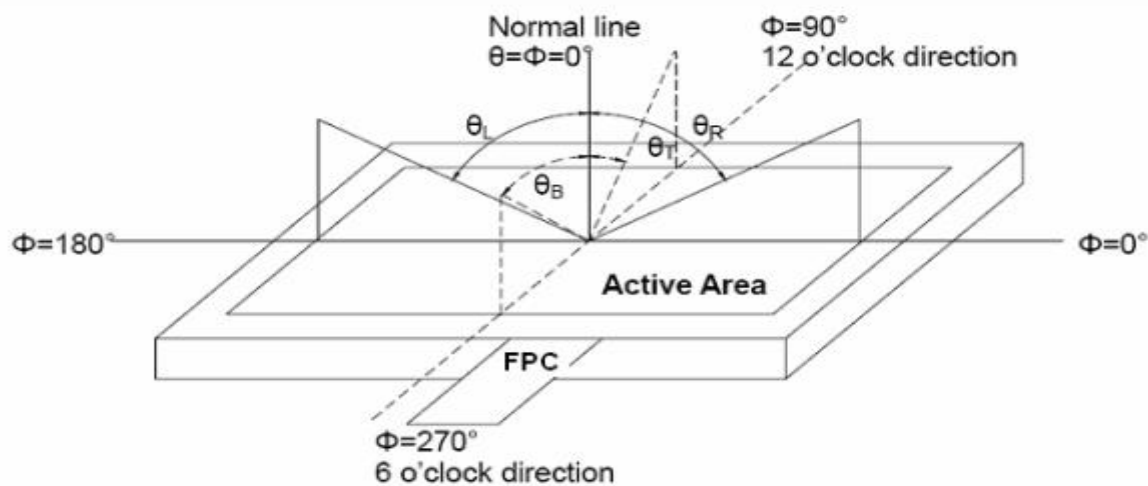


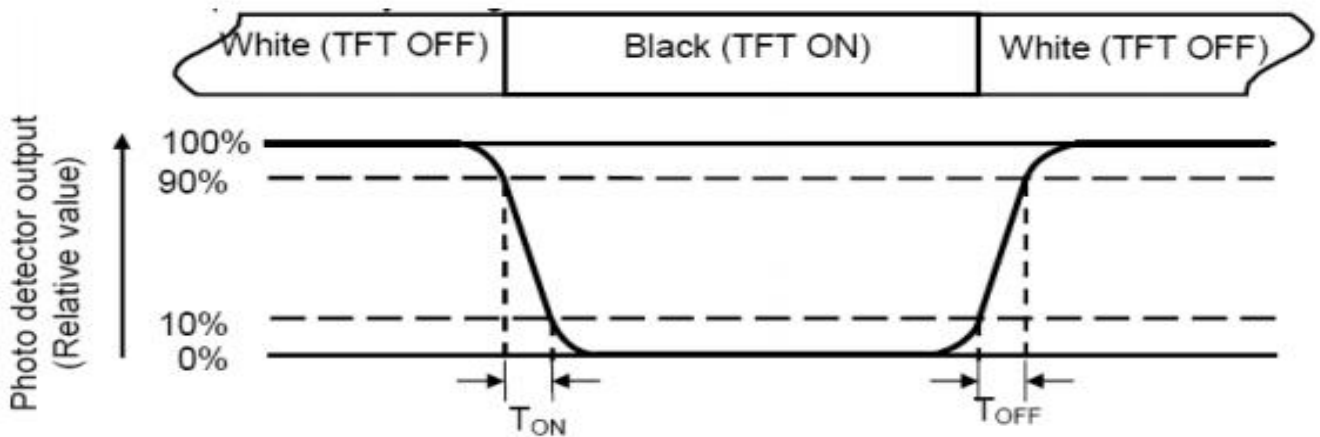
Fig 2 Definition of viewing angle

Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.3-a/b

Note 7: Surface luminance is the luminance with all pixels displaying white.

L_v = Average Surface Luminance with all white pixels ($P_1, P_2, P_3, \dots, P_n$)

For more information see FIG.3-a/b

Note 8: Size : $S \leq 5"$ (see Figure a) H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or test spot position : see Figure a.
measurement instrument : TOPCON's luminance meter SR-3A or BM-7 or compatible (see Figure 1).

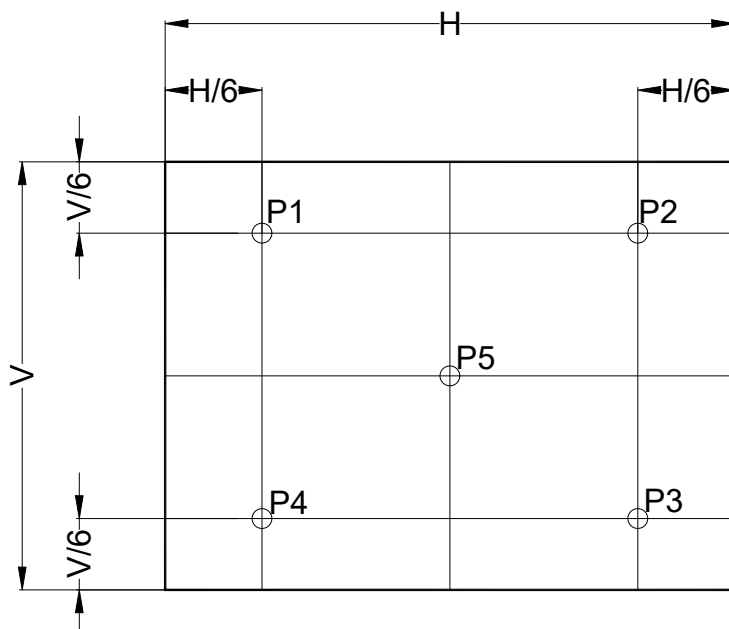


Fig. 3-a Definition of points

$5" < S \leq 12.3"$ (see Figure b) . H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens. test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter SR-3A or BM-7 or compatible (see Figure 1).

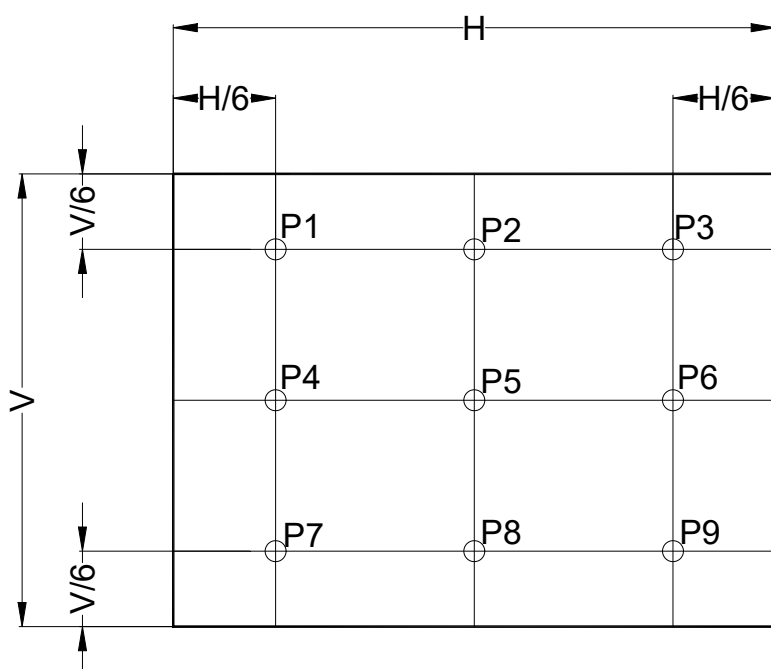


Fig. 3-b Definition of points

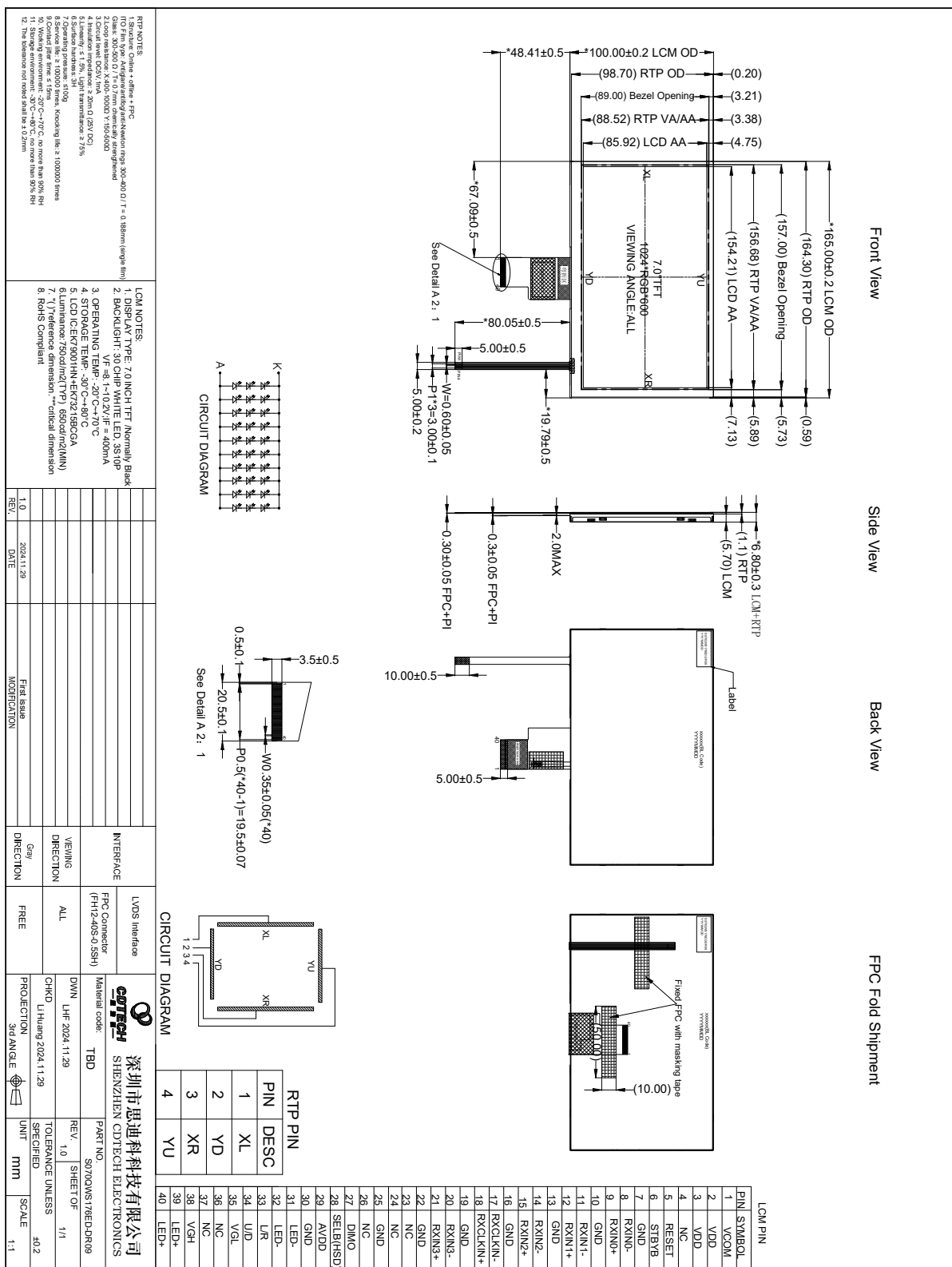
7. Reliability Test Items

Test Item	Test Conditions
High Temperature Storage	Ta= +80℃ 96hrs
Low Temperature Storage	Ta= -30℃ 96hrs
High Temperature Operation	Ta= +70℃ 96hrs
Low Temperature Operation	Ta= -20℃ 96hrs
High Temperature and Humidity Storage	Ta= +60℃, 90% RH 96hrs
Thermal Shock (Non-operation)	-30℃/30 min ~ +80℃/30 min for 20 cycles Start with cold temperature end with high temperature
Electro Static Discharge	Contact = ± 4 kV, class B Air = ± 8 kV, class B R=330Ω,C=150pF
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces

Notes: The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

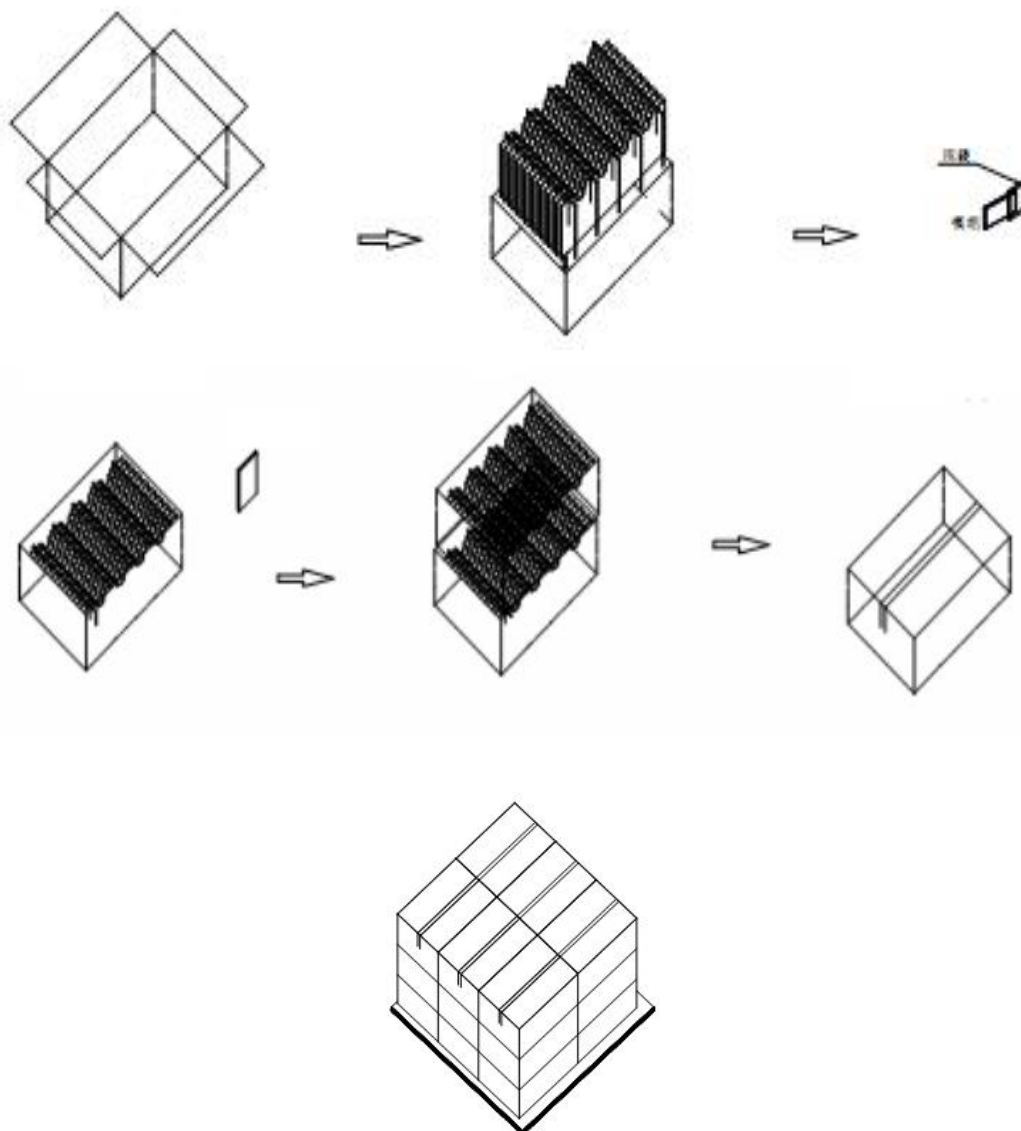
- 1). Air bubble in the LCD
- 2). Seal leak or Glass crack
- 3). Non display or abnormal display
- 4). Brightness reduction >50%

Web: www.cdtech-lcd.com



9. Packing

Packing Method



10. Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2. The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time, the recommend condition is :

Temperature : 0℃ ~40℃ Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

10.4 Packaging instructions

when the customers using trays, they have to stack the adjacent trays in a 180° staggered to prevent pressure that could cause product damage.