

Product Specification

Model Name	S043HWV94ES
Description	Standard LCD Module 4.3" WVGA 800(RGB)x480 Dots
Date	2018/08/17
Version	1.0

Approved by/Date	Check by/Date	Prepared by/Date
ZHP 2018/08/17	HZX 2018/08/17	Yigui.Han 2018/08/17

Customer Approval	
Date	

Table of Contents

1. Record of Revision	3
2. General Specifications	4
3. Input/OutputTerminals	5
4. Absolute Maximum Rating	6
5. Electrical Characteristics.....	6
6. Interface Timing.....	9
7. Optical Characteristics	15
8. Environmental / Reliability Tests	19
9. Mechanical Drawing	20
10. Packing	21
11. Precautions for Use of LCD modules	22

1. Record of Revision

Rev	Issued Date	Description	Editor
1.0	2018/08/17	First Release.	Yigui.Han

2. General Specifications

	Feature	Spec
Characteristics	Size	4.3 inch
	Resolution	800(horizontal)*480(Vertical)
	Interface	RGB-24bit
	Connect type	Connector
	Display Colors	16.2M
	Pixel pitch (mm)	0.1188*0.1122
	Pixel Configuration	R.G.B.-Stripe
	Display Mode	Normally black
	LCD Driver IC	HX8264-D06+HX8664-B
	Viewing Direction	Full view
Mechanical	LCM (W x H x D) (mm)	105.50*67.20*2.85
	Active Area(mm)	95.04 x 53.86
	Weight (g)	TBD
	LED Numbers	18 LEDS

Note 1: Requirements on Environmental Protection: RoHs

Note 2: LCM weight tolerance: +/- 5%

3. Input/Output Terminals

LCD PIN-MAP

No.	Symbol	Description
1	VLED-	Backlight LED Cathode
2	VLED+	Backlight LED Anode.
3	GND	Ground
4	VDD	Power supply
5~12	R0~R7	Data bus
13~20	G0~G7	Data bus
21~28	B0~B7	Data bus
29	GND	Ground
30	DCLK	Dot clock signal input. Latching input data at its rising edge.
31	DISP	Standby mode. Normally pulled high. DISP="1": Normally operation (Default) DISP="0": Timing controller, source driver will turn off ,all output are High-Z.
32	HSYNC	Horizontal sync input. Negative polarity.
33	VSYNC	Vertical sync input. Negative polarity..
34	DE	Data enable input. Active high to enable the input data bus under "DE Mode."
35	NC	No connect
36	GND	System Ground
37	XR(NC)	No connect
38	YD(NC)	No connect
39	XL(NC)	No connect
40	YU(NC)	No connect

4. Absolute Maximum Rating

Item	Symbol	MIN	Typ	MAX	Unit	Remark
Supply Voltage	VDD	-0.5	-	3.96	V	-
Operating Temperature	TOPR	-30	-	85	°C	-
Storage Temperature	TSTG	-30	-	85	°C	

5. Electrical Characteristics

5.1 Driving TFT LCD Panel

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Supply Voltage		VDD	3.0	3.3	3.6	V	
Operation current		I _{DD}	210	230	250	mA	White screen
Input Signal Voltage	Low Leve	V _{IL}	GND	-	0.3x VDD	V	
	High Level	V _{IH}	0.7x VDD	-	VDD	V	
Output Signal Voltage	Low Leve	V _{IL}	GND	-	VDD+0.4	V	
	High Level	V _{IH}	VDD-0.4	-	VDD	V	
(Panel+LSI) Power Consumption		Black Mode (60Hz)	-	74		nW	
		Standby	-	50	-	uW	

5.2 LED Driving Conditions

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I _F	-	40	-	mA	
Forward Voltage	V _F	24.3	27	30.6	V	
Backlight Power consumption	W _{BL}	-	1.08	-	W	
LED Lifetime		-	25000	-	Hrs	

Note 1: Each LED: I_F =20 mA, V_F =3.2+/-0.2V.

Note 2: Optical performance should be evaluated at T_a=25°C only.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life Time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

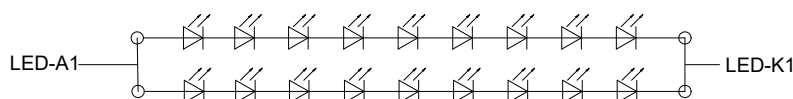
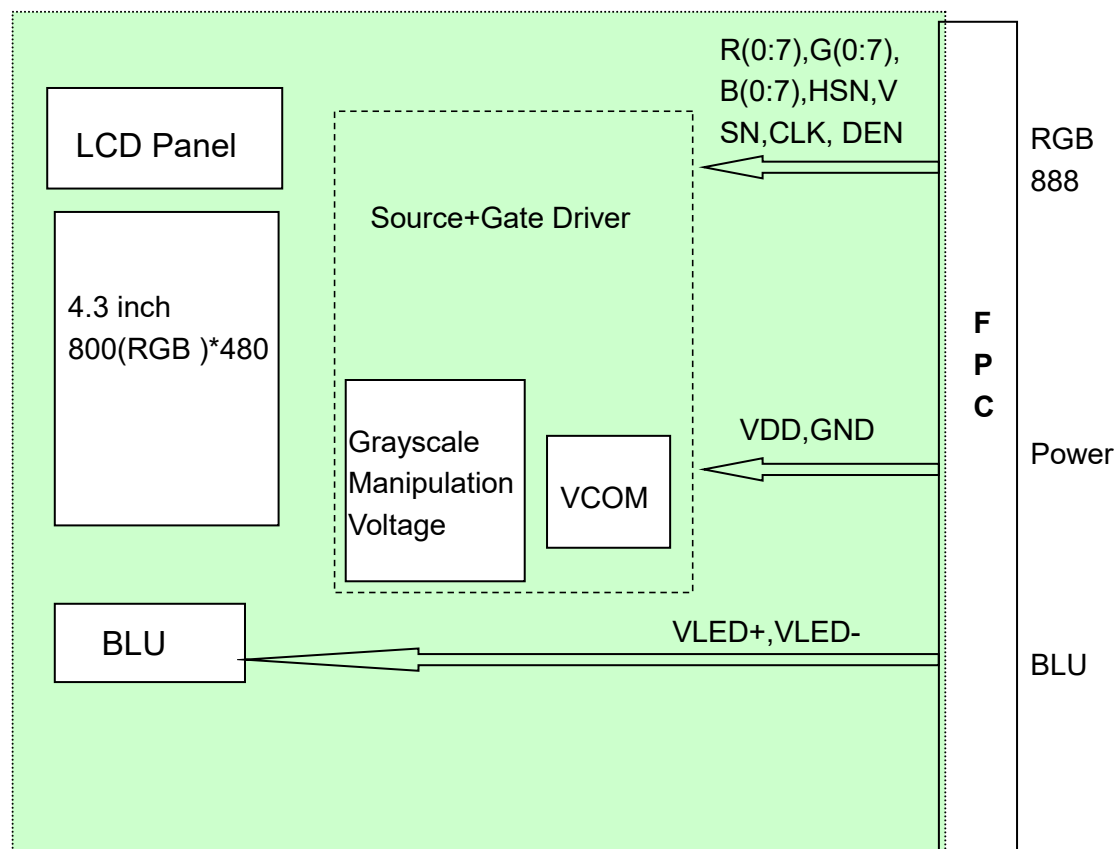


Figure: LED connection of backlight(Constant Current)

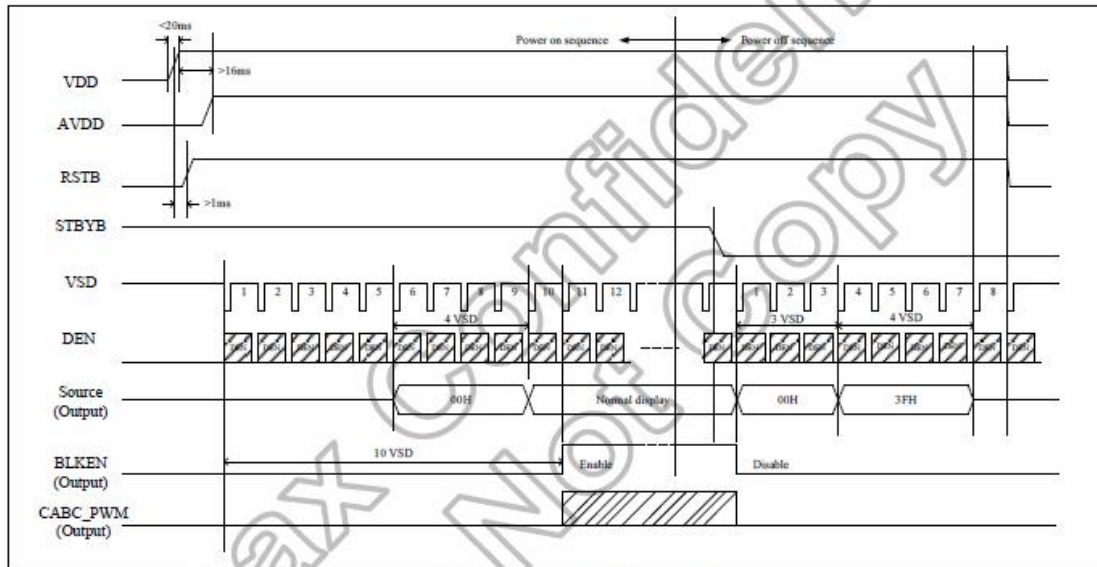
5.3 Block Diagram



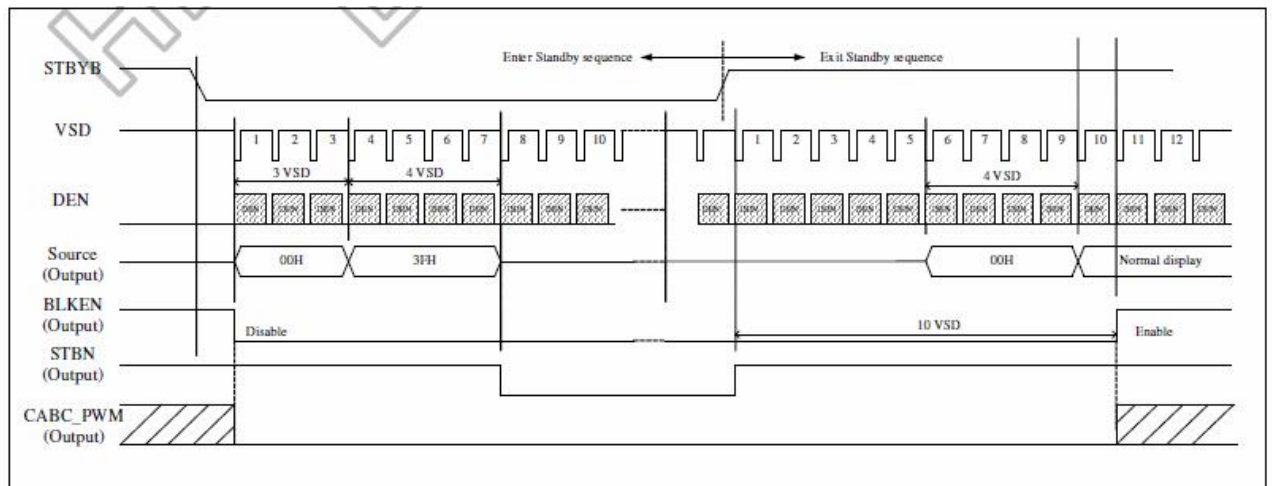
6. Interface Timing

6.1 Power Sequence

Power on/off control:



Enter and exit standby mode sequence:



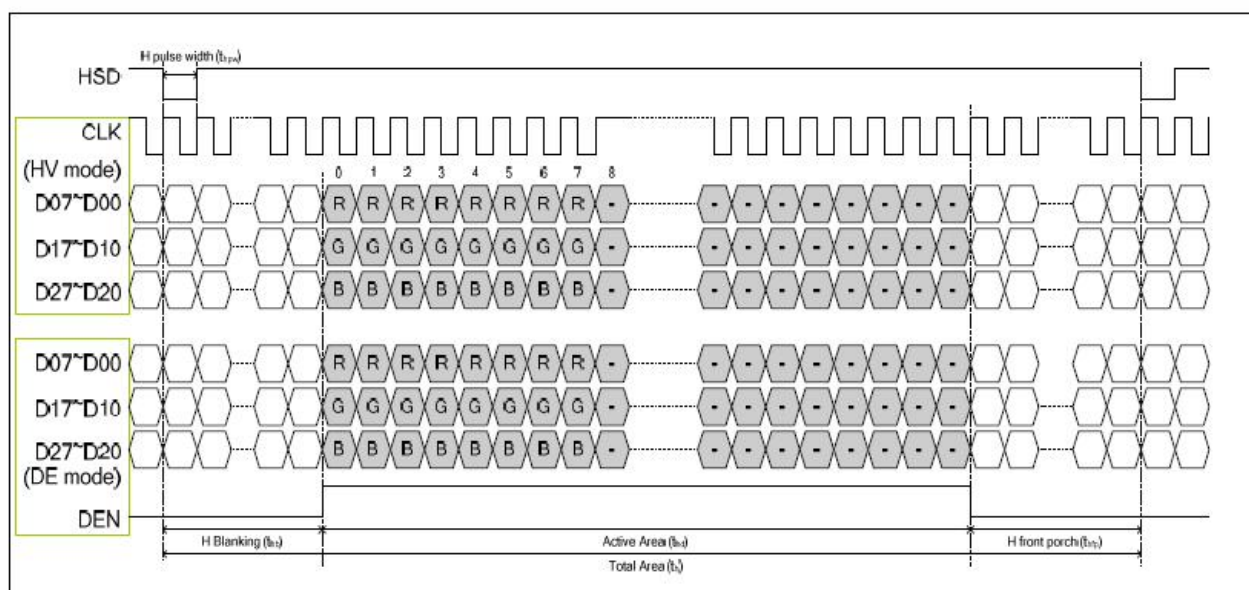
6.2 AC Characteristics

6.2.1 AC electrical characteristics

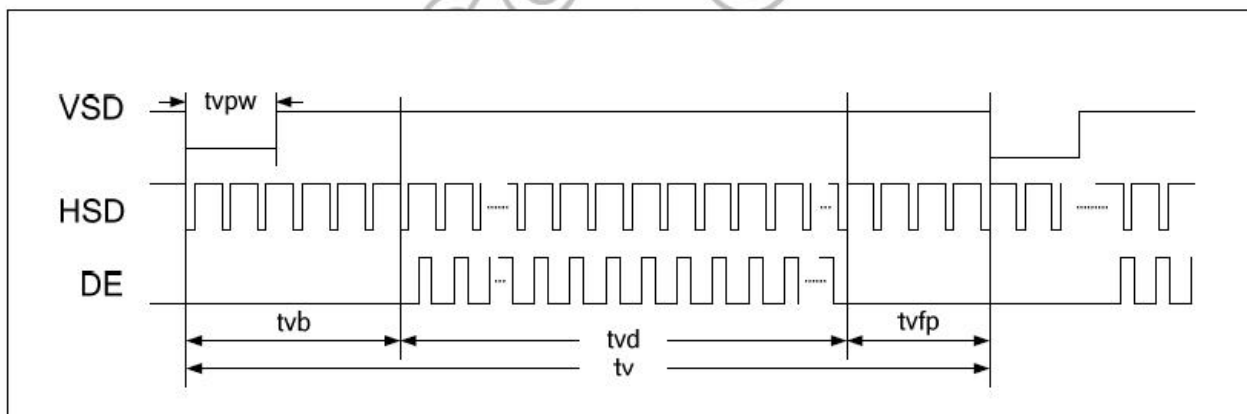
Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{owh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	us

6.2.2 Data input format

● Horizontal timing



- Vertical timing



- Horizontal timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	862	1056	1200	DCLK
HS pulse width	thpw	1	-	40	DCLK
HS Back Porch (Blanking)	thb		46		DCLK
HS Front Porch	thfp	16	210	354	DCLK
DE mode Blanking	th-thd	85	256	400	DCLK

- Vertical timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd		480		T _H
VS period time	tv	513	525	650	T _H
VS pulse width	tvpw	3	-	20	T _H
VS Back Porch (Blanking)	tvb		23		T _H
VS Front Porch	tvfp	7	22	147	T _H
DE mode Blanking	tv-tvd	30	45	170	T _H

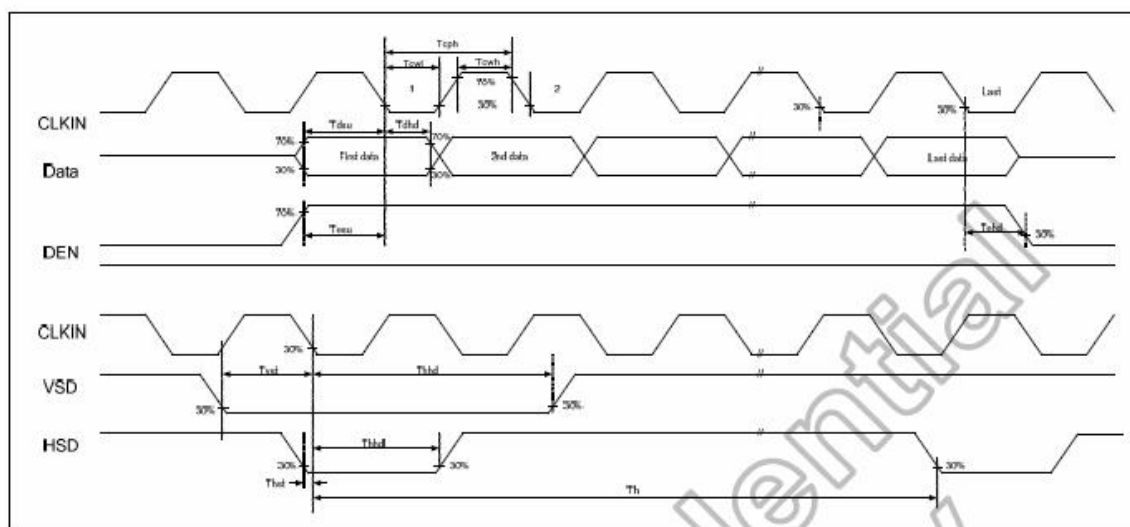
6.3 Waveform

6.3.1 Timing waveform table

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	-
Time from HSD to STV	Thstv		2		CLKIN	-
Time from HSD to CKV	Thckv		20		CLKIN	-
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10		CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74		CLKIN	-

6.3.2 Timing diagram

6.3.2.1 Input clock and data timing waveform:



6.3.2.2 Source output timing waveform (Cascade):

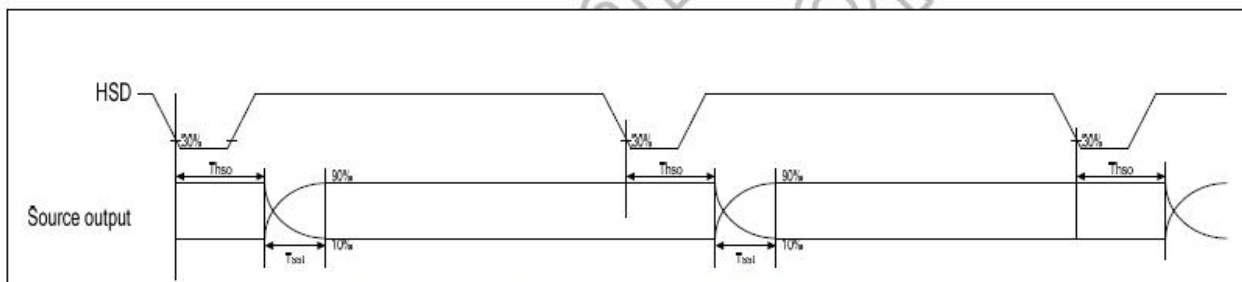


Figure 12. 2: Source Output Timing Diagram

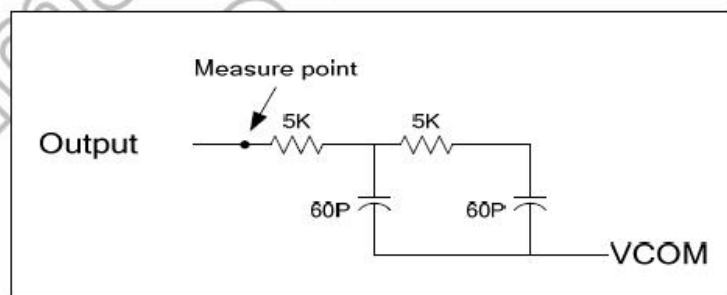
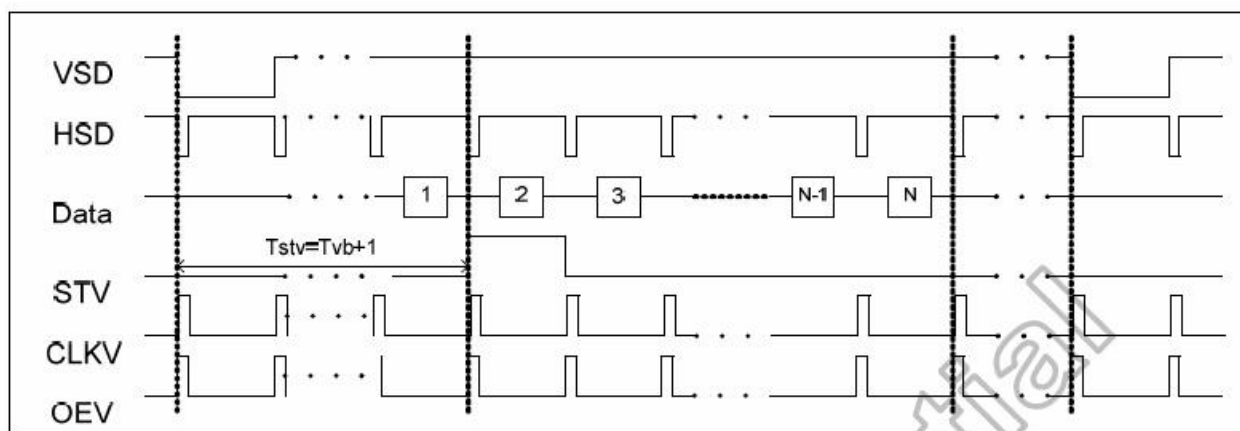
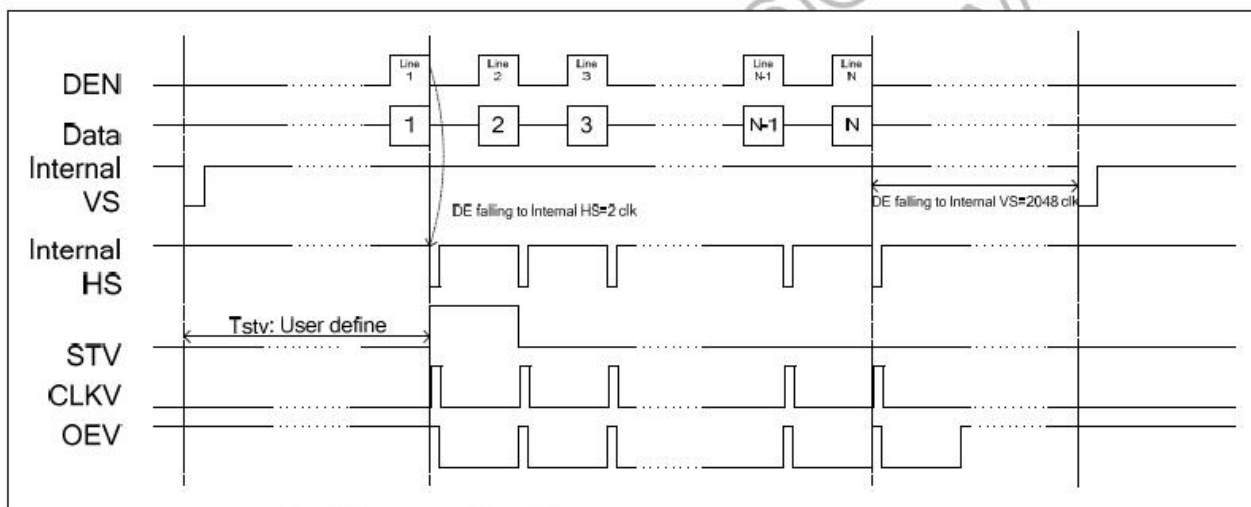


Figure 12. 3: Output Load Condition

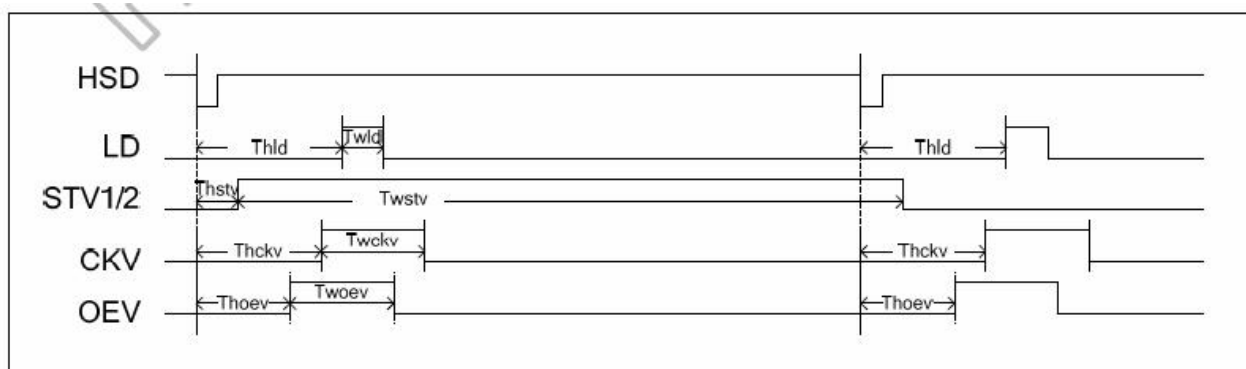
6.3.2.3 Vertical timing diagram HV (Cascade)



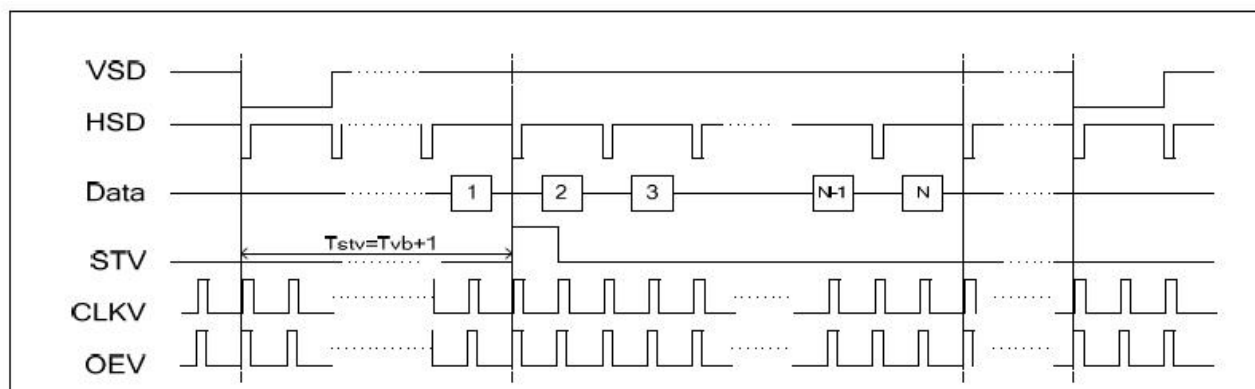
6.3.2.4 Vertical timing diagram DE (Cascade)



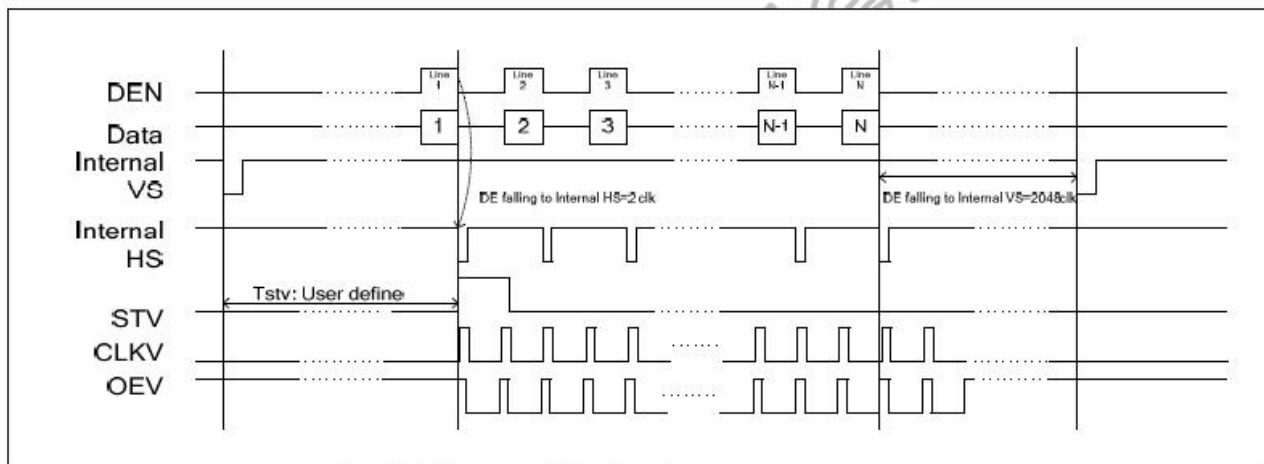
6.3.2.5 Gate output timing diagram (Cascade)



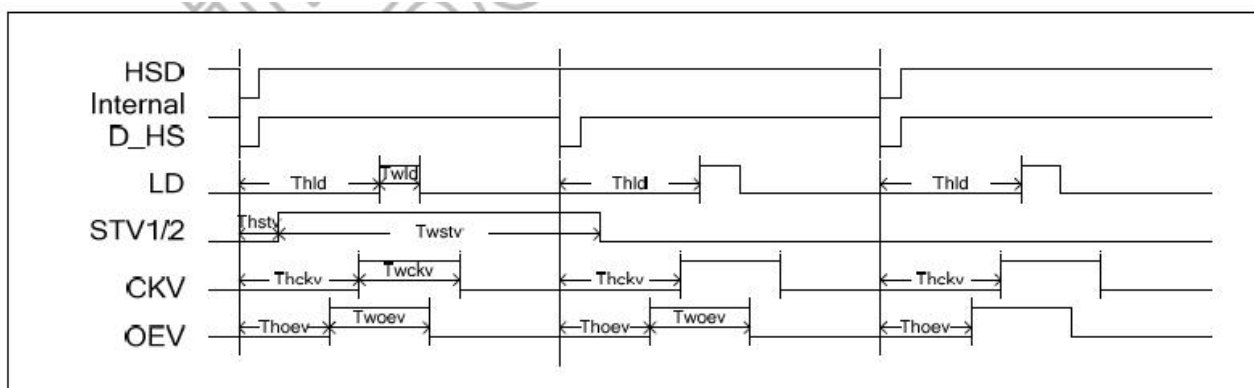
6.3.2.6 Vertical timing diagram HV (Dual gate)



6.3.2.7 Vertical timing diagram DE (Dual gate)



6.3.2.8 Gate output timing diagram (Dual gate)



7. Optical Characteristics

Items		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	-	-	30	40	ms	FIG.1	Note4
Contrast Ratio		CR		-	800	-	-	FIG.2	Note1
Surface luminance		LV	$\theta = 0^\circ$	-	1000	-	cd/m ²	FIG.2	Note2
Luminance uniformity		Yu	$\theta = 0^\circ$	80	85	-	%	FIG.2	Note3
NTSC		-	$\theta = 0^\circ$	-	50	-	%	FIG.2	Note5
Viewing angle		θ_T	Center CR $\geq$ 10	-	80	-	deg	FIG.3	Note6
		θ_B		-	80	-	deg	FIG.3	
		θ_L		-	80	-	deg	FIG.3	
		θ_R		-	80	-	deg	FIG.3	
Chromaticity	Red	R _X	$\theta = 0^\circ$ $\phi = 0^\circ$ Ta=25°	0.57	0.620	0.67	-	FIG.2 CIE1931	Note5
		R _Y		0.278	0.328	0.378	-		
	Green	G _X		0.284	0.334	0.384	-		
		G _Y		0.494	0.544	0.594	-		
	Blue	B _X		0.089	0.139	0.189	-		
		B _Y		0.083	0.133	0.183	-		
	White	W _X		0.2651	0.3151	0.3651	-		
		W _Y		0.2764	0.3264	0.3764	-		

Note1. Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula. For more information see FIG.2.

$$\text{Contrast ratio} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is based on TOPCON's BM-5 or BM-7 photo detector or compatible.

Note2. Definition of surface luminance.

Surface luminance is the luminance with all pixels displaying white. For more information see FIG.2.

L_v = Average Surface Luminance with all white pixels ($P_1, P_2, P_3, \dots, P_n$)

Note3. Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

$$YU = \frac{\text{Minimum surface luminance with all white pixels } (P_1, P_2, P_3, \dots, P_n)}{\text{Maximum surface luminance with all white pixels } (P_1, P_2, P_3, \dots, P_n)}$$

Note4. Definition of response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_r) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_f) is the time between photo detector output intensity changed from 10% to 90%.

For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity, The x,y value is determined by screen active area center position P5. For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. Angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope or DMS series Instruments or compatible.

FIG.1.The definition of response Time

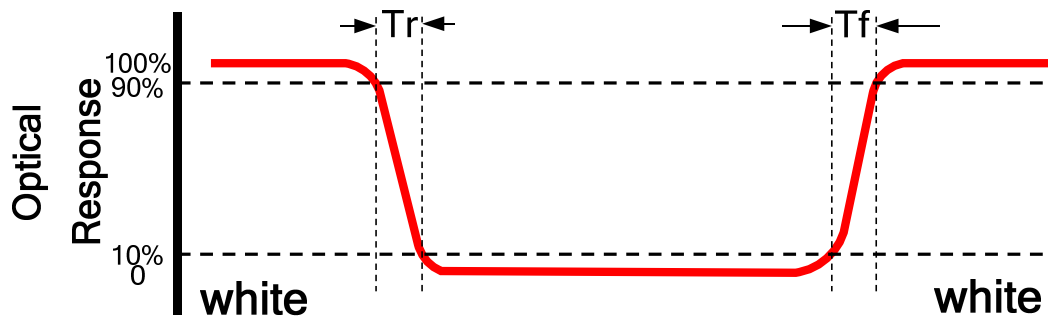


FIG.2. Measuring method for contrast ratio, surface luminance,

luminance uniformity, CIE (x,y) chromaticity

Size : $S \leq 5"$ (see Figure a) A : 5 mm B : 5 mm

H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure a.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

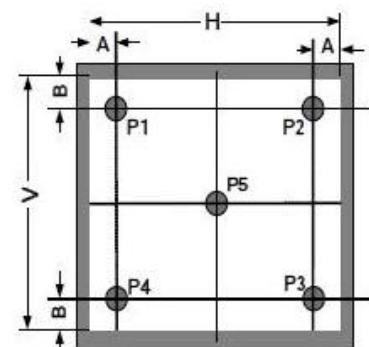


Figure a

Size : $5" < S \leq 12.3"$ (see Figure b) H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

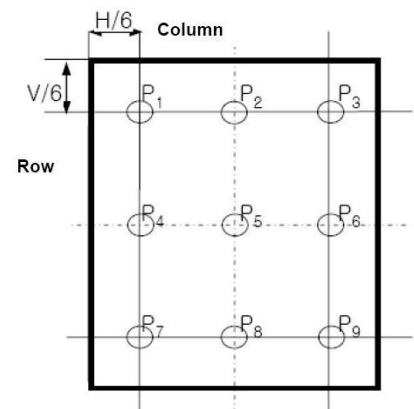


Figure b

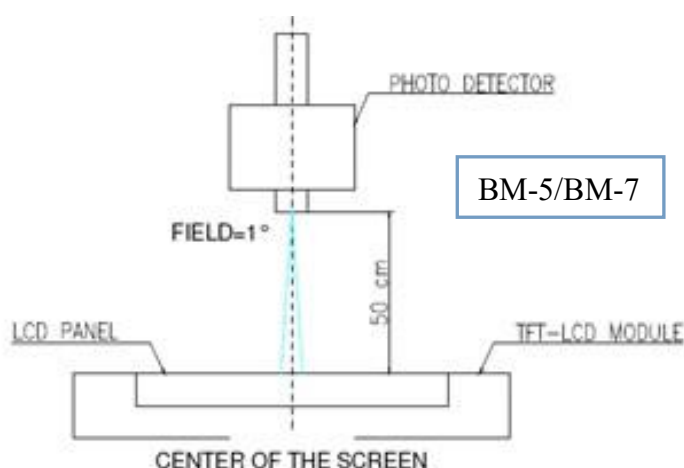
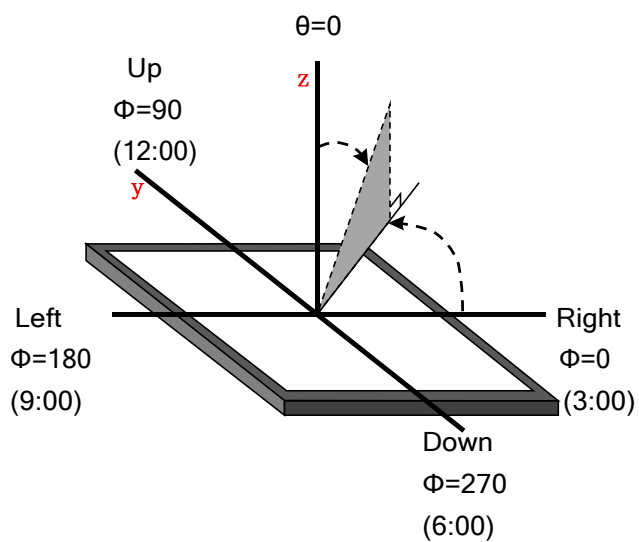


Figure c

FIG.3.The definition of viewing angle



8. Environmental / Reliability Tests

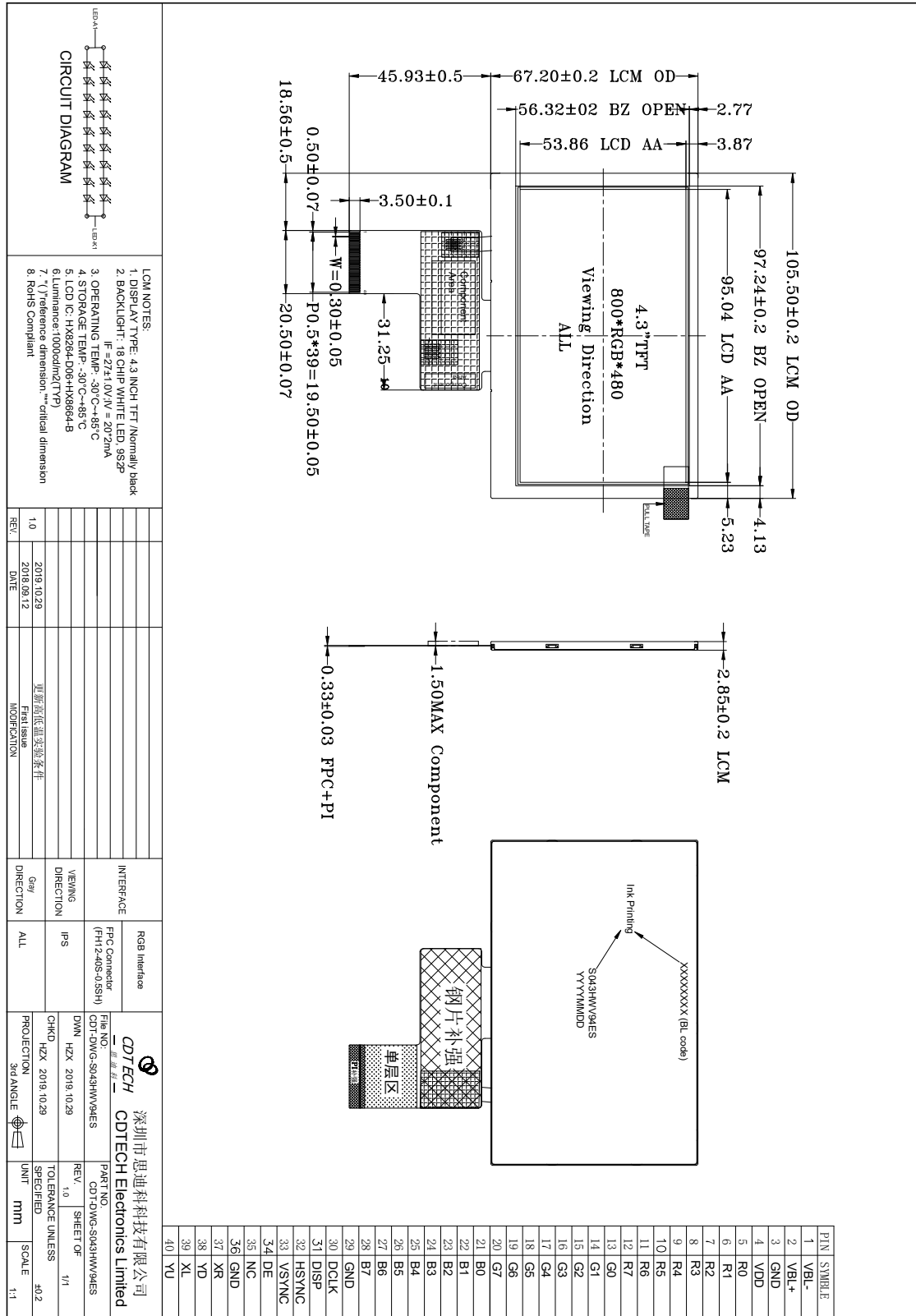
No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +85°C, 96hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	Ta= -30°C, 96hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +85°C, 96hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30°C, 96hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60°C, 90% RH max,96 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-20°C 30 min ~ +60°C 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Discharge (Operation) Static	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15°C ~ 35°C, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

Note: 1. Ts is the temperature of panel's surface.

2. Ta is the ambient temperature of sample.

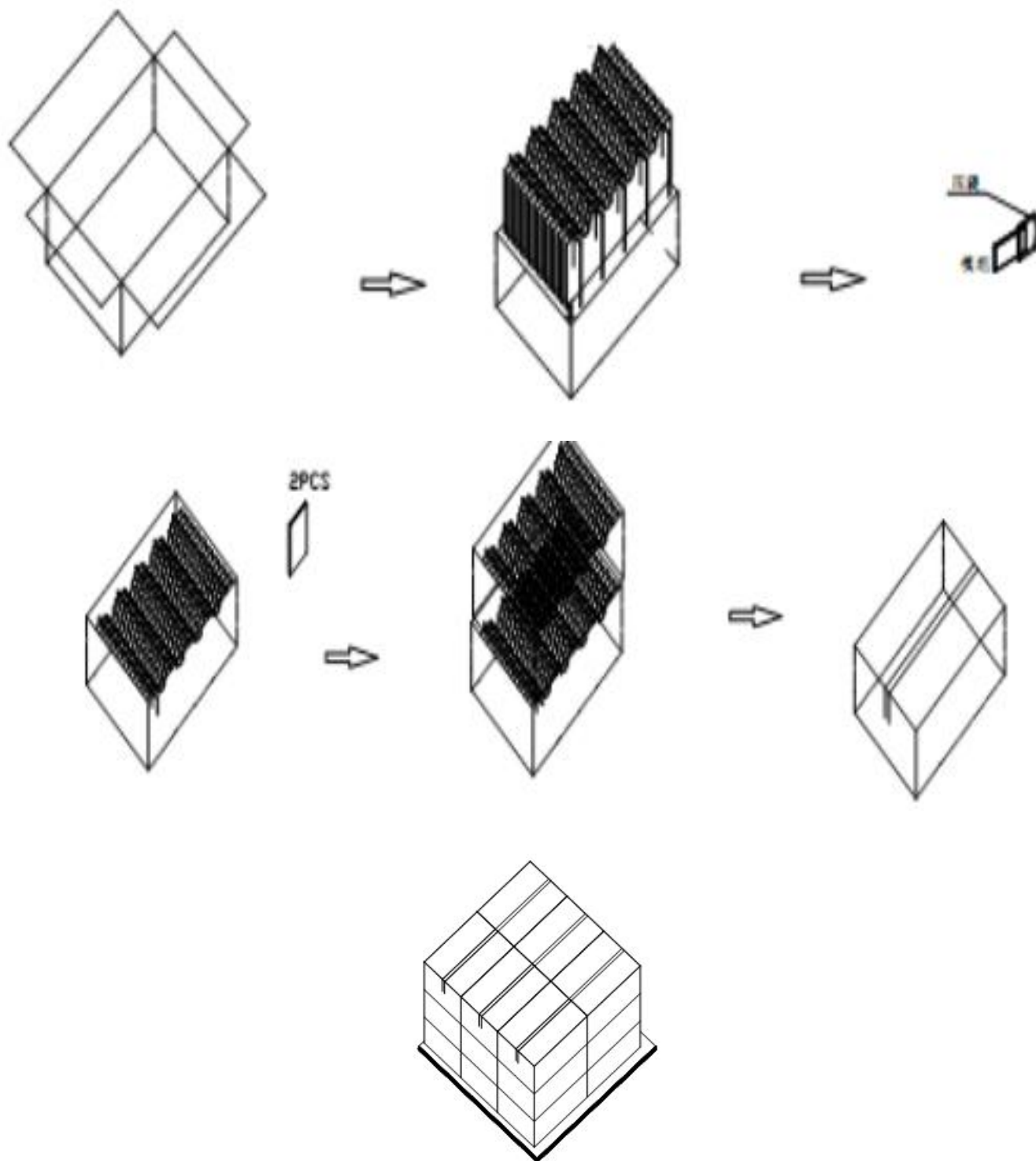
3. The size of sample is 5pcs.

9. Mechanical Drawing



10. Packing

Packing Method



11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.