

Product Specification

Model Name	S050HWV18HS-DR09
Description	Standard LCD Module 5.0" WVGA 800(RGB)x480 Dots
Date	2019/4/12
Version	1.0

Approved by/Date	Check by/Date	Prepared by/Date
ZHP 2019/4/12	HZX 2019/4/12	Yigui.Han 2019/4/12

Customer Approval	
Date	

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1. Record of Revision

Rev	Issued Date	Description	Editor
1.0	2019/4/12	First Release.	Yigui.Han

2. General Specifications

	Feature	Spec
Characteristics	Size	5 inch
	Resolution	800(horizontal)*480(Vertical)
	Interface	RGB-24bit
	Connect type	Connector
	Display Colors	16M
	Technology type	a-Si
	Pixel pitch (mm)	0.135*0.135
	Pixel Configuration	R.G.B.-Stripe
	Display Mode	Normally Black
	LCD Driver IC	ST7262-G4
	Viewing Direction	Full view
Mechanical	LCM (W x H x D) (mm)	120.7*75.80*4.2
	Active Area(mm)	108 x 64.80
	Weight (g)	TBD
	LED Numbers	12 LEDs

Note 1: Requirements on Environmental Protection: RoHs

Note 2: LCM weight tolerance: +/- 5%

3. Input/Output Terminals

No.	Symbol	Description
1	VLED-	Backlight LED Cathode
2	VLED+	Backlight LED Anode.
3	GND	Ground
4	VDD	Power supply
5~12	R0~R7	Data bus
13~20	G0~G7	Data bus
21~28	B0~B7	Data bus
29	GND	Ground
30	DCLK	Dot clock signal input. Latching input data at its rising edge.
31	DISP	Standby mode. Normally pulled high. DISP="1": Normally operation (Default) DISP="0": Timing controller, source driver will turn off ,all output are High-Z.
32	HSYNC	Horizontal sync input. Negative polarity.
33	VSYNC	Vertical sync input. Negative polarity..
34	DE	Data enable input. Active high to enable the input data bus under "DE Mode."
35	NC	No connect
36	GND	System Ground
37	XR	The right side signal of TP
38	YD	The down side signal of TP
39	XL	The left side signal of TP
40	YU	The up side signal of TP

4. Absolute Maximum Rating

Item	Symbol	MIN	Typ	MAX	Unit	Remark
Supply Voltage	VDD	-0.5	-	5	V	-
Operating Temperature	TOPR	-30	-	85	°C	-
Storage Temperature	TSTG	-30	-	85	°C	

5. Electrical Characteristics

5.1 ELECTRICAL CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Supply Voltage	VDD	3.0	3.3	3.6	V	
IO Supply Voltage	VDDI	3.0	-	3.6	V	
Charge Pump Supply Voltage	PVDD	3.0	3.3	3.6	V	

5.2 DC Characteristics for Digital Circuit

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Logic-High Input Voltage	Vih	0.7VDDI	-	VDDI	V	
Logic-Low Input Voltage	Vil	DGND	-	0.3VDDI	V	
Logic-High Output Voltage	Voh	VDDI-0.4	-	VDDI	V	
Logic-Low Output Voltage	Vol	DGND	-	DGND+0.4	V	

5.3 DC Characteristics for Analog Circuit

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Positive High-Voltage Power	VGH	13	15	16.5	V	No Load@ FR=60Hz
Negative High-Voltage Power	VGL	-7	-10	-11	V	
Output Voltage Deviation	Vod	-	±35	±45	mV	
Standby Current	Isc	-	-	50-	uA	
Operation Current	Ioc	-	20	-	mA	

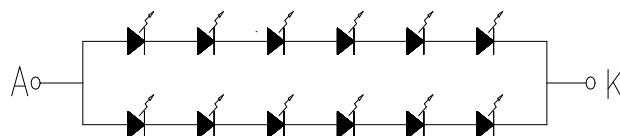
5.4 LED Driving Conditions

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I_F	-	40	-	mA	
Forward Voltage	V_F	18.0	19.2	20.4	V	
Backlight Power consumption	W_{BL}	-	0.768	-	W	
LED Lifetime		-	30000	-	Hrs	

Note 1: Each LED: $I_F = 20 \text{ mA}$, $V_F = 3.2 \pm 0.2 \text{ V}$.

Note 2: Optical performance should be evaluated at $T_a = 25^\circ\text{C}$ only.

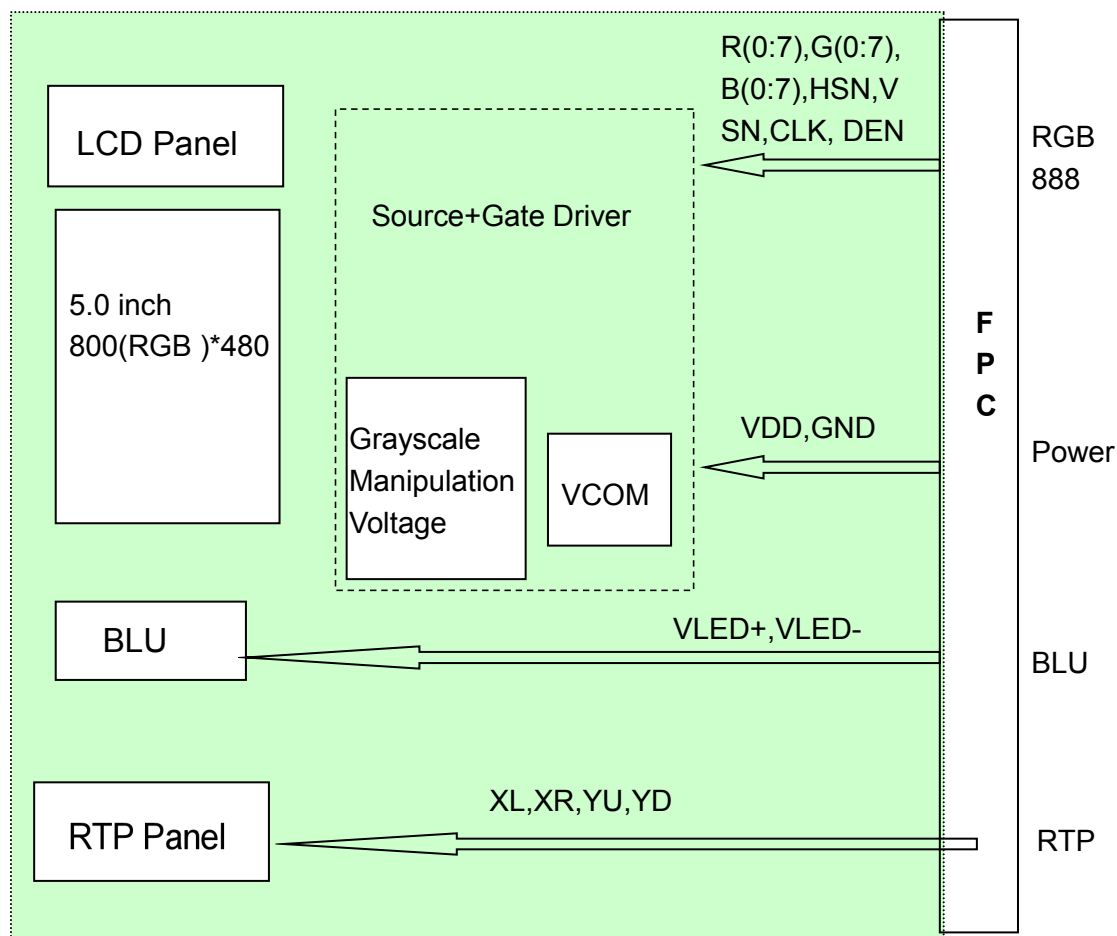
Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life Time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



CIRCUIT DIAGRAM

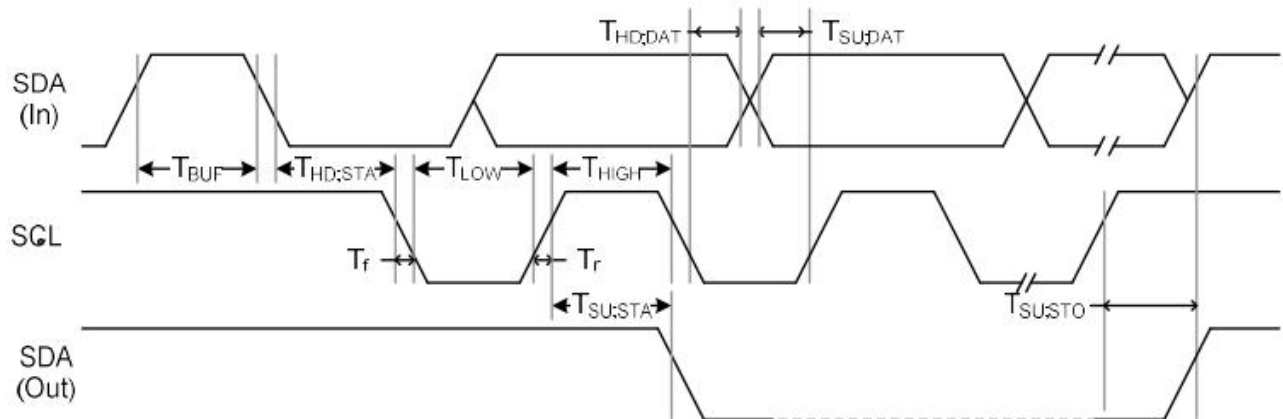
Figure: LED connection of backlight(Constant Current)

5.5 Block Diagram



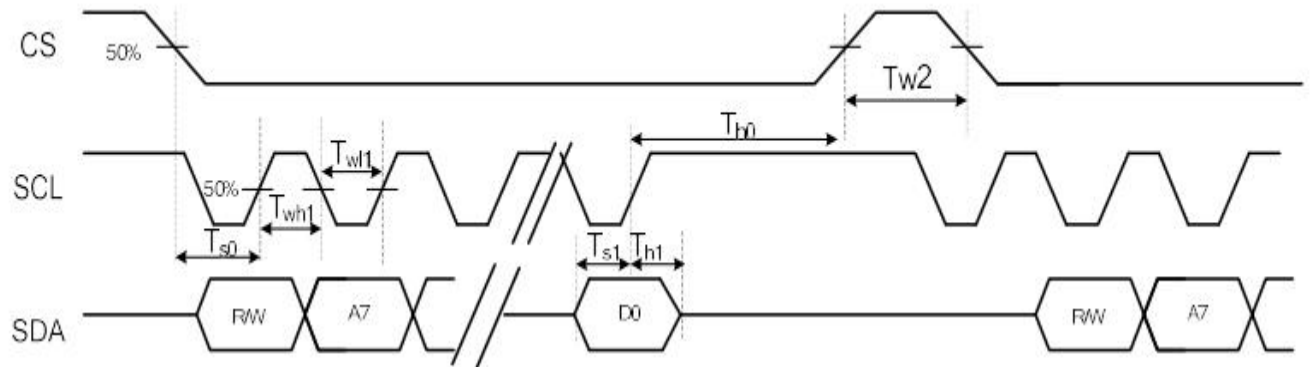
6. Interface Timing

6.1 System Bus Timing for I2C Interface



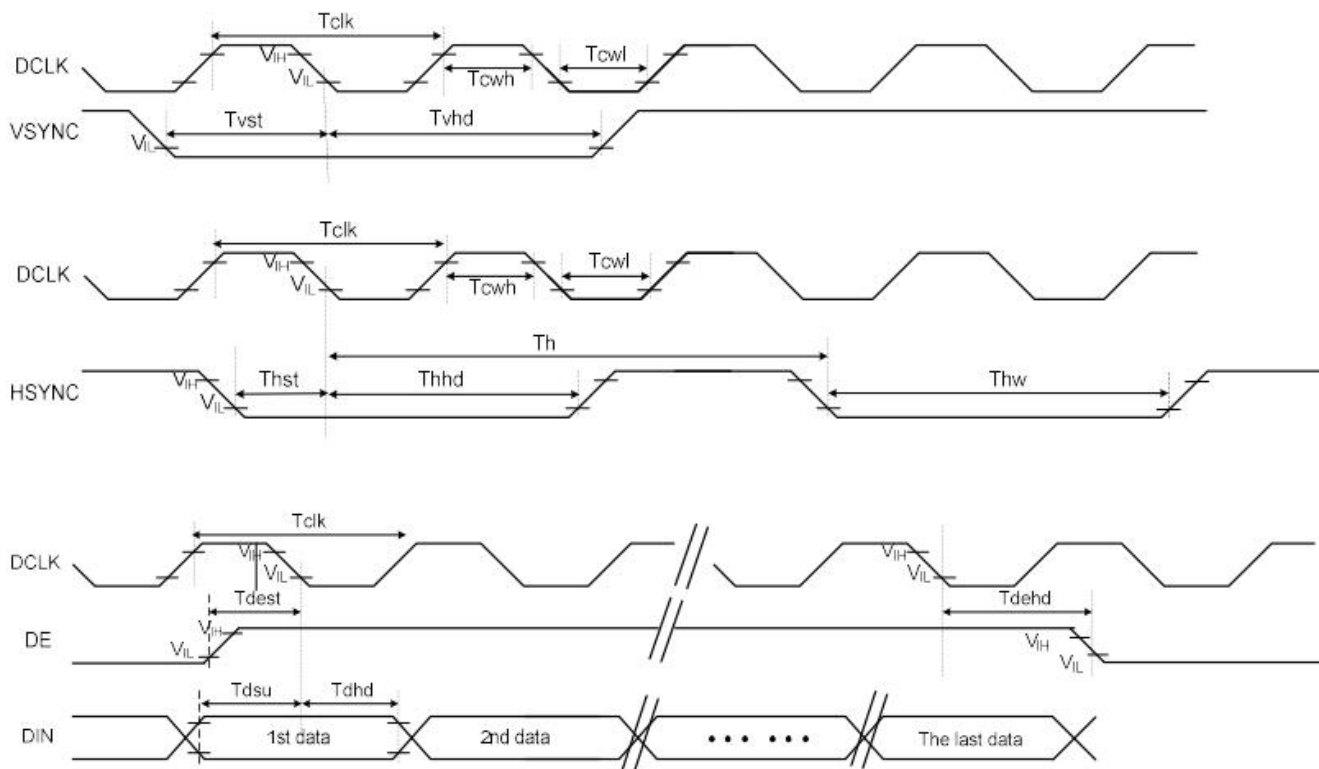
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
SCL Clock Frequency	F _{SCL}	-	-	400	KHz	
SCL Clock Low Period	T _{LOW}	1300	-	-	ns	
SCL Clock High Period	T _{HIGH}	600	-	-	ns	
Signal Rise Time	T _r	20+0.1Cb	-	300	ns	
Signal Fall Time	T _f	20+0.1Cb	-	300	ns	
Start Condition Setup Time	T _{SU;STA}	600	-	-	ns	
Start Condition Hold Time	T _{HD;STA}	600	-	-	ns	
Data Setup Time	T _{SU;DAT}	100	-	-	ns	
Data Hold Time	T _{HD;DAT}	0	-	900	ns	
Setup Time for STOP Condition	T _{SU;STO}	600	-	-	ns	
Bus Free Time Between a STOP and START	T _{BUF}	100	-	-	ns	
Capacitive load represented by each bus line		C _b		400	pF	
Tolerable Spike Width on Bus	T _{SW}	-	-	50	ns	

6.2 System Bus Timing for 3-Wire SPI Interface



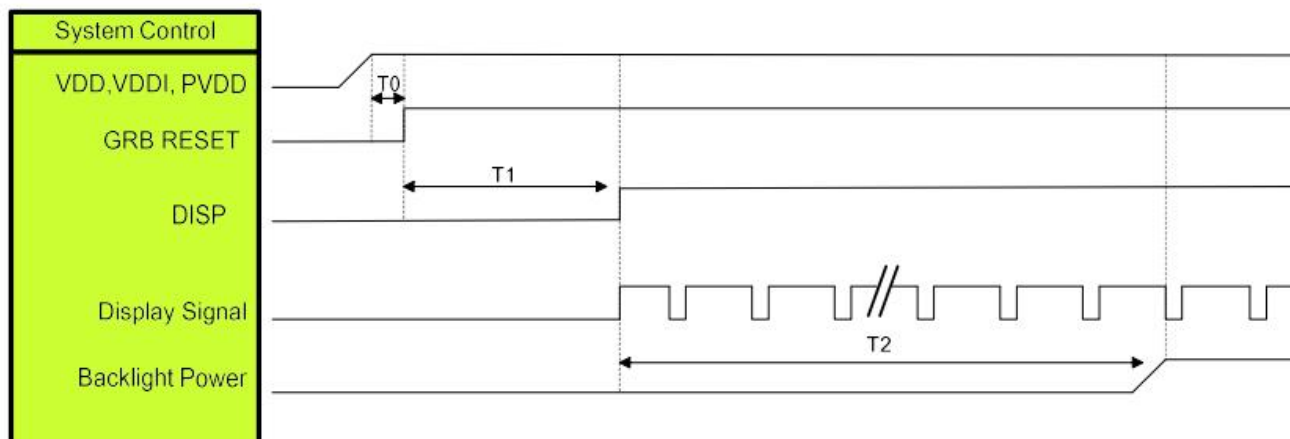
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CS Input Setup Time	T_{s0}	50	-	-	ns	
Serial Data Input Setup Time	T_{s1}	50	-	-	ns	
CS Input Hold Time	T_{h0}	50	-	-	ns	
Serial Data Input Hold Time	T_{h1}	50	-	-	ns	
SCL Write Pulse High Width	T_{wh1}	50	-	-	ns	
SCL Write Pulse Low Width	T_{wl1}	50	-	-	ns	
SCL Read Pulse High Width	T_{rh1}	300	-	-	ns	
SCL Read Pulse Low Width	T_{rl1}	300	-	-	ns	
CS Pulse High Width	T_{w2}	400	-	-	ns	

6.3 System Bus Timing for RGB Interface



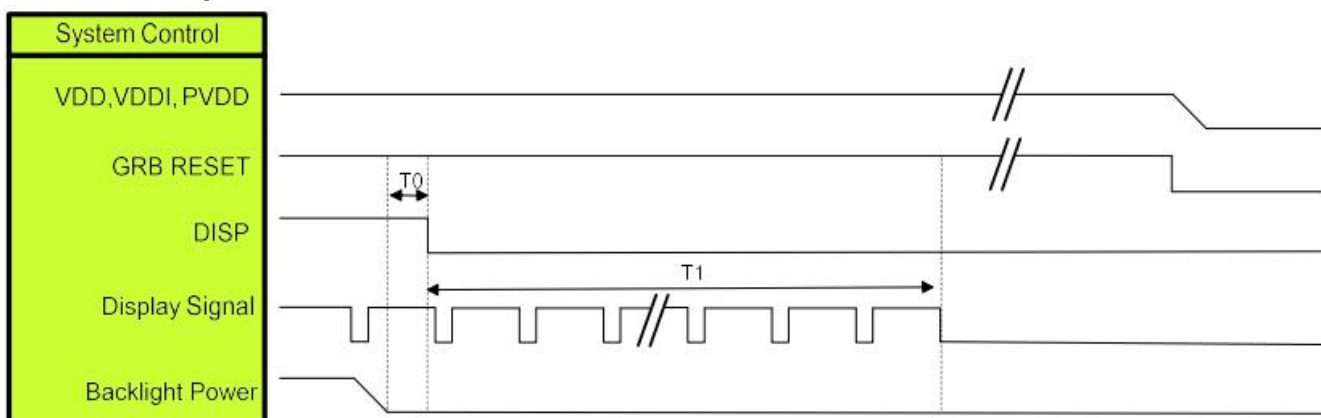
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	T_{cw}	40	50	60	%	
HSYNC Width	T_{hw}	2	-	-	DCLK	
HSYNC Period	T_h	55	60	65	us	
VSYNC Setup Time	T_{vst}	12	-	-	ns	
VSYNC Hold Time	T_{vhd}	12	-	-	ns	
HSYNC Setup Time	T_{hst}	12	-	-	ns	
HSYNC Hold Time	T_{hhd}	12	-	-	ns	
Data Setup Time	T_{dsu}	12	-	-	ns	
Data Hold Time	T_{dhhd}	12	-	-	ns	
DE Setup Time	T_{dest}	12	-	-	ns	
DE Hold Time	T_{dehd}	12	-	-	ns	

6.4 Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET = "High" to DISP = "High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

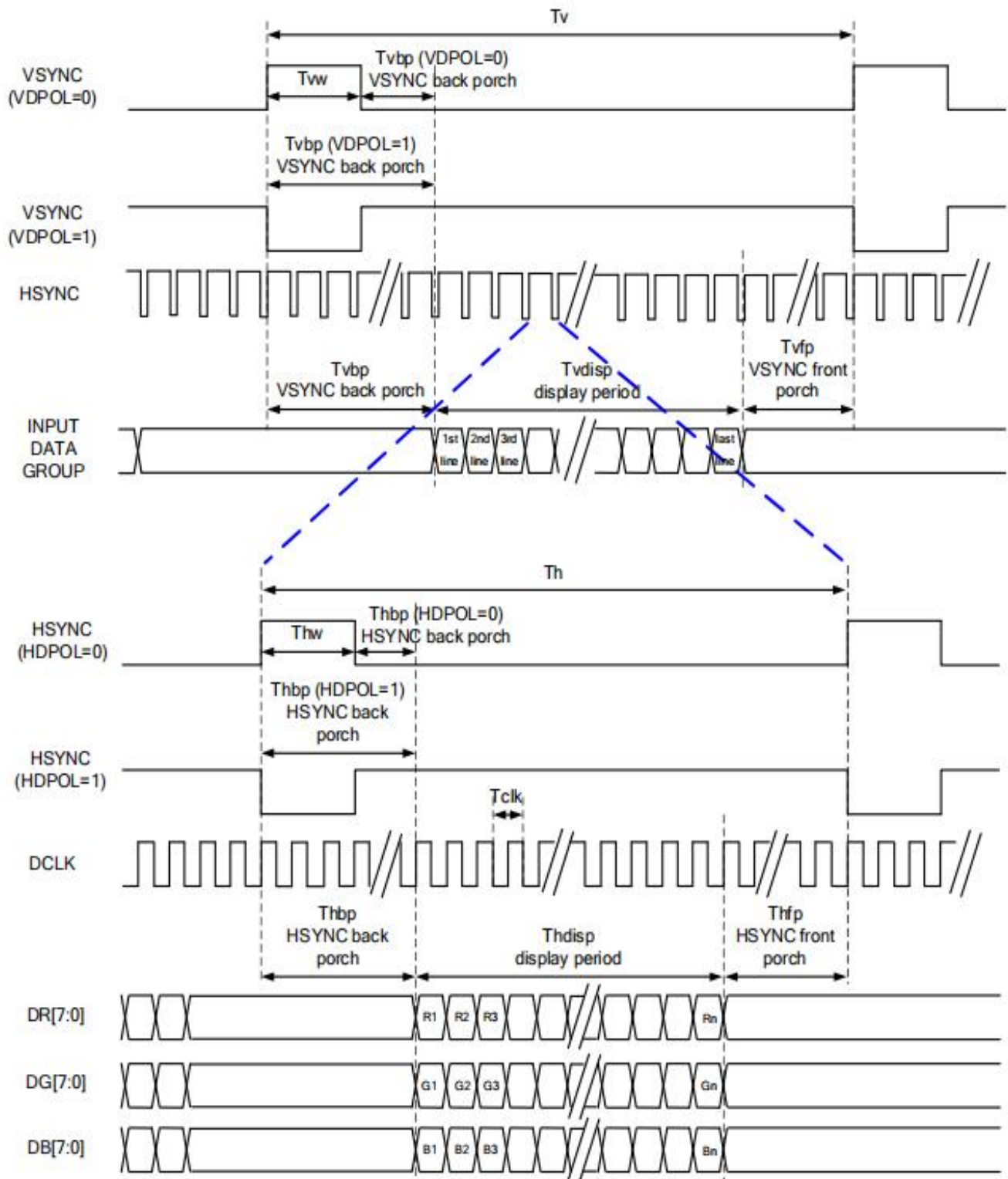
6.5 Power Off Sequence



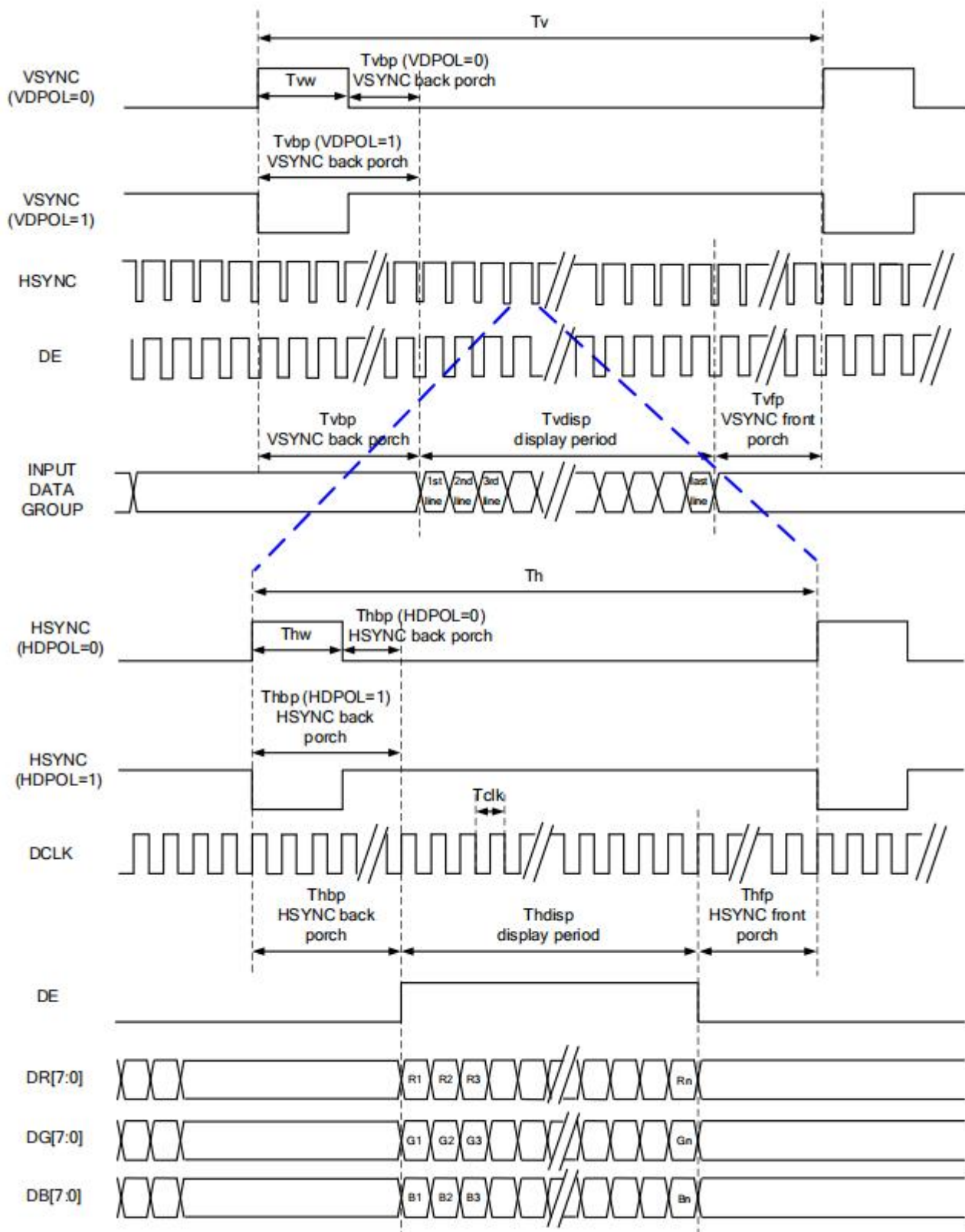
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP = "Low"	5	ms
T1	DISP = "Low" to IC internal voltage discharge complete	80	ms

6.6 RGB Interface

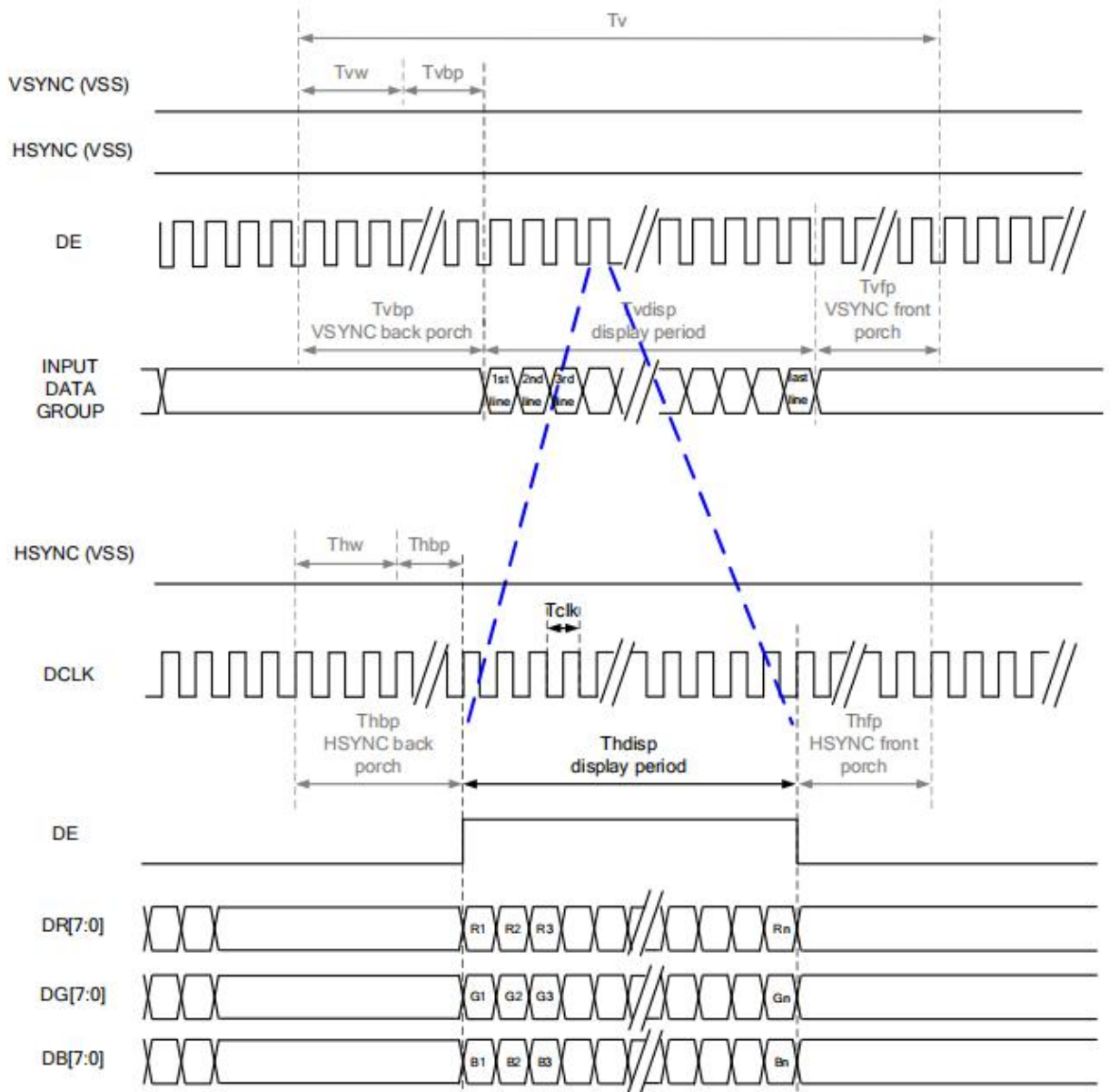
6.6.1 SYNC Mode



6.6.2 SYNC-DE Mode



6.6.3 DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

Parallel 24-bit RGB Input Timing (PVDD=PVDD1=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

Parallel 24-bit RGB Interface Timing Table						
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency	Fclk	23	25	27	MHz	
HSYNC	Period Time	Th	808	816	896	DCLK
	Display Period	Thdisp	800			DCLK
	Back Porch	Thbp	4	8	48	DCLK
	Front Porch	Thfp	4	8	48	DCLK
	Pulse Width	Thw	2	4	8	DCLK
VSYNC	Period Time	Tv	492	496	504	HSYNC
	Display Period	Tvdisp	480			HSYNC
	Back Porch	Tvbp	6	8	12	HSYNC
	Front Porch	Tvfp	6	8	12	HSYNC
	Pulse Width	Tvw	2	4	8	HSYNC

7. Optical Characteristics

Items		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	-	-	30	40	ms	FIG.1	Note4
Contrast Ratio		CR		800	1000	-	-	FIG.2	Note1
Luminance uniformity		Yu	$\theta = 0^\circ$	75	80	-	%	FIG.2	Note3
Surface luminance		LV	$\theta = 0^\circ$	350	400	-	cd/m2	FIG.2	Note2
NTSC		-	$\theta = 0^\circ$	45	50	-	%	FIG.2	Note5
Viewing angle		θ_T	Center CR $\geq$ 10	-	80	-	deg	FIG.3	Note6
		θ_B		-	80	-	deg	FIG.3	
		θ_L		-	80	-	deg	FIG.3	
		θ_R		-	80	-	deg	FIG.3	
Chromaticity	Red	R _X	$\theta = 0^\circ$ $\phi = 0^\circ$ Ta=25°	0.579	0.629	0.679	-	FIG.2 CIE1931	Note5
		R _Y		0.276	0.326	0.376	-		
	Green	G _X		0.287	0.337	0.387	-		
		G _Y		0.496	0.546	0.596	-		
	Blue	B _X		0.086	0.136	0.186	-		
		B _Y		0.093	0.143	0.193	-		
	White	W _X		0.250	0.300	0.350	-		
		W _Y		0.260	0.310	.0360	-		

Note1. Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula. For more information see FIG.2.

$$\text{Contrast ratio} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

For contrast ratio, Surface Luminance, Luminance uniformity and CIE,the testing data is base on TOPCON' s BM-5 or BM-7 photo detector or compatible.

Note2. Definition of surface luminance.

Surface luminance is the luminance with all pixels displaying white. For more information see FIG.2.

Lv = Average Surface Luminance with all white pixels(P1,P2,P3,,Pn)

Note3. Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance.For more information see FIG.2.

$$YU = \frac{\text{Minimum surface luminance with all white pixels (P1,P2,P3,.....,Pn)}}{\text{Maximum surface luminance with all white pixels (P1,P2,P3,.....,Pn)}}$$

Note4. Definition of response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_r) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_f) is the time between photo detector output intensity changed from 10% to 90%.

For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity, The x,y value is determined by screen active area center position P5. For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. Angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is base on Autronic-Melchers' s ConoScope or DMS series Instruments or compatible.

FIG.1. The definition of response Time

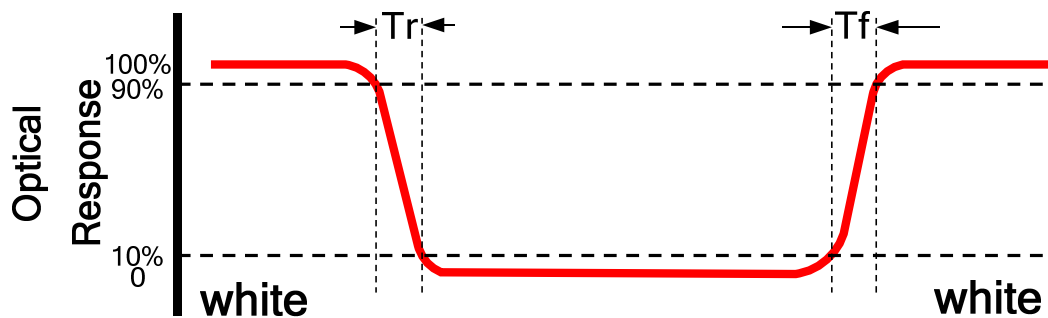


FIG.2. Measuring method for contrast ratio, surface luminance,

luminance uniformity, CIE (x,y) chromaticity

Size : $S \leq 5"$ (see Figure a) A : 5 mm B : 5 mm

H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure a.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

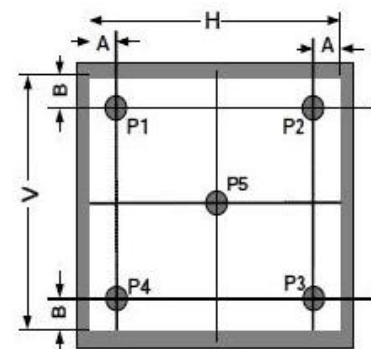


Figure a

Size : $5'' < S \leq 12.3''$ (see Figure b) H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

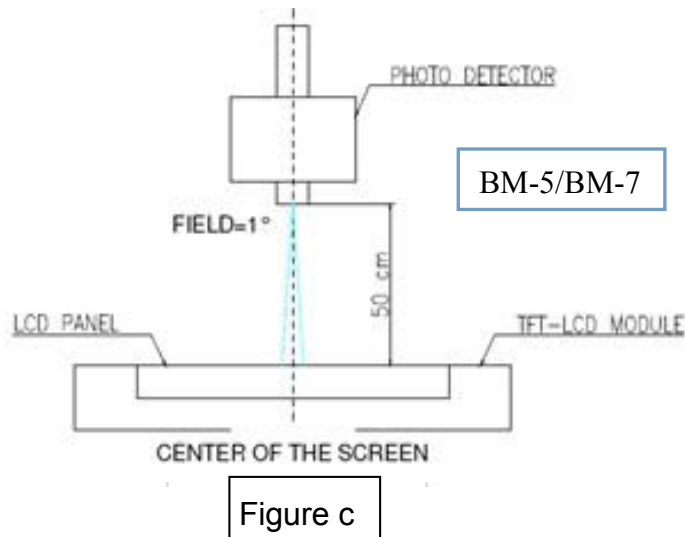
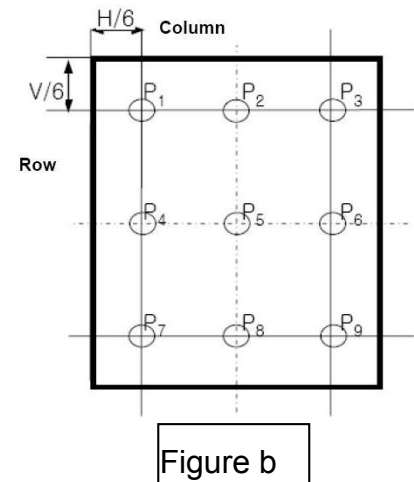
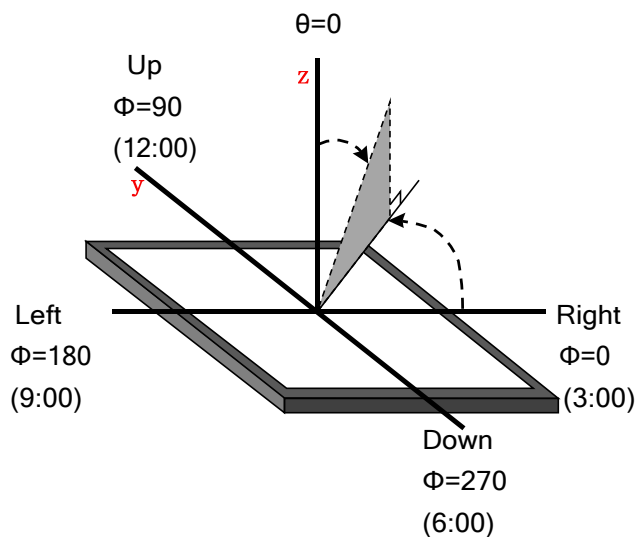


FIG.3.The definition of viewing angle



8. Environmental / Reliability Tests

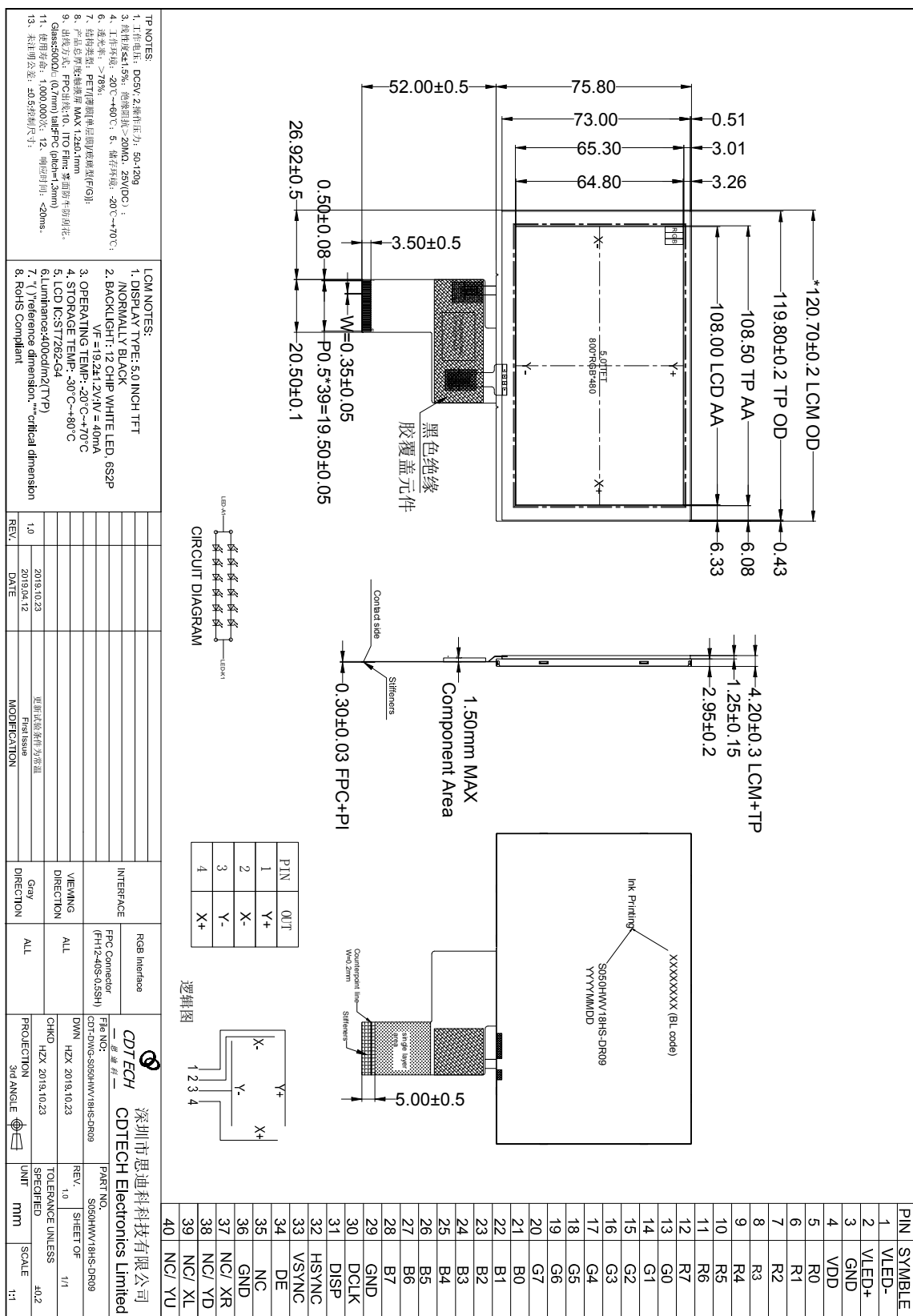
No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +70℃, 96hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	Ta= -20℃, 96hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +80℃, 96hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30℃, 96hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60℃, 90% RH max, 96 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-20℃ 30 min ~ +60℃ 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Discharge (Operation) Static	C=150pF, R=330 Ω, 5 points/panel Air: ±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15℃ ~ 35℃, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

Note: 1. Ts is the temperature of panel's surface.

2. Ta is the ambient temperature of sample.

3. The size of sample is 5pcs.

9. Mechanical Drawing



10. Packing

Packing Method

TBD

11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.