



深圳市思迪科科技有限公司

SHENZHEN CDTECH ELECTRONICS

Product Specification

Model Name	S070SWV49HGO-DC64
Description	7.0" WVGA 800(RGB)x480 Dots
Date	2021/01/05
Version	1.0

Approved by/Date	Check by/Date	Prepared by/Date
ZHP 2020/01/05	HZX 2020/01/05	ZWF 2020/01/05

Customer Approval	
Date	



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1. Record of Revision

Rev	Issued Date	Description	Editor
1.0	2020/01/05	First Release.	ZWF

2. General Specifications

	Feature	Spec
Characteristics	Size	7 inch
	Resolution	800(horizontal)*480(Vertical)
	Interface	24-bit RGB
	Connect type	Connector
	Display Colors	16.7M
	Technology type	a-Si
	Pixel pitch (mm)	0.192*0.179
	Pixel Configuration	R.G.B.-Stripe
	Display Mode	Normally White
	LCD Driver IC	TBD
	CTP Driver IC	ILI2130
	Viewing Direction	12 O'clock
	Gray Inversion Direction	6 O'clock
Mechanical	LCM (W x H x D) (mm)	165.0*100.0*7.45
	Active Area(mm)	154.08 x 85.92
	With /Without TSP	With CTP
	Weight (g)	TBD
	LED Numbers	27 LEDs

Note 1: Requirements on Environmental Protection: RoHs

Note 2: LCM weight tolerance: +/- 5%

3. Input/Output Terminals

LCD PIN-MAP

No.	Symbol	Description
1, 2	VLED+	Power for LED backlight (Anode)
3, 4	VLED-	Power for LED backlight (Cathode)
5	GND	Power Ground
6	VCOM	Common voltage
7	DVDD	Power for Digital Circuit
8	MODE	DE/SYNC mode select Note1
9	DE	Data Enable
10	VS	Vertical sync signal
11	HS	Horizontal sync Signal
12	B7	Blue data(MSB)
13	B6	Blue data
14	B5	Blue data
15	B4	Blue data
16	B3	Blue data
17	B2	Blue data
18	B1	Blue data Note2
19	B0	Blue data(LSB) Note2
20	G7	Green data(MSB)
21	G6	Green data
22	G5	Green data
23	G4	Green data
24	G3	Green data
25	G2	Green data
26	G1	Green data Note2
27	G0	Green data (LSB) Note2
28	R7	Red Data(MSB) Note2
29	R6	Red Data
30	R5	Red Data
31	R4	Red Data

32	R3	Red Data	
33	R2	Red Data	
34	R1	Red Data	Note2
35	R0	Red Data(LSB)	Note2
36	GND	Power Ground	
37	DCLK	Pixel clock	Note3
38	GND	Power Ground	
39	L/R	Left/right selection	Note4/5
40	U/D	up/down Note4/5	selection
41	VGH	Gate ON Voltage	
42	VGL	Gate OFF Voltage	
43	AVDD	Power for Analog Circuit	
44	RESET	Global reset pin	Note6
45	NC	NO connection	
46	Vcom	Common Voltage	
47	DITHB	Dithering function	Note7
48	GND	Power Ground	
49	NC	NO connection	
50	NC	NO connection	

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

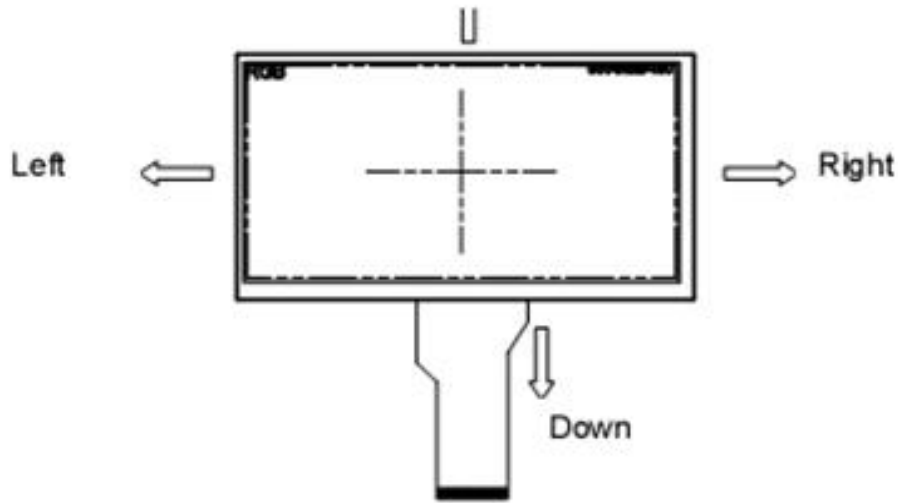
Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DV _{DD}	Up to down, left to right
DV _{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV _{DD}	DV _{DD}	Down to up, left to right

Note 5: Definition of scanning direction.

Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.
When DITHB="1", Disable internal dithering function,
When DITHB="0", Enable internal dithering function,

Note 8: Reserve for LED power input.

CTP PIN-MAP

Pin	Signal	Description
1	SCL	I2C clock input
2	SDA	I2C data input and output
3	VDD	Power supply for CTP
4	RST	Reset Pin for CTP
5	INT	Interrupt request to the host
6	GND	Ground

4. Absolute Maximum Rating

Item	Symbol	MIN	Typ	MAX	Unit	Remark
Supply Voltage	VDD	-0.3	-	5.0	V	-
Operating Temperature	TOPR	-20	-	70	°C	-
Storage Temperature	TSTG	-30	-	80	°C	

5. Electrical Characteristics

5.1 Driving TFT LCD Panel

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power voltage	V _{DD}	3.0	3.3	3.6	V	
	AVDD	10.2	10.4	10.6	V	
	VGH	15.3	16.0	16.7	V	
	VGL	-7.7	-7	-6.3	V	
Input signal voltage	VCOM	2.8	(3.3)	4.8	V	
Operating Temperature	T _{OPR}	-20	-	70	°C	
Storage Temperature	T _{STG}	-30	-	80	°C	

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power supply for LCD	I _{VDD}	30	45	60	mA	
	I _{AVDD}	25	35	50	mA	
	I _{VGH}	0.8	1	1.2	mA	
	I _{VGL}	0.8	1	1.2	mA	
	I _{VCOM}	8	10	15	mA	

5.2 CTP Electrical Characteristics

FPC Design	Item	Description	Remark
COF	IC solution on TP Model	ILI2130	
	Touch Count Max	5 point	
	Display Resolution	800*480	
	Interface Type	I2C	
	I2C Slave Address	0X41	
	Origin of Coordinate	Top left corner	

Parameter	Symbol	Min	Typ	Max	Unit
Interface Signal Voltage	-	-	3.3	-	V
Power Voltage	VDD	-	3.3	-	V

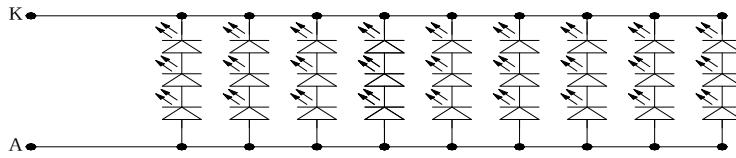
5.2 LED Driving Conditions

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I _F	-	180	-	mA	
Forward Voltage	V _F	8.4	9.6	10.8	V	
Backlight Power consumption	W _{BL}	1.488	1.728	1.98	W	
LED Lifetime		-	30000	-	Hrs	

Note 1: Each LED: I_F =20 mA, V_F =3.2+/0.2V.

Note 2: Optical performance should be evaluated at Ta=25℃ only.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life Time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



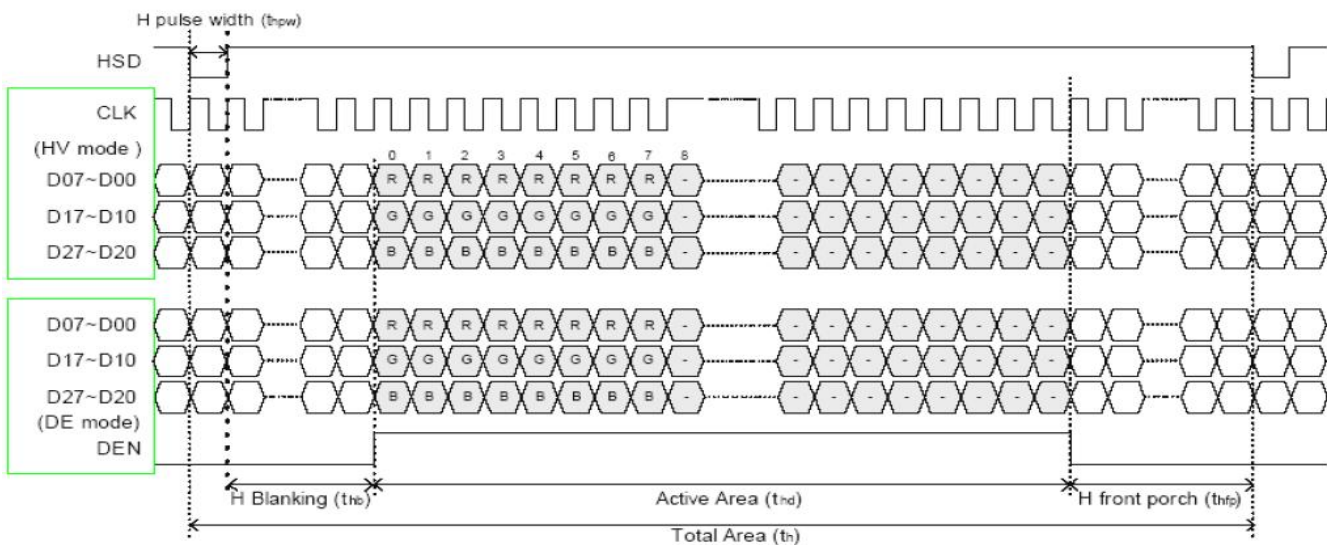
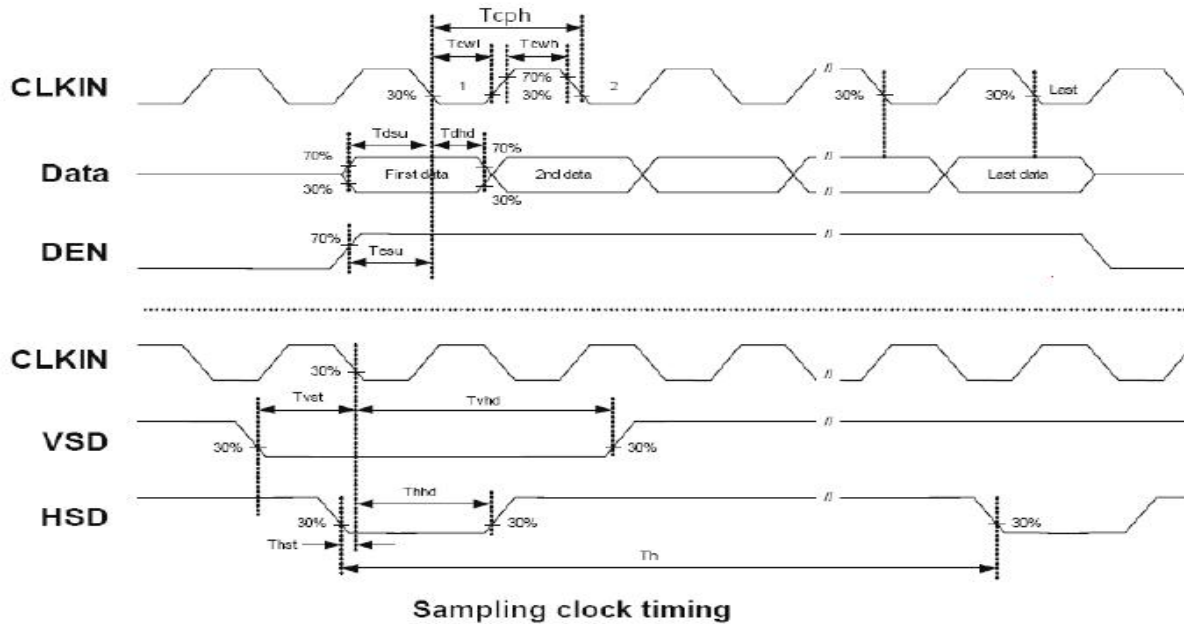
Backlight Circuit diagram

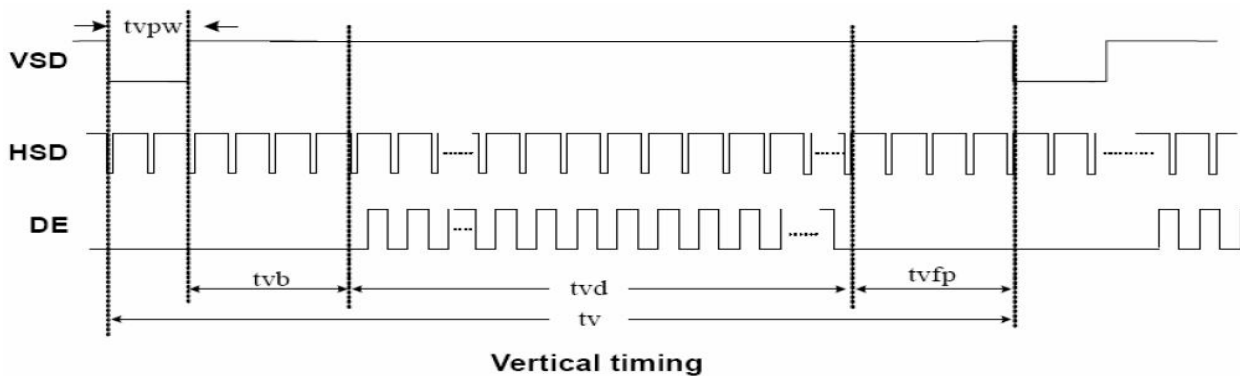
6. Interface Timing

6.1 Timing

Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK cycle time	Tcph	25			ns	
DCLK frequency	fcik		30	40	MHz	
DCLK pulse duty	Tcwh	40	50	60	%	
VSD setup time	Tvst	8			ns	
VSD hold time	Tvhd	8			ns	
HSD setup time	Thst	8			ns	
HSD hold time	Thhd	8			ns	
Data setup time	Tdsu	8			ns	
Data hold time	Tdhd	8			ns	
DE setup time	Tesu	8			ns	
DE hold time	Tehd	8			ns	
Horizontal display area	thd		800		Tcph	
HSD period time	th		928		Tcph	
HSD pulse width	thpw	1	48		Tcph	
HSD back porch	thb		40		Tcph	
HSD front porch	thfp		40		Tcph	
Vertical display area	tvd		480		th	
VSD period time	tv		525		th	
VSD pulse width	tvpw		3		th	
VSD back porch	tvb		29		th	
VSD front porch	tvfp		13		th	

6.2 Timing Diagram of Interface Signal





6.3 Capacitive touch panel Specification

I2C

The I2C is always configured in the Slave mode. The data transfer format is shown in [Figure 2-4](#).

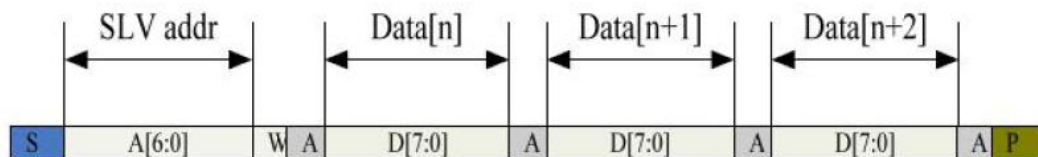
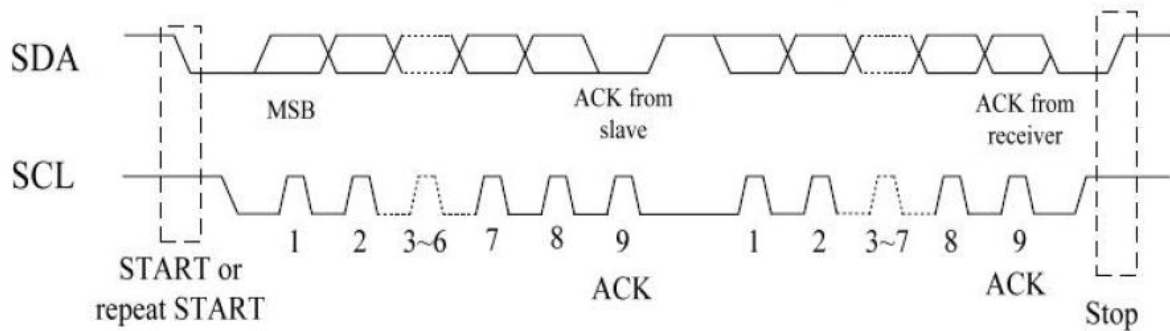


Figure 2-5 I2C master write, slave read

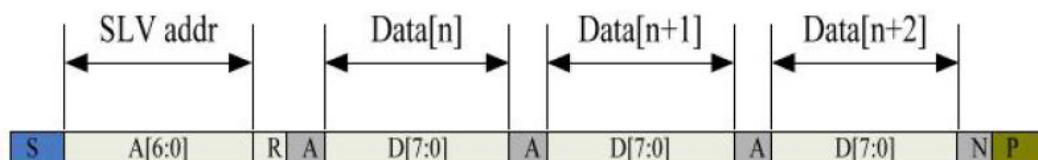


Figure 2-6 I2C master read, slave write

Table 2-1 lists the meanings of the mnemonics used in the above figures.

Table 2-1 Mnemonics Description

Mnemonics	Description
S	I2C Start or I2C Restart
A[6:0]	Slave address A[6:4]: 3'b011 A[3:0]: data bits are identical to those of I2CCON[7:4] register.
W	1'b0: Write
R	1'b1: Read
A(N)	ACK(NACK)
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

I2C Interface Timing Characteristics is shown in Table 2-2.

Table 2-2 I2C Timing Characteristics

Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) START condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup Time for STOP condition	us	4.0	\

7. Optical Characteristics

Items		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	-	-	25	35	ms	FIG.1	Note4
Contrast Ratio		CR		400	500	-	-	FIG.2	Note1
Surface luminance		LV	$\theta = 0^\circ$	400	450	-	cd/m ²	FIG.2	Note2
Luminance uniformity		Yu	$\theta = 0^\circ$	75	80	-	%	FIG.2	Note3
NTSC		-	$\theta = 0^\circ$	-	50	-	%	FIG.2	Note5
Viewing angle		θ_T	Center CR $\geq$ 10	-	75	-	deg	FIG.3	Note6
		θ_B		-	70	-	deg	FIG.3	
		θ_L		-	75	-	deg	FIG.3	
		θ_R		-	75	-	deg	FIG.3	
Chromaticity	Red	R _X	$\theta = 0^\circ$ $\phi = 0^\circ$ Ta=25°	0.521	0.571	0.621	-	FIG.2 CIE1931	Note5
		R _Y		0.294	0.344	0.394	-		
	Green	G _X		0.302	0.352	0.402	-		
		G _Y		0.548	0.598	0.648	-		
	Blue	B _X		0.102	0.152	0.202	-		
		B _Y		0.034	0.084	0.134	-		
	White	W _X		0.261	0.312	0.362	-		
		W _Y		0.279	0.329	0.379	-		

Note1. Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula. For more information see FIG.2.

$$\text{Contrast ratio} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is based on TOPCON's BM-5 or BM-7 photo detector or compatible.

Note2. Definition of surface luminance.

Surface luminance is the luminance with all pixels displaying white. For more information see FIG.2.

L_v = Average Surface Luminance with all white pixels($P_1, P_2, P_3, \dots, P_n$)

Note3. Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

$$YU = \frac{\text{Minimum surface luminance with all white pixels } (P_1, P_2, P_3, \dots, P_n)}{\text{Maximum surface luminance with all white pixels } (P_1, P_2, P_3, \dots, P_n)}$$

Note4. Definition of response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_r) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_f) is the time between photo detector output intensity changed from 10% to 90%.

For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity, The x,y value is determined by screen active area center position P5. For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. Angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is based on Autronic-Melchers' s ConoScope or DMS series Instruments or compatible.

FIG.1.The definition of response Time

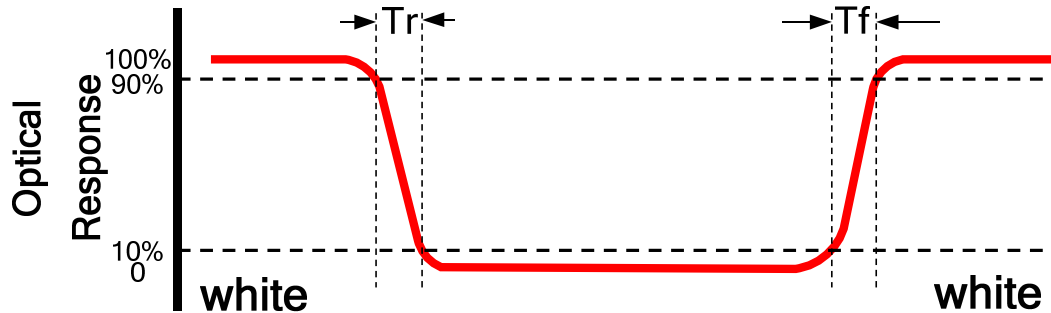


FIG.2. Measuring method for contrast ratio, surface luminance,

luminance uniformity, CIE (x,y) chromaticity

Size : $S \leq 5"$ (see Figure a) A : 5 mm B : 5 mm

H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure a.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

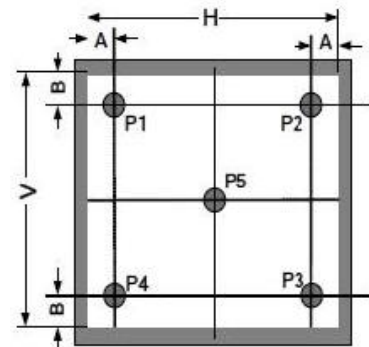


Figure a

Size : $5" < S \leq 12.3"$ (see Figure b) H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c).

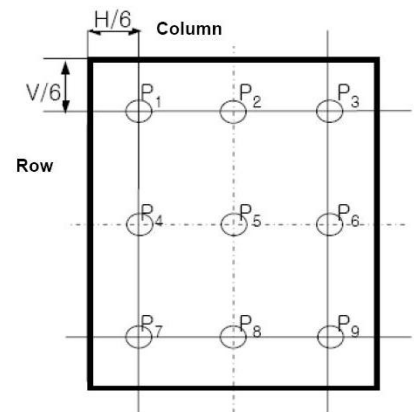


Figure b

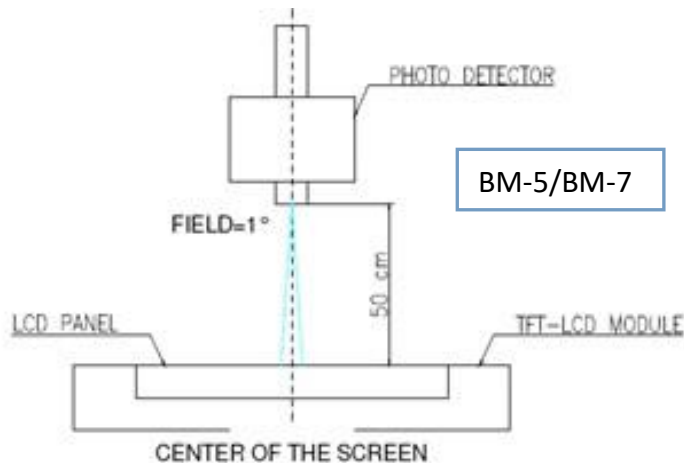
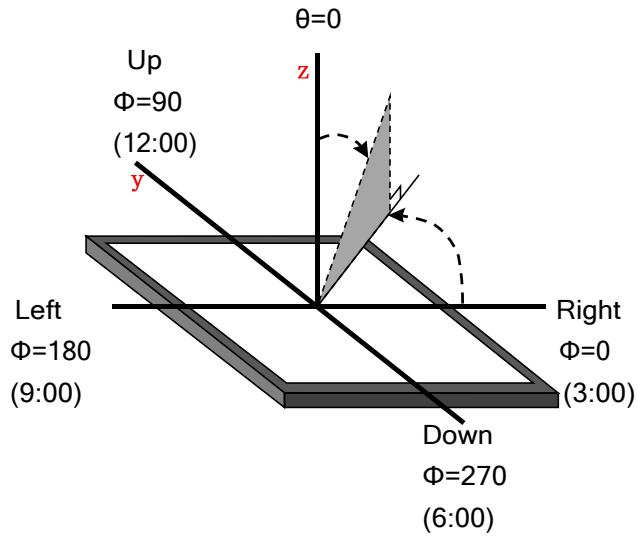


Figure c

FIG.3.The definition of viewing angle



8. Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +70℃, 96hrs	IEC60068-2-1:2007 GB2423. 2-2008
2	Low Temperature Operation	Ta= -20℃, 96hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta= +80℃, 96hrs	IEC60068-2-1:2007 GB2423. 2-2008
4	Low Temperature Storage	Ta= -30℃, 96hrs	IEC60068-2-1:2007 GB2423.1-2008
5	High Temperature & Humidity Operation	Ta= +60℃, 90% RH max, 96 hours	IEC60068-2-78:2001 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-30℃ 30 min ~ +80℃ 30 min Change time: 5min, 20 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14:1984, GB2423.22-2002
7	ESD	C=150pF, R=330 Ω, 5 points/panel , Air:±8KV, 5 times Contact: ±4KV, 5 times (Environment: 15℃ ~ 35℃, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.5mm , Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (6 hours for total)	IEC60068-2-6:1982 GB/T2423.10-1995
9	Mechanical Shock (Non-operation)	Half Sine Wave 60G ,6ms,±X,±Y,±Z 3times for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32:1990 GB/T2423.8-1995



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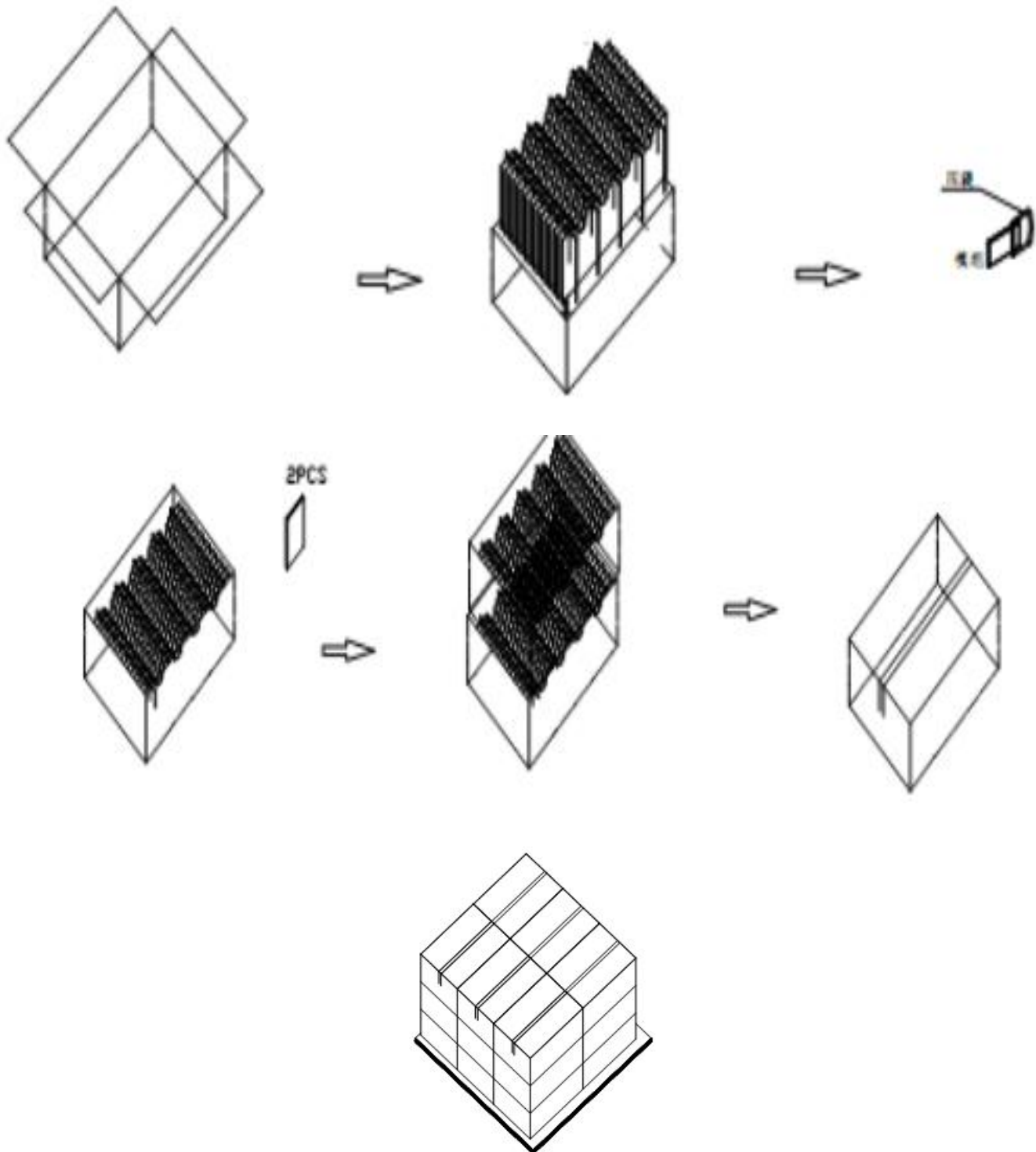
Notes:

1. The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:
 - 1).Air bubble in the LCD;
 - 2).Seal leak
 - 3).Non-display
 - 4).missing segments
 - 5).Glass crack
 - 6).CR reduction >40%
 - 7).IDD increase >100%
 - 8).Brightness reduction >50%
 - 9).Color coordinate tolerance >0.05
2. ≤ 7.0 inch: The size of sample is 5pcs;
 > 7.0 inch: The size of sample is 2pcs;
3. One test sample must complete each test item;
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
5. In the test of High Temperature Operation and High Temperature & Humidity Operation ,the operation temperature is the surface temperature of module.

[illegible]

10. Packing

Packing Method



11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions



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The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.