



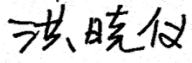
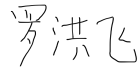
PRODUCT SPECIFICATION

CDTECH Model: **S101HWX81EP-FC70-AG**

CUSTOMER Model: **-**

Description: **10.1" IPS-LCD Module with CTP**

Version: **2.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2024.7.11	2024.7.11	2024.7.11

CUSTOMER APPROVAL	SIGNATURE	DATE

Record of Revisions

Version	Revise Date	Description	Page
1.0	2023-9-18	First Release	-
	2023-10-13	Modify series connection and parallel connection, adjust the position of the FPC 1-50 PIN	19
	2023-10-20	Update FPC	
	2023-10-31	Update DST	
	2023-11-2 2023-11-3	Update Cover Lens	
	2023-11-9	Remove PIN1-PIN50 annotations from the front view	
	2024-3-27	Updates the external dimensions and color coordinates	4,15
2.0	2024.4.23	Update NOTE 6 / 7 / 8 and refine the test point	17,18
		Update FPC	19
	2024.5.16	Update the OCA	19
	2024.6.5	Update the total thickness after adding 0.2mm foam adhesive to the backlight frame	4,19



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1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	10.1	inch
Number of Pixels	1280 (H) RGB x 800 (V)	pixels
Display Mode	Normally Black	-
Viewing Direction	Free	o' clock
Interface	LVDS	-
Display Colors	16.7M	colors
Outline Dimension	252.40 (H) x 160.40 (V) x 6.53 (D)	mm
Active Area	216.96 (H) x 135.60 (V)	mm
Pixel Pitch	0.1695 (H) x 0.1695 (V)	mm
Driver IC	EK79202B1	-
Operation Temperature	-30~80	°C
Storage Temperature	-40~80	°C

1.2 Touch Panel Information

Item	Specification
Touch Structure	G+G
Bonding Type with LCM	OCA Optical Bonding
Driver IC	GT928
Interface	I ² C
Touch Count Max	10 Points
Surface treatment	AG
Surface hardness	6H
I2C slave address	0x28
Origin of coordinate	Top Left Corner

Note1: Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VDD	-0.3	5.0	V	Note 1

Note 1: Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

3. Electrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

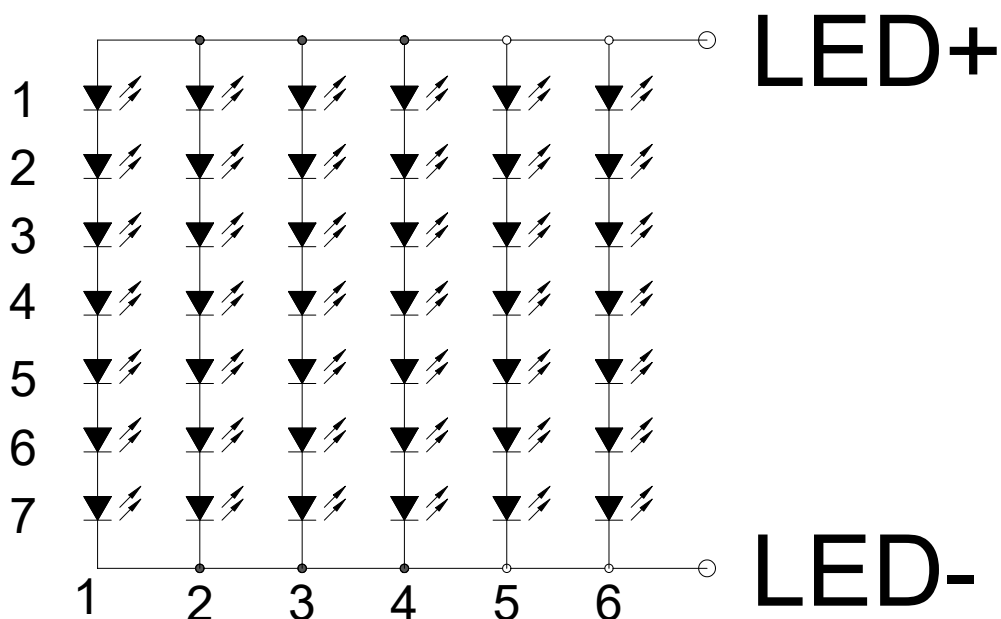
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VDD	3.0	3.3	3.6	V	
Analog supply current	I _{VDD}	-	TBD	-	mA	VDD=3.3V
Logic input voltage	V _{IH}	0.7*VDD	-	VDD	V	
	V _{IL}	GND	-	0.3*VDD	V	

3.2 Recommended Driving Condition for Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I _F	-	300	-	mA	
Driving Voltage	V _F	18.9	-	23.8	V	
Power consumption	W _{BL}	5.67	-	7.14	W	
LED Life-Time	N/A	-	50,000	-	Hours	Ta=25°C Note 1

Note 1: LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2:LED circuit :



3.3 Touch Panel

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply voltage	VDD	-	3.3	-	V	
Analog supply current	I _{VDD}	-	TBD	-	mA	VDD=3.3V
Input high-level voltage	VIH	0.7*VDD	-	VDD	V	
Input low -level voltage	VIL	GND	-	0.3*VDD	V	

4. Interface Pin Assignment

4.1 LCM Pin Assignment

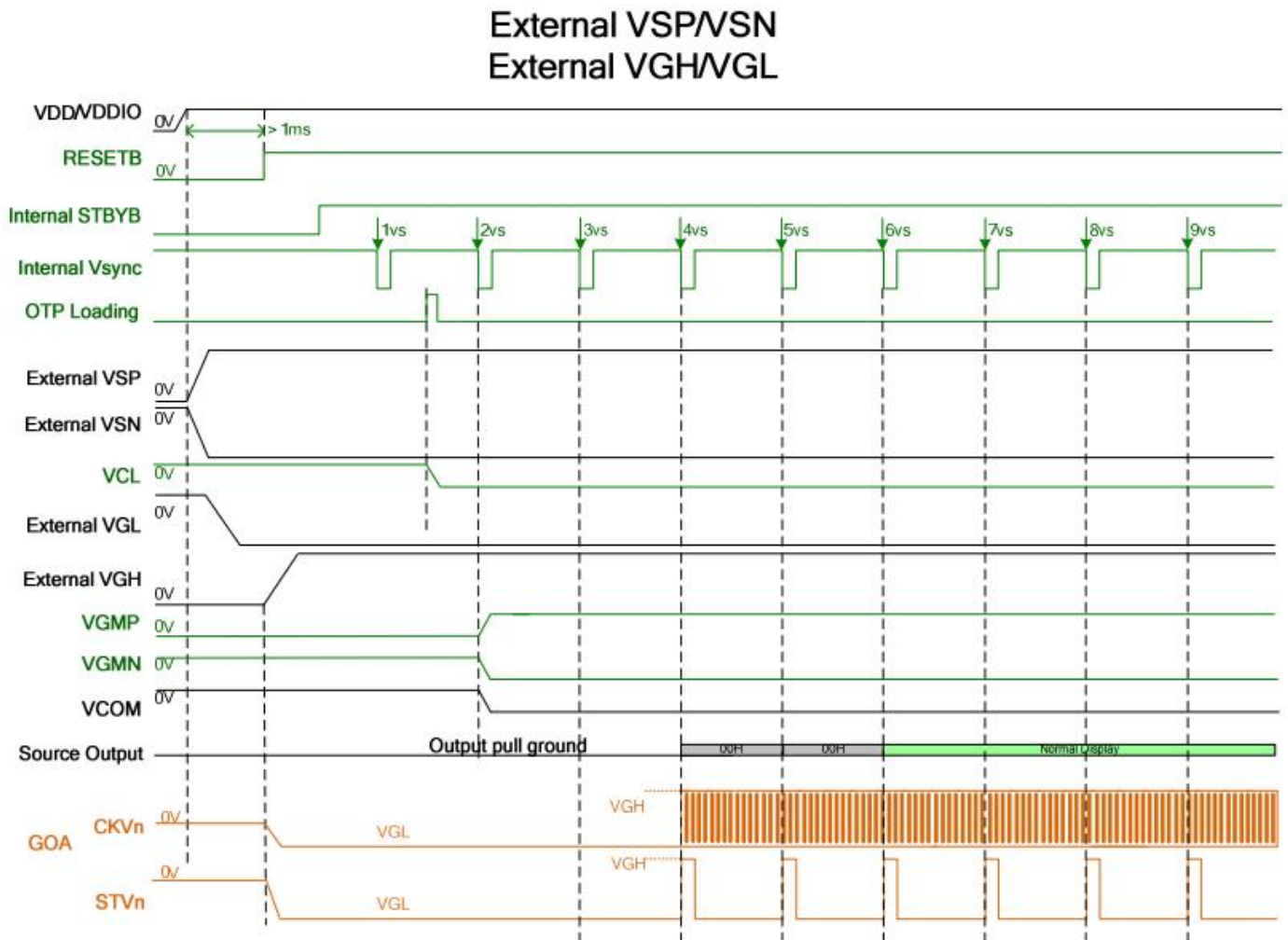
No.	Symbol	Description
1	GND	Ground
2	VDD3V3	Power supply (3.3V)
3	VDD3V3	Power supply (3.3V)
4	GND	Ground
5	RESET	Global reset pin
6	STBYB	Standby mode Normally pulled high STBYB=1,normal operation STBYB=0 timing contrller,source Driver will turn off,all output are High
7	GND	Ground
8	D0N_LVDS	- LVDS differential data input
9	D0P_LVDS	+ LVDS differential data input
10	GND	Ground
11	D1N_LVDS	- LVDS differential data input
12	D1P_LVDS	+ LVDS differential data input
13	GND	Ground
14	D2N_LVDS	- LVDS differential data input
15	D2P_LVDS	+ LVDS differential data input
16	GND	Ground
17	CLKN_LVDS	-LVDS differential clock input
18	CLKP_LVDS	+LVDS differential clock input
19	GND	Ground
20	D3N_LVDS	- LVDS differential data input
21	D3P_LVDS	+ LVDS differential data input
22	GND	Ground
23	SDA(NC)	No connection
24	SCL(NC)	No connection
25	NC	No connection
26	CS(NC)	No connection
27	LVBIT	6-bit / 8-bit input select for LVDS mode
28	GND	Ground
29	NC	No connection
30	NC	No connection
31	NC	No connection

32	GND	Ground
33	INT_CTP	Interrupt signal for CTP
34	SDA_CTP	I2C data input and output for CTP
35	SCL_CTP	I2C clock input for CTP
36	RESET_CTP	Reset Pin for CTP
37	GND	Ground
38	VDD_3V3_CTP	Power supply for CTP (3.3V)
39	VDD_3V3_CTP	Power supply for CTP (3.3V)
40	GND	Ground
41	LEDK	Power for LED backlight (Cathode)
42	LEDK	Power for LED backlight (Cathode)
43	LEDK	Power for LED backlight (Cathode)
44	NC	No connection
45	NC	No connection
46	NC	No connection
47	NC	No connection
48	LEDA	Power for LED backlight (Anode)
49	LEDA	Power for LED backlight (Anode)
50	LEDA	Power for LED backlight (Anode)

5. Interface Characteristics

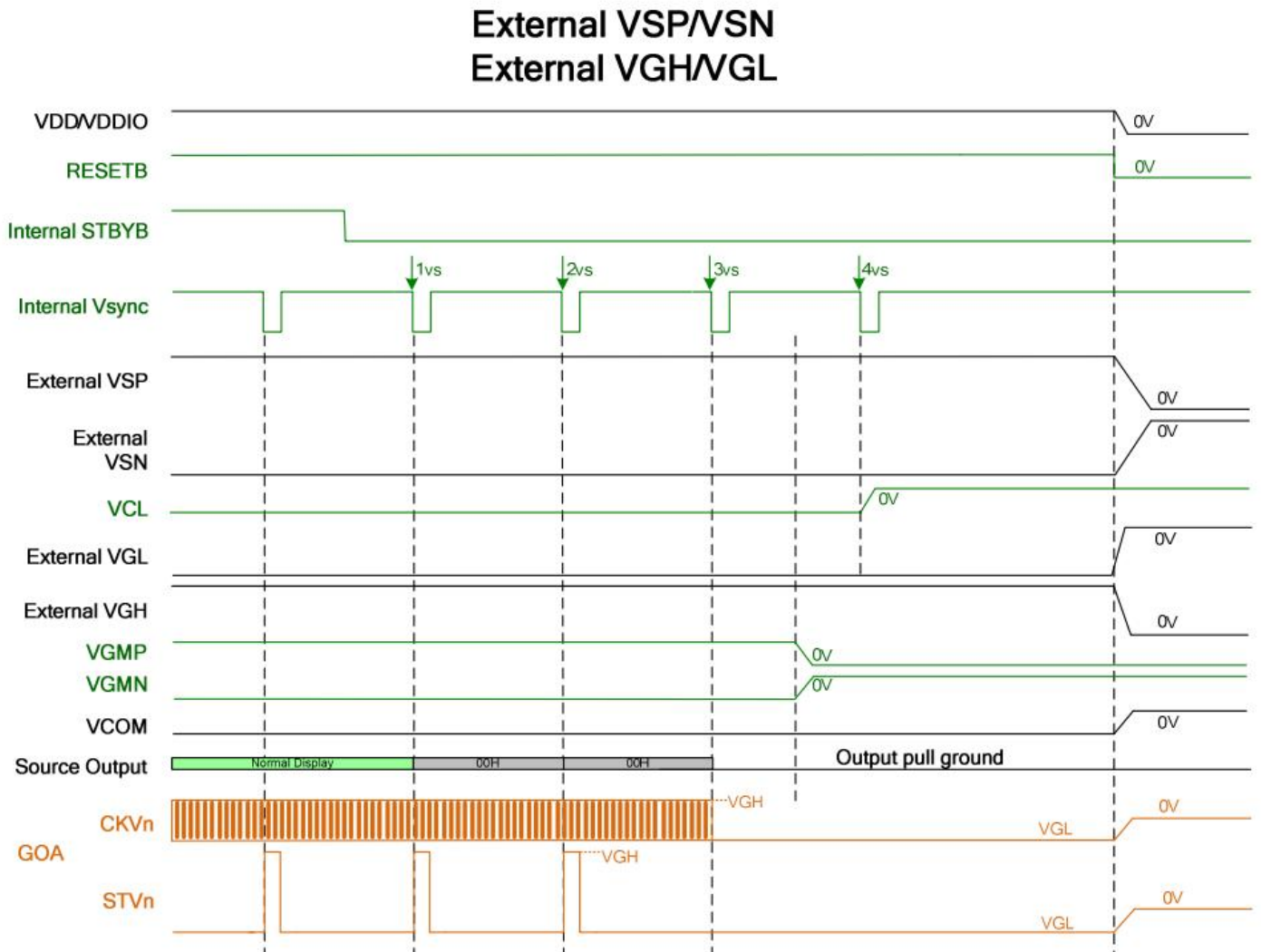
5.1 Power Sequence

5.1.1 Power on sequence



Power on sequence with PMODE=H

5.1.2 Power off sequence



Power off sequence with PMODE=H

5.2 AC Characteristics

5.2.1 LVDS mode AC electrical characteristics

Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
Clock frequency	R_{xCLK}	30	-	TBD	MHz	Refer to input timing table for each display resolution
Input data skew margin	T_{RSKM}	500	-	-	ps	$ VID = 200mV$ $R_{xVCM} = 1.2V$ $R_{xCLK} = 81MHz$
Clock high time	T_{LVCH}	-	$4/(7 * R_{xCLK})$	-	ns	
Clock low time	T_{LVCL}	-	$3/(7 * R_{xCLK})$	-	ns	
PLL wake-up time	T_{enPLL}	-	-	150	us	

Table 13.1: LVDS mode AC electrical characteristics

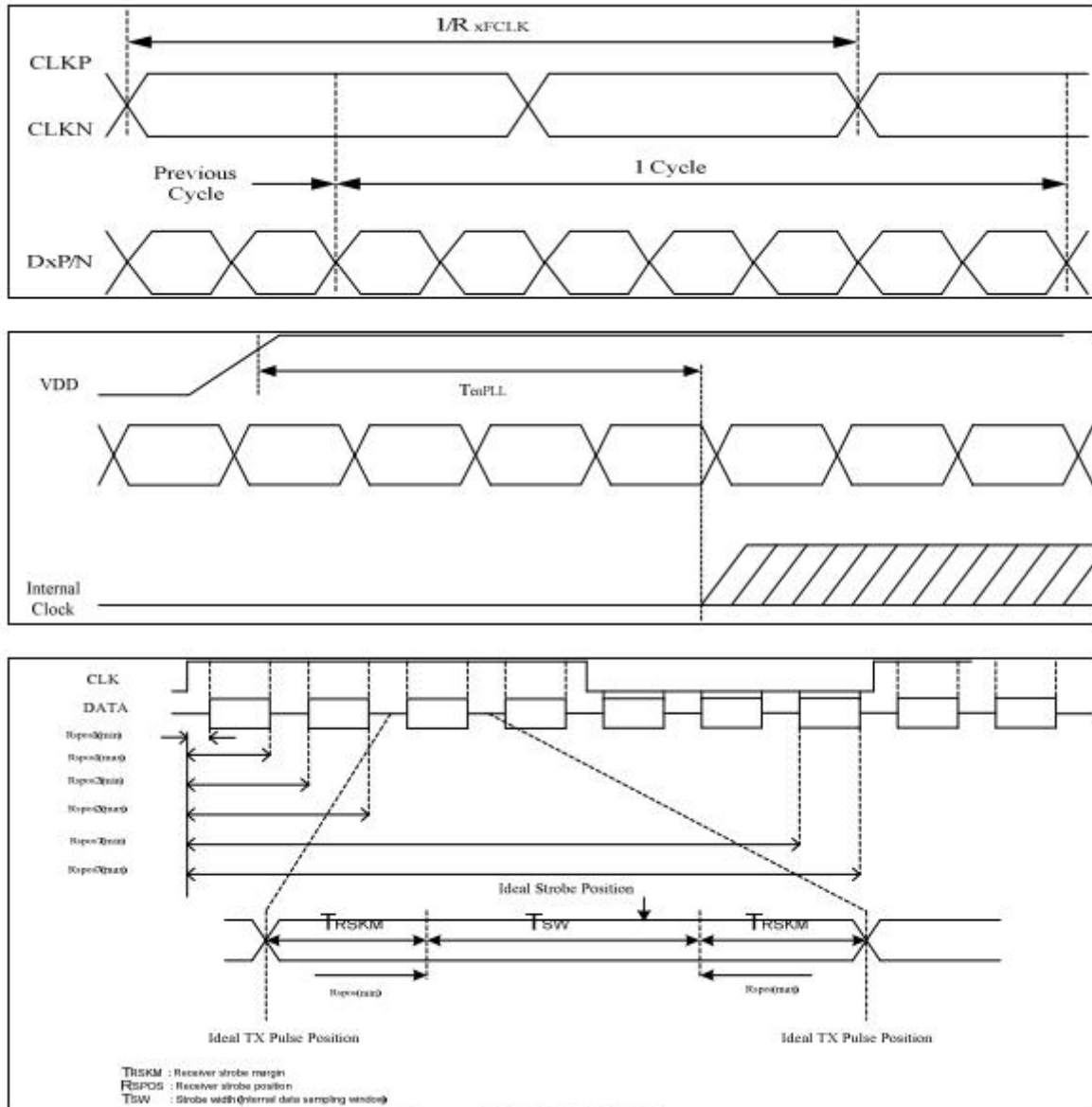


Figure 13.3: LVDS figure

5.2.2 Timing requirements for RESETB

When RESETB of the reset pin equals to Low, it will be in the condition of reset.

When it is in the condition of reset, it will make the device recover the initial set.

However, in order to avoid the reset noise cause reset, there is a mechanism to judge about whether the reset is needed or not.

The closed interval of Low can be shown as the following.

(Test condition: VDDIO=2.3V~3.6V, VSS=0V, T_A=-20 ~+85)

Parameter	Symbol	Conditions	Spec.			Unit
			Min.	Typ.	Max	
Reset low pulse width	Trst		20	-	-	μs

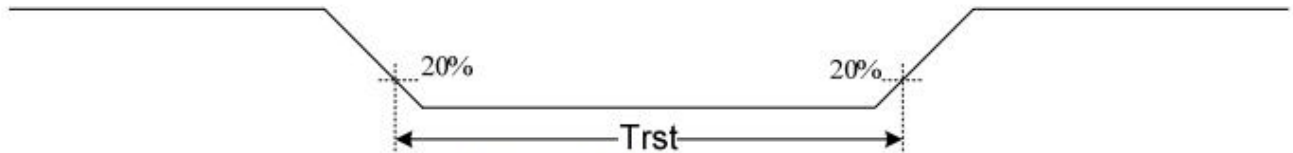
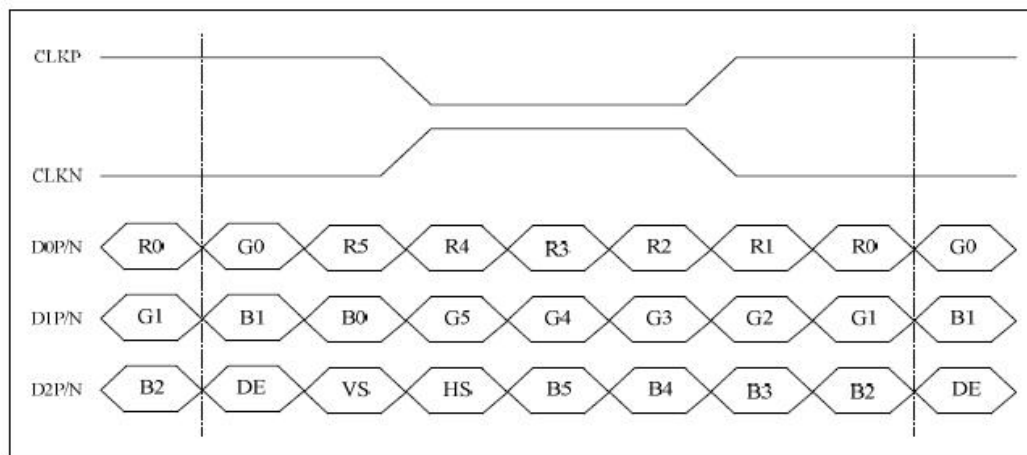


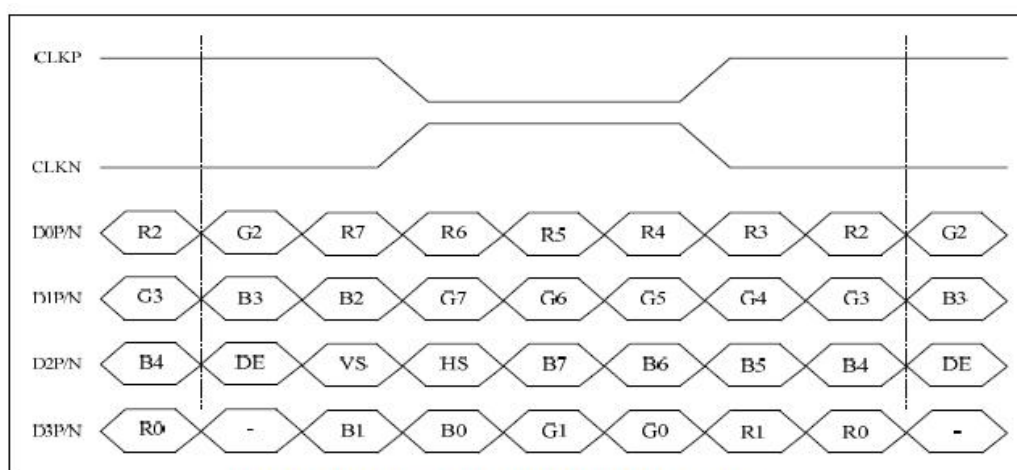
Figure 13.5: Reset timing

5.2 LVDS interface

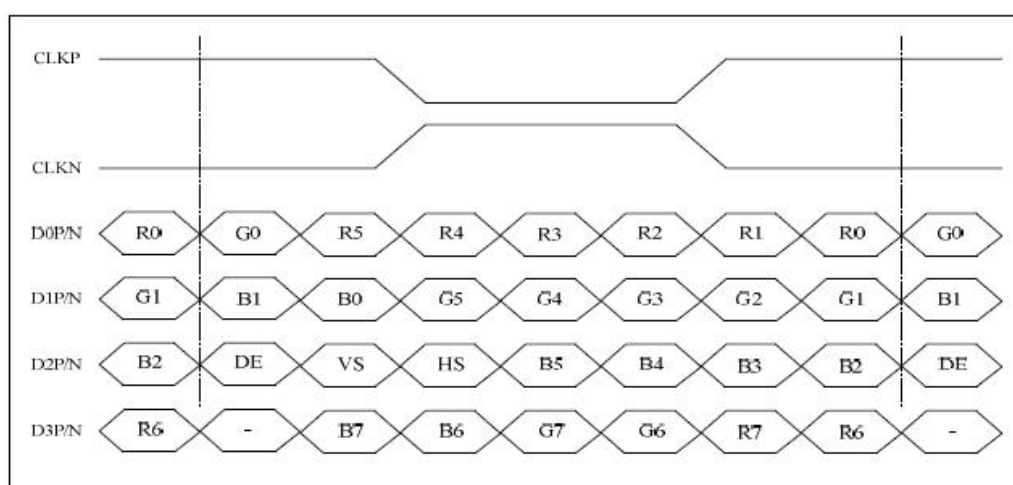
5.2.1 Data input format for LVDS



6-bit LVDS input (LVBIT=L, LVFMT=Don't care)



8-bit LVDS input (LVBIT=H, LVFMT=L)



8-bit LVDS input (LVBIT=H, LVFMT=H)

For 1280RGBx800

Parameter		Symbol	Value			Unit
			Min.	Typ.	Max.	
DCLK frequency @Frame rate=60Hz (LVDS)		F _{DCLK}	66.3	72.4	78.9	MHz
HSYNC period time		T _H	1380	1440	1500	DCLK
Horizontal display area		T _{HD}	1280			DCLK
HSYNC pulse width	Min.	T _{HPW}	1			
	Typ.		-			
	Max.		40			
HSYNC back porch(with pulse width)		T _{HBP}	88	88	88	DCLK
HSYNC front porch		T _{HFP}	12	72	132	DCLK
VSYNC period time		T _V	824	838	872	H
Vertical display area		T _{VD}	800			H
VSYNC pulse width	Min.	T _{VPW}	1			H
	Typ.		-			
	Max.		20			
VSYNC back porch(with pulse width)		T _{VBP}	23	23	23	H
VSYNC front porch		T _{VFP}	1	15	49	H

6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	$\Phi=90^\circ$ (12 o'clock)	70	85	-	deg	Note2
	θ_B	$\Phi=270^\circ$ (6 o'clock)	70	85	-	deg	Note2
	θ_L	$\Phi=180^\circ$ (9 o'clock)	70	85	-	deg	Note2
	θ_R	$\Phi=0^\circ$ (3 o'clock)	70	85	-	deg	Note2
Response Time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	12	17	msec	Note4
	T_{OFF}		-	12	17	msec	Note4
Contrast Ratio	CR		800	1000	-	-	Note1 Note3
Color Chromaticity	W_X		0.3028	0.3328	0.3628	-	Note1 Note5
	W_Y		0.3278	0.3578	0.3878	-	Note1 Note5
Luminance	L		650	850	-	cd/m ²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		60	65	-	%	Subject to actual testing

Note 1: Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

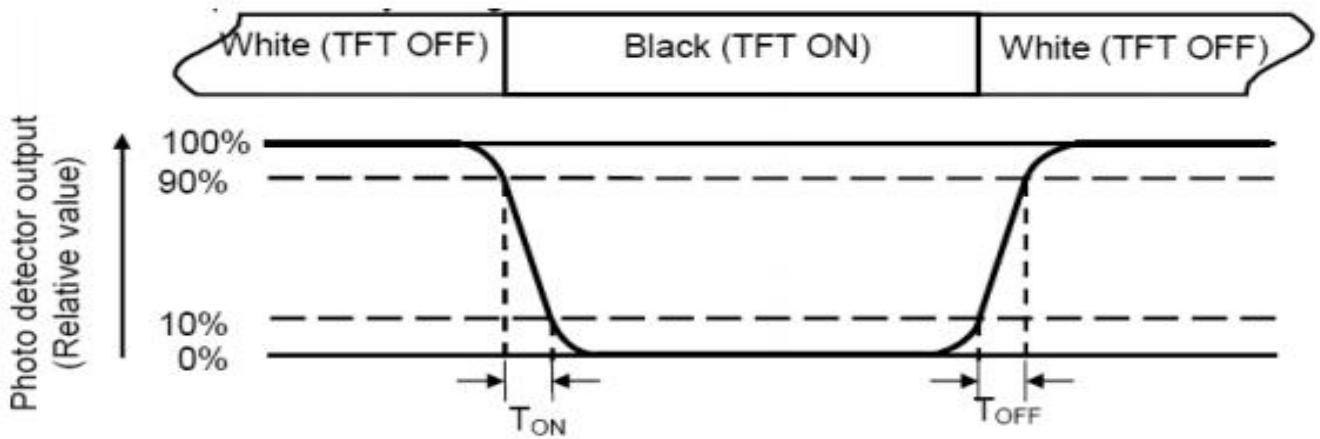


Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.3-a/b

Note 7: Surface luminance is the luminance with all pixels displaying white.

L_v = Average Surface Luminance with all white pixels ($P_1, P_2, P_3, \dots, P_n$)

For more information see FIG.3-a/b

Note 8: Size : $S \leq 5"$ (see Figure a) H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or test spot position : see Figure a.
measurement instrument : TOPCON's luminance meter SR-3A or BM-7 or compatible (see Figure 1).

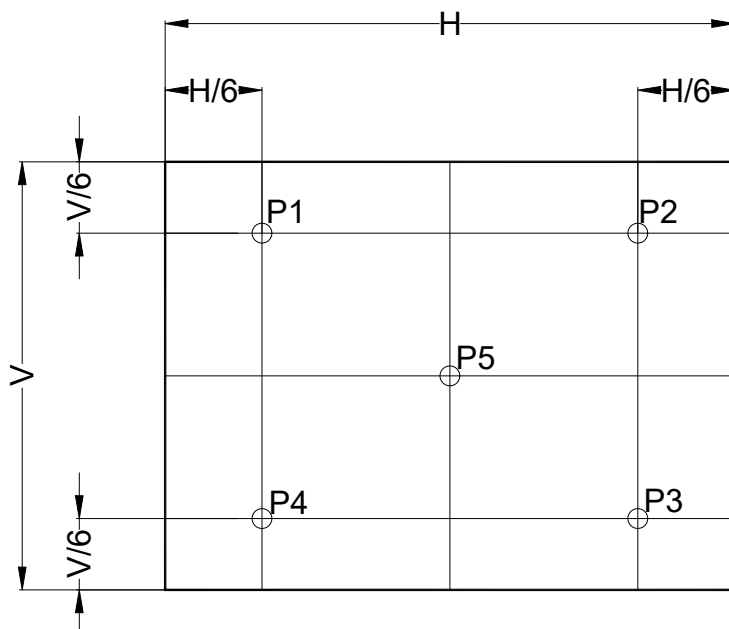


Fig. 3-a Definition of points

$5" < S \leq 12.3"$ (see Figure b) . H,V : Active area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens. test spot position : see Figure b.

measurement instrument : TOPCON's luminance meter SR-3A or BM-7 or compatible (see Figure 1).

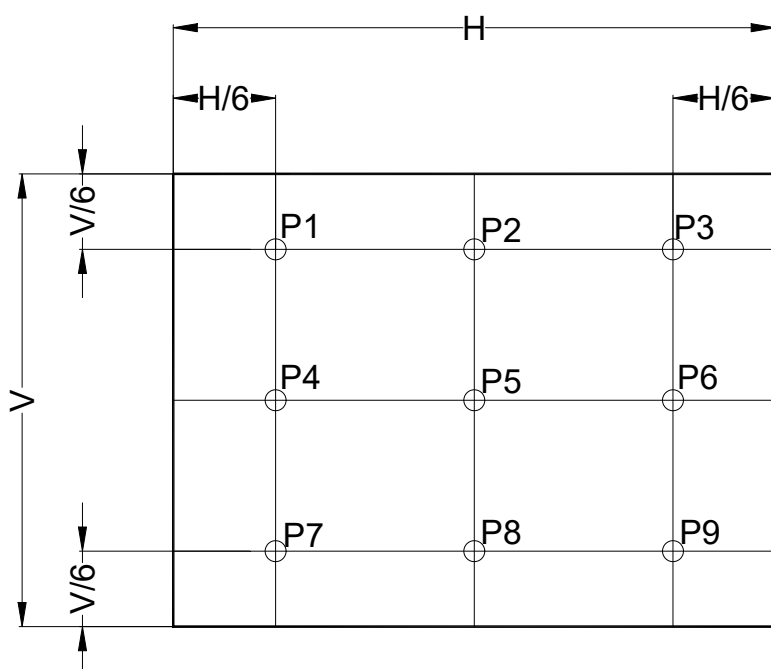


Fig. 3-b Definition of points

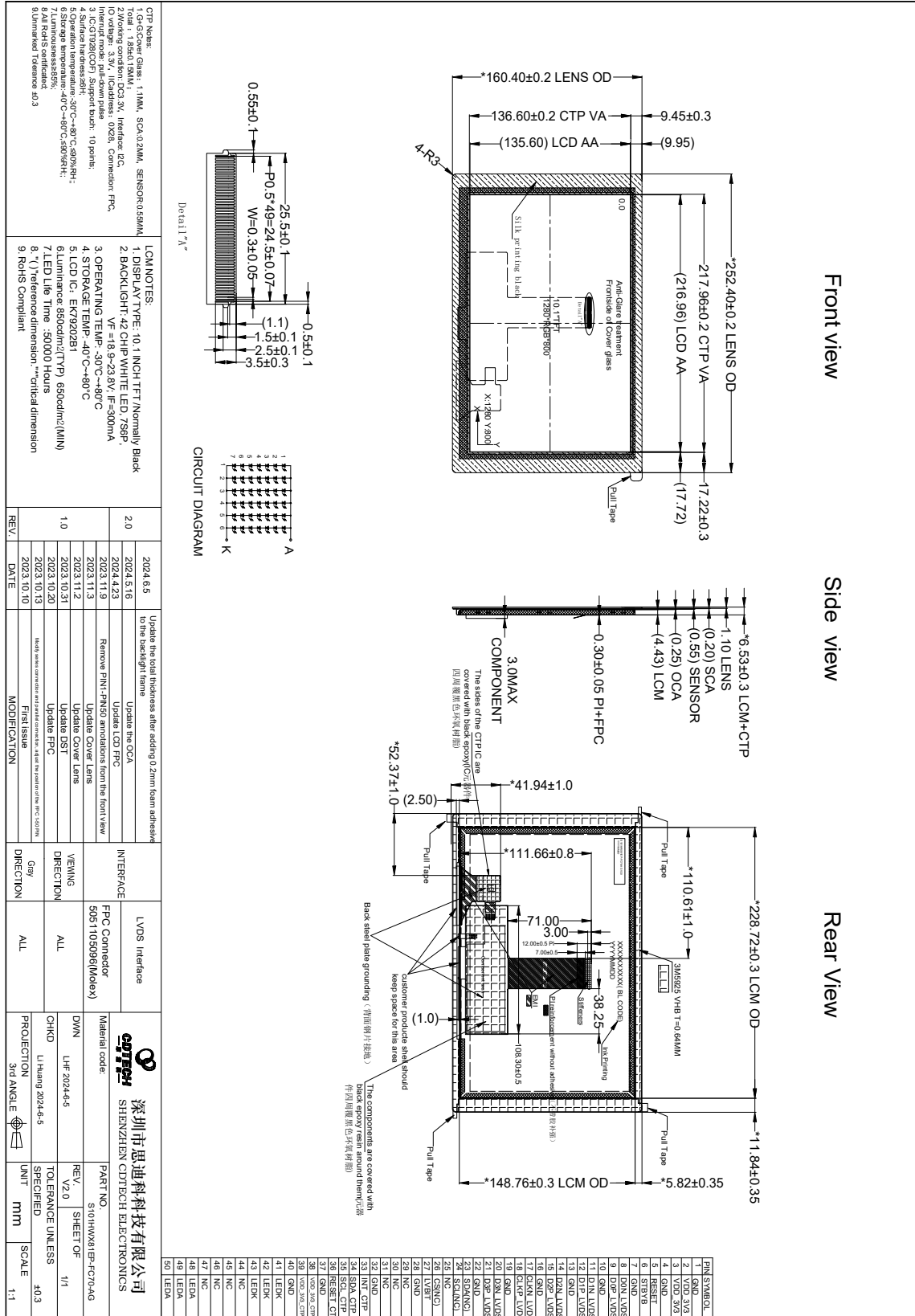
7. Reliability Test Items

Test Item	Test Conditions
High Temperature Storage	Ta= +80℃ 96hrs
Low Temperature Storage	Ta= -40℃ 96hrs
High Temperature Operation	Ta= +80℃ 96hrs
Low Temperature Operation	Ta= -30℃ 96hrs
High Temperature and Humidity Storage	Ta= +60℃, 90% RH 96hrs
Thermal Shock (Non-operation)	-40℃/30 min ~ +80℃/30 min for 20 cycles Start with cold temperature end with high temperature
Electro Static Discharge	Power off: -Contact discharge, housing metal: ±8kv -Air discharge, housing plastic: ±15kv -Air discharge, housing metal: ±8kv Power on: -Contact discharge, housing metal: ±8kv(Class B) -Air discharge, housing plastic: ±15kv(Class B) -Air discharge, housing metal: ±8kv(Class B) Notes: The ESD test need to combine the LCD display, customer's housing, and main board together.
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces

Notes: The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

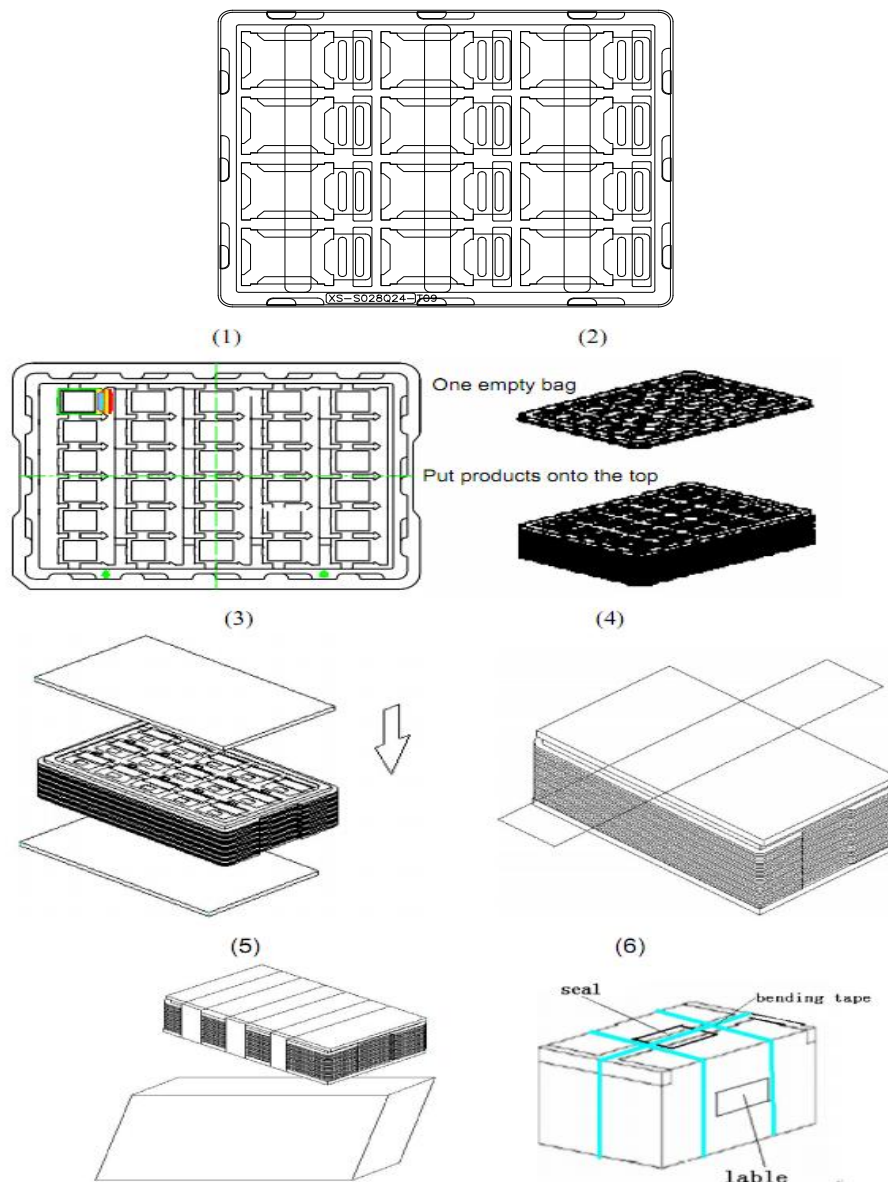
- 1). Air bubble in the LCD
- 2). Seal leak or Glass crack
- 3). Non display or abnormal display
- 4). Brightness reduction >50%

8. Mechanical Drawing



9. Packing

Packing Method



Steps:

1. Put module into tray cavity
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above
4. Fix the cardboard to the tray stack with adhesive tape
5. Put the tray stack into carton
6. Carton sealing with adhesive tape

10. Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2. The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time, the recommend condition is :

Temperature : 0℃ ~40℃ Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.