

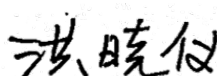
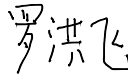
PRODUCT SPECIFICATION

CDTECH Model: **S043HWV94HS-DR12-TK**

CUSTOMER Model: **-**

Description: **4.3 " TFT-LCD Module with RTP**

Version: **1.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2025.5.16	2025.5.16	2025.5.16

CUSTOMER APPROVAL	SIGNATURE	DATE

[illegible]



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1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	4.3	inch
Number of Pixels	800 (H) RGB x 480 (V)	pixels
Display Mode	Normally Black	-
Viewing Direction	Free	o' clock
Interface	RGB	-
Display Colors	16.2M	colors
Outline Dimension	120.50 (H) x 81.10 (V) x 5.03 (D)	mm
Active Area	95.04 (H) x 53.86 (V)	mm
Pixel Pitch	0.1188 (H) x 0.1122 (V)	mm
Driver IC	HX8264+HX8664	-
Operation Temperature	-30~85	°C
Storage Temperature	-30~85	°C

Note1:Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VDD	-0.5	3.96	V	Note 1

Note 1:Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

3. Electrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

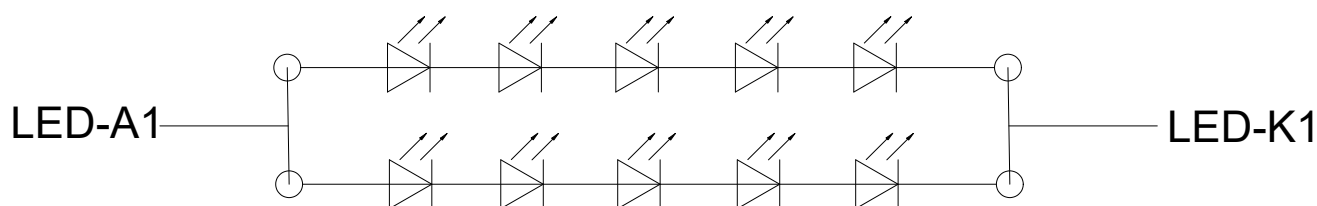
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VDD	3.0	3.3	3.6	V	
Analog supply current	I _{VDD}	135	195	250	mA	VDD=3.3 (White picture)
Logic input voltage	V _{IH}	0.7*VDD	-	VDD	V	
	V _{IL}	GND	-	0.3*VDD	V	

3.2 Recommended Driving Condition for Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I _F	-	40	-	mA	
Driving Voltage	V _F	13.5	-	17	V	
Power consumption	W _{BL}	0.54	-	0.68	W	
LED Life-Time	N/A	-	50,000	-	Hours	Ta=25°C Note 1

Note 1: LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2: LED circuit :



4. Interface Pin Assignment

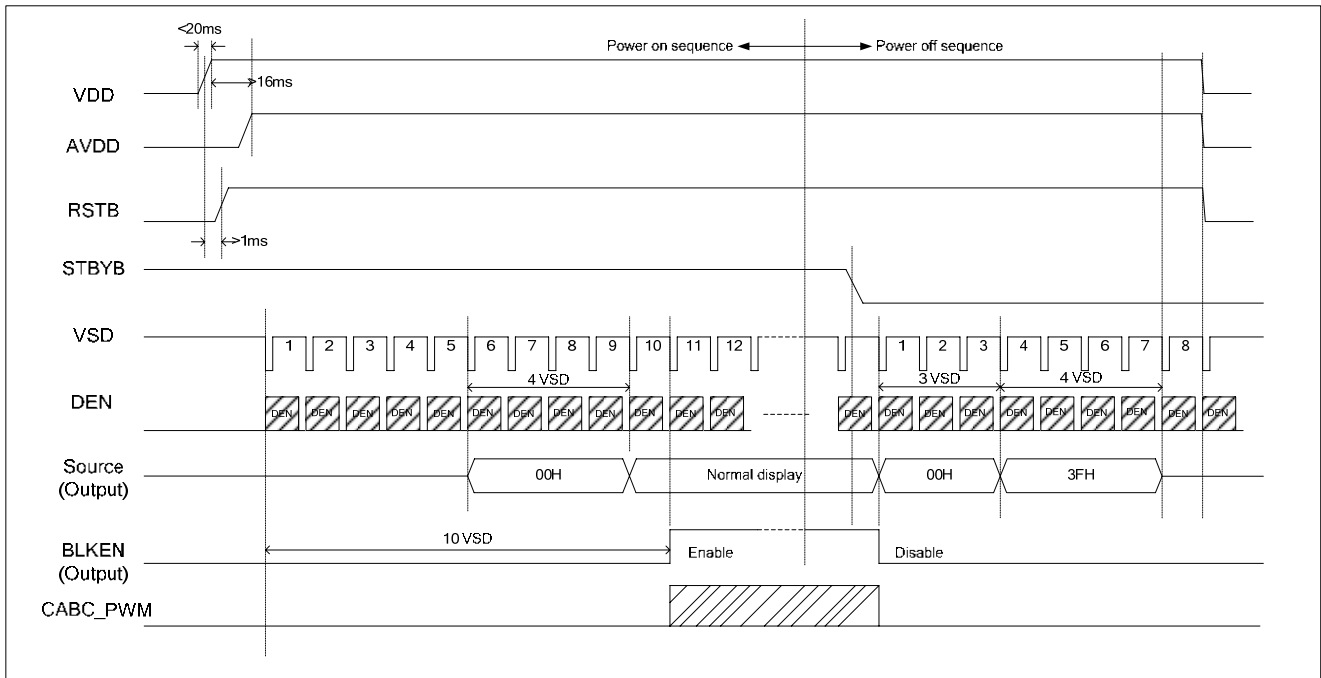
4.1 LCM Pin Assignment

No.	Symbol	Description
1	VBL-	Power for LED backlight (Cathode)
2	VBL+	Power for LED backlight (Anode)
3	GND	Ground
4	VDD	Power supply
5-12	R0-R7	Data bus
13-20	G0-G7	Data bus
21-28	B0-B7	Data bus
29	GND	Ground
30	DCLK	Dot clock signal input. Latching input data at its rising edge
31	DISP	Standby mode. Normally pulled high. DISP="1": Normally operation(Default) DISP="0": Timing controller, source driver will turn off,all output are High-Z.
32	HSYNC	Horizontal sync input. Negative polarity.
33	VSYNC	Vertical sync input. Negative polarity.
34	DE	Data enable input. Active high to enable the input data bus under "DE Mode"
35	NC	No connection
36	GND	Ground
37	XR	The right side signal of TP
38	YD	The down side signal of TP
39	XL	The left side signal of TP
40	YU	The up side signal of TP

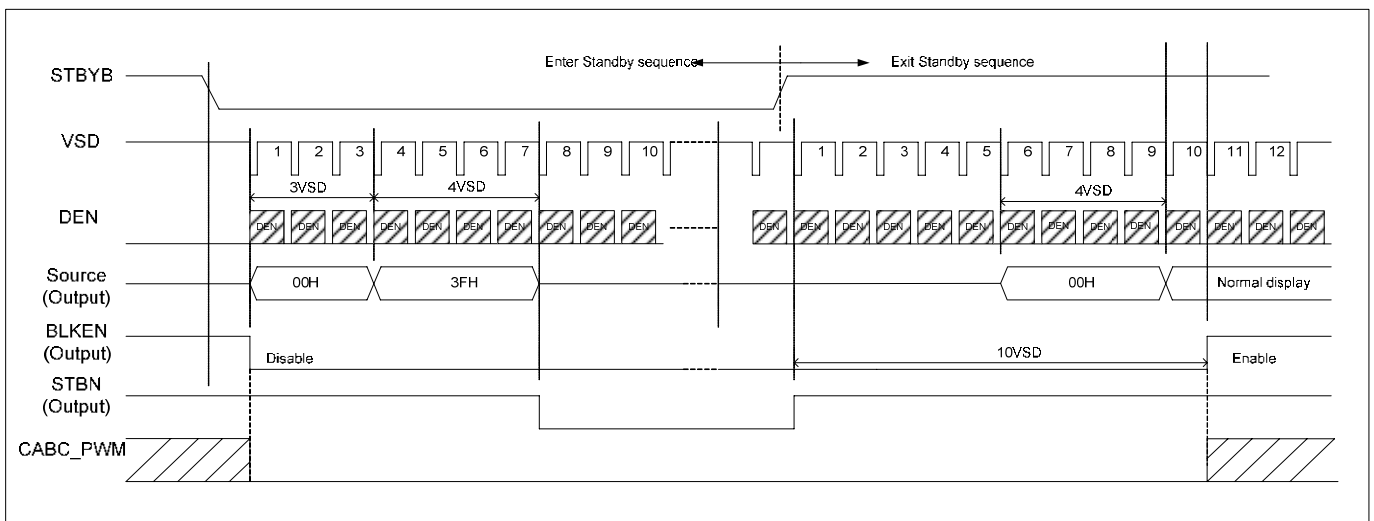
5. Interface Characteristics

5.1 Power on/off control

5.1.1 Power on/off control:



5.1.2 Enter and exit standby mode sequence:

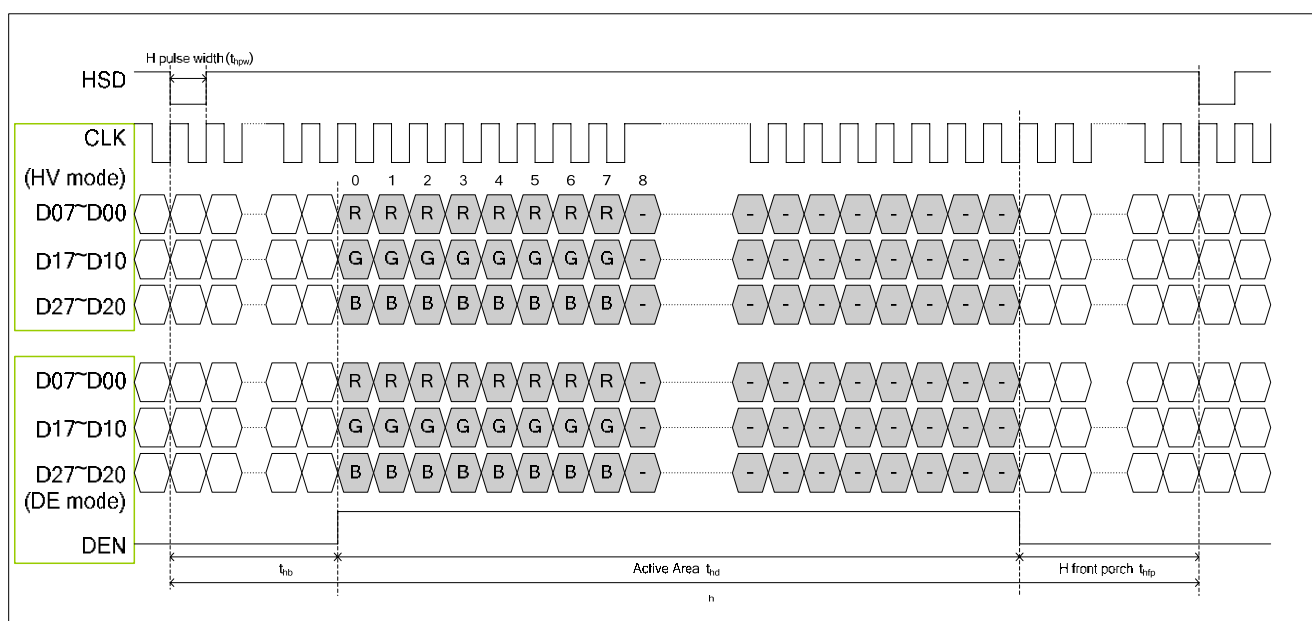


5.2.1 AC electrical characteristics

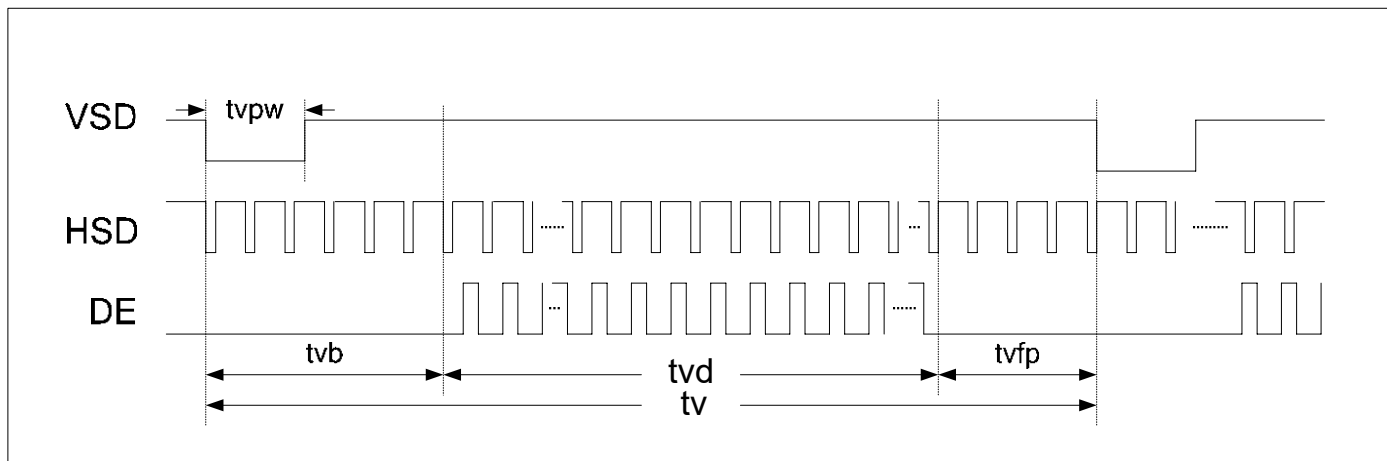
Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{cwh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	us

5.2.2 Data input format

● Horizontal timing



● Vertical timing



● Horizontal timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255	DCLK
HS Back Porch (Blanking)	thb		88		DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	512	DCLK

● Vertical timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd		480		T_H
VS period time	tv	513	525	767	T_H
VS pulse width	tvpw	3	3	255	T_H
VS Back Porch (Blanking)	tvb		32		T_H
VS Front Porch	tvfp	1	13	255	T_H
DE mode Blanking	tv-tvd	4	45	255	T_H

5.3 Waveform

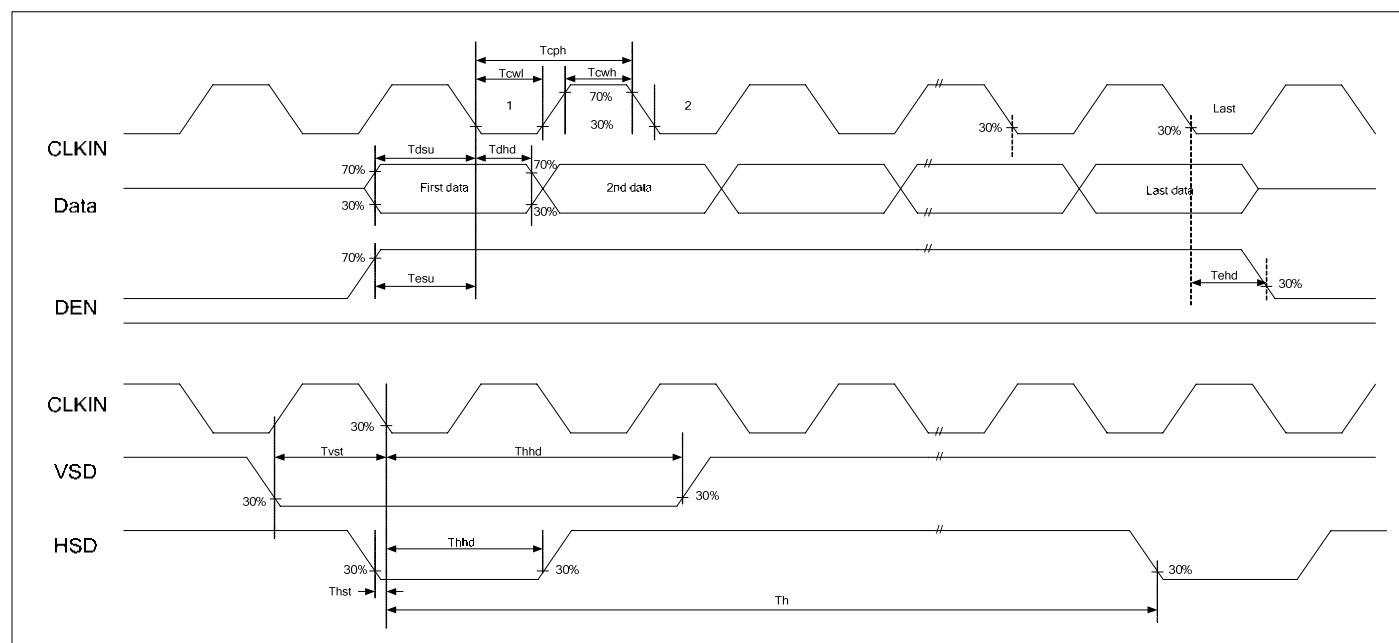
5.3.1 Timing waveform table

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	-
Time from HSD to STV	Thstv		2		CLKIN	-
Time from HSD to CKV	Thckv		20		CLKIN	-
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10		CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74		CLKIN	-

Parallel 24-bit RGB mode

5.3.2 Timing diagram

5.3.2.1 Input clock and data timing waveform:



5.3.2.2 Source output timing waveform (Cascade):

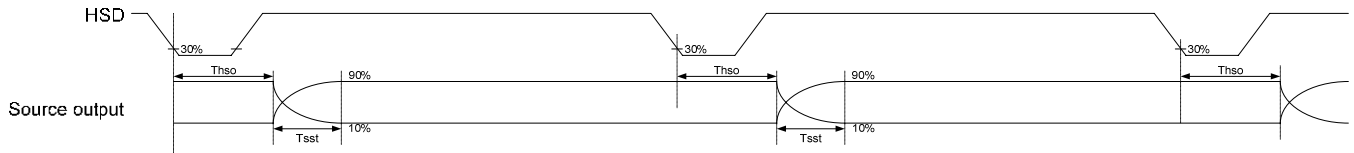
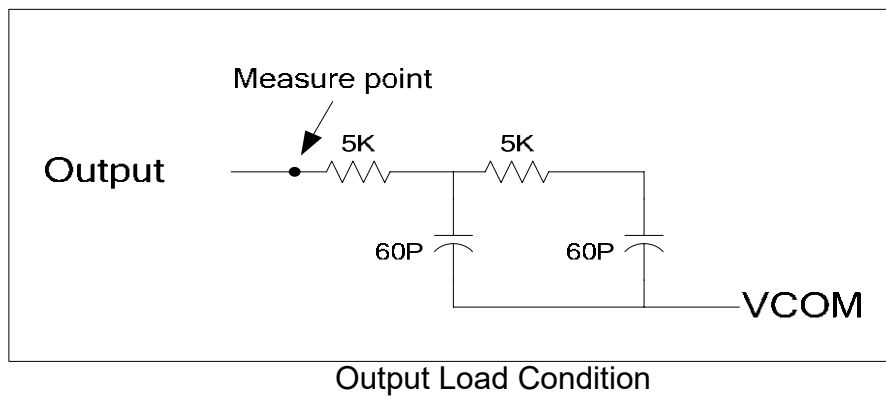
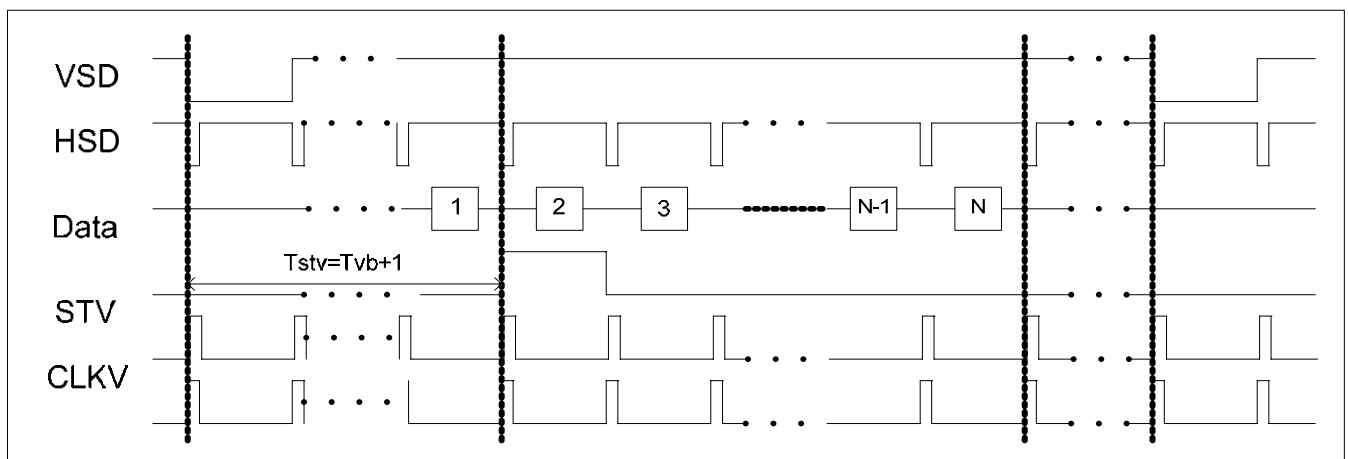


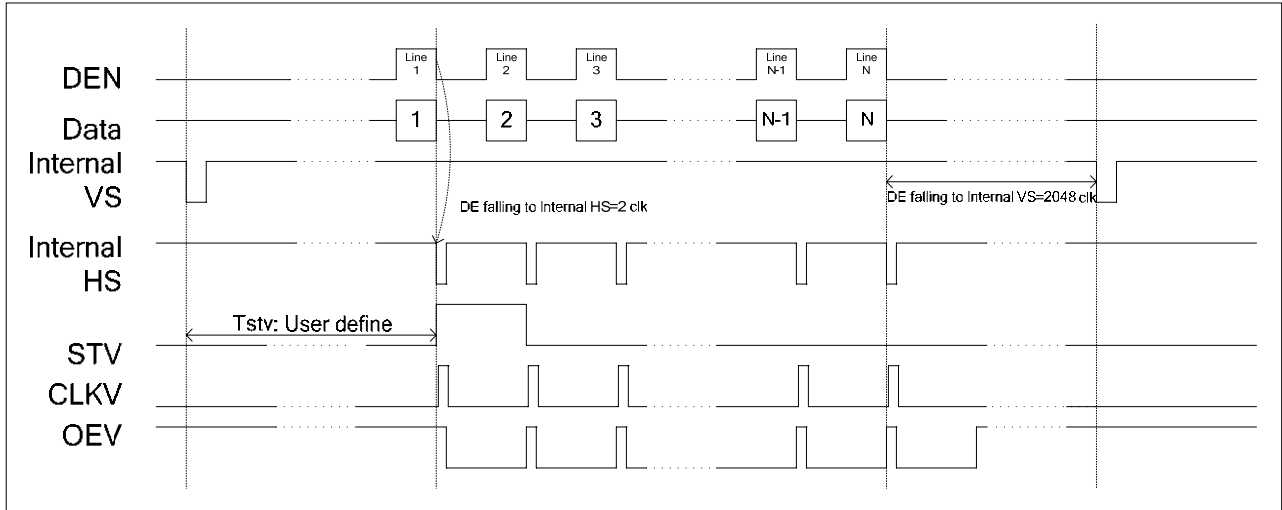
Figure 12.2: Source Output Timing Diagram



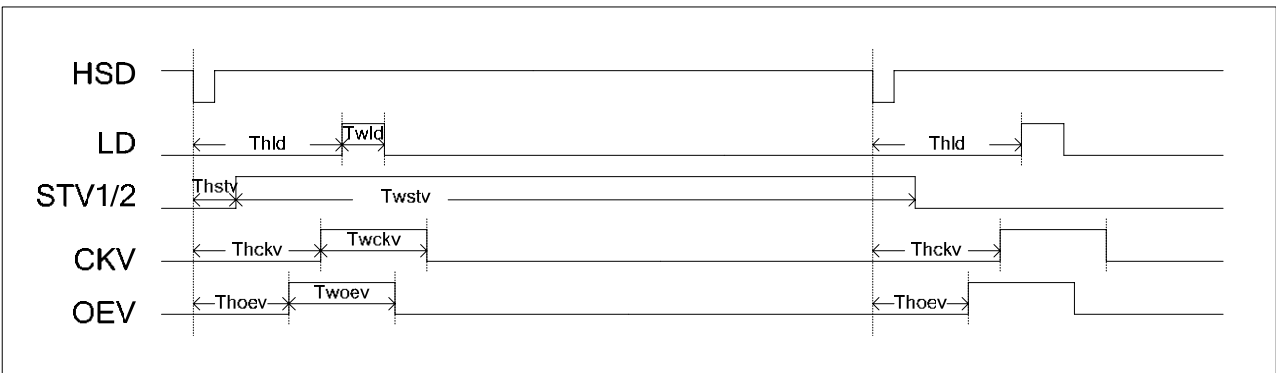
5.3.2.3 Vertical timing diagram HV (Cascade)



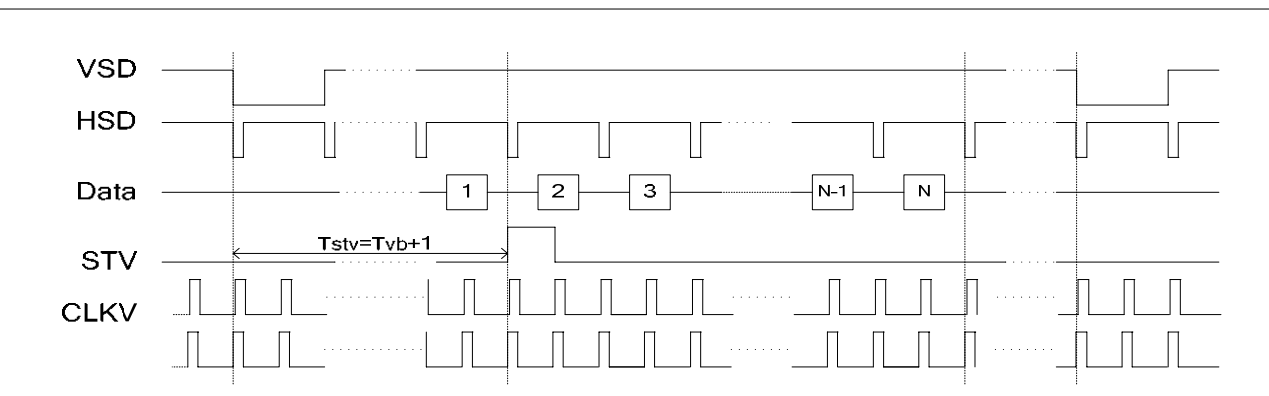
5.3.2.4 Vertical timing diagram DE (Cascade)



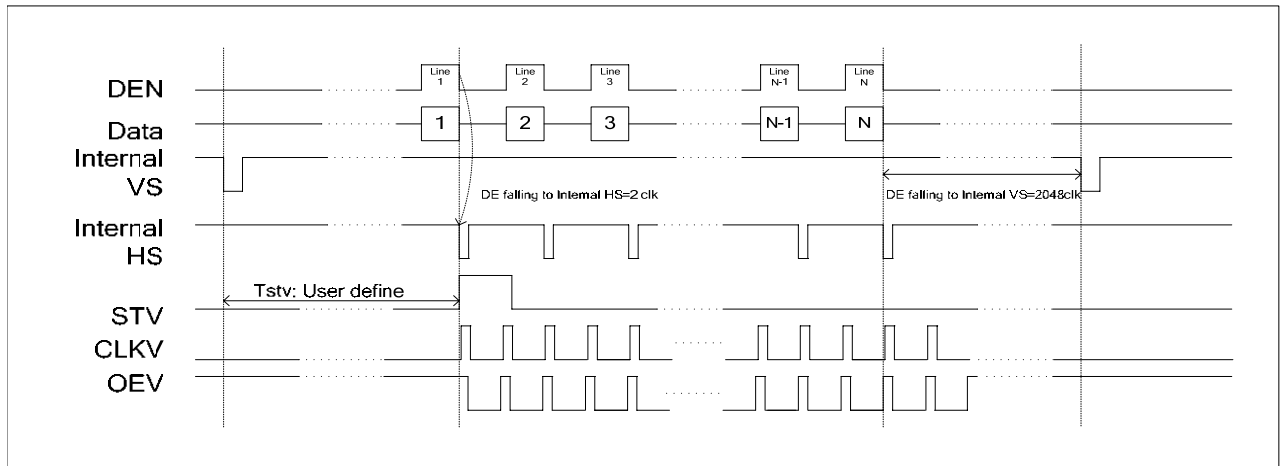
5.3.2.5 Gate output timing diagram (Cascade)



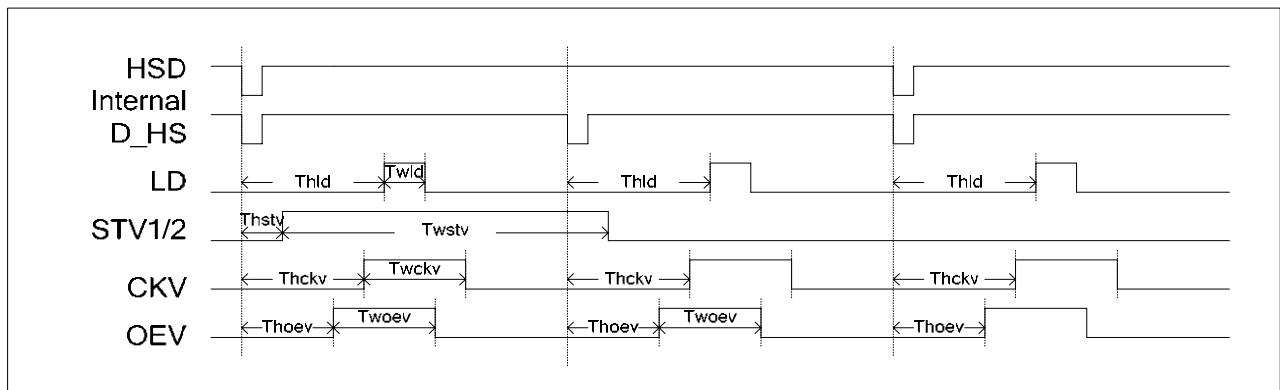
5.3.2.6 Vertical timing diagram HV (Dual gate)



5.3.2.7 Vertical timing diagram DE (Dual gate)



5.3.2.8 Gate output timing diagram (Dual gate)



6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	$\Phi=90^\circ$ (12 o'clock)	70	80	-	deg	Note2
	θ_B	$\Phi=270^\circ$ (6 o'clock)	70	80	-	deg	Note2
	θ_L	$\Phi=180^\circ$ (9 o'clock)	70	80	-	deg	Note2
	θ_R	$\Phi=0^\circ$ (3 o'clock)	70	80	-	deg	Note2
Response Time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	15	20	msec	Note4
	T_{OFF}		-	15	20	msec	Note4
Contrast Ratio	CR		640	800	-	-	Note1 Note3
Color Chromaticity	W_X		0.302	0.332	0.362	-	Note1 Note5
	W_Y		0.329	0.359	0.389	-	Note1 Note5
Luminance	L		350	400	-	cd/m ²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		45	50	-	%	-

Note 1:Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

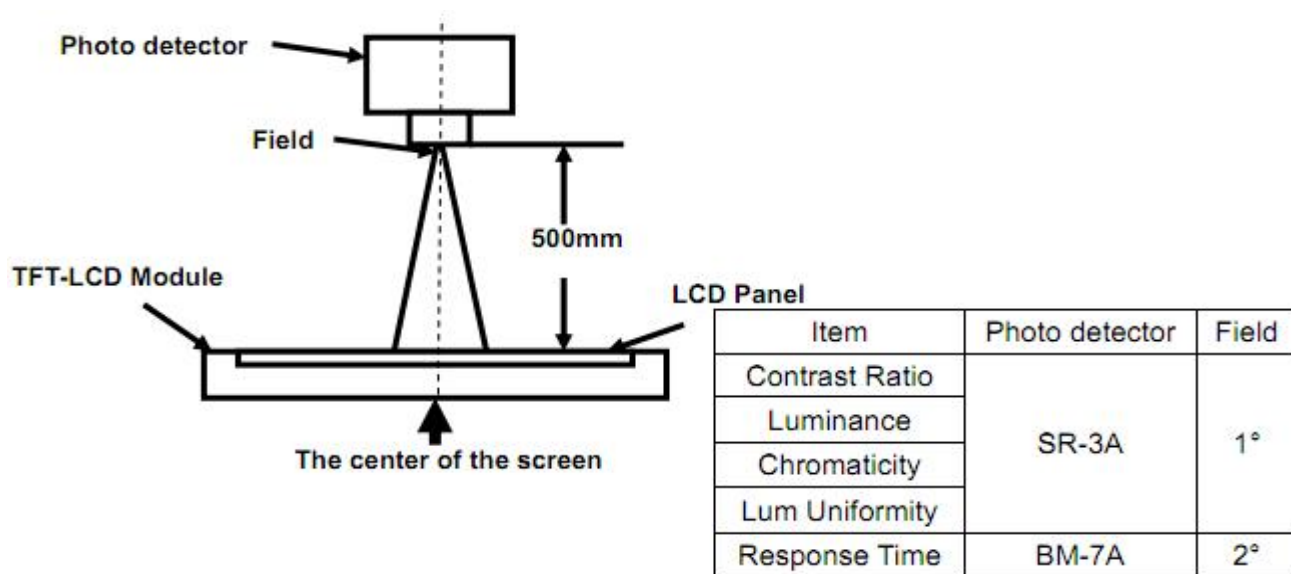


Fig 1

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

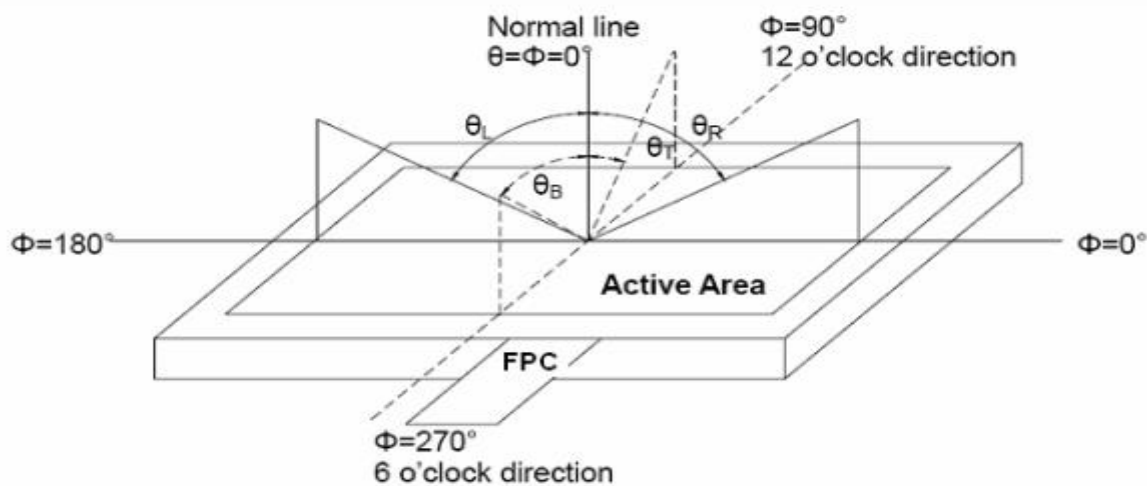


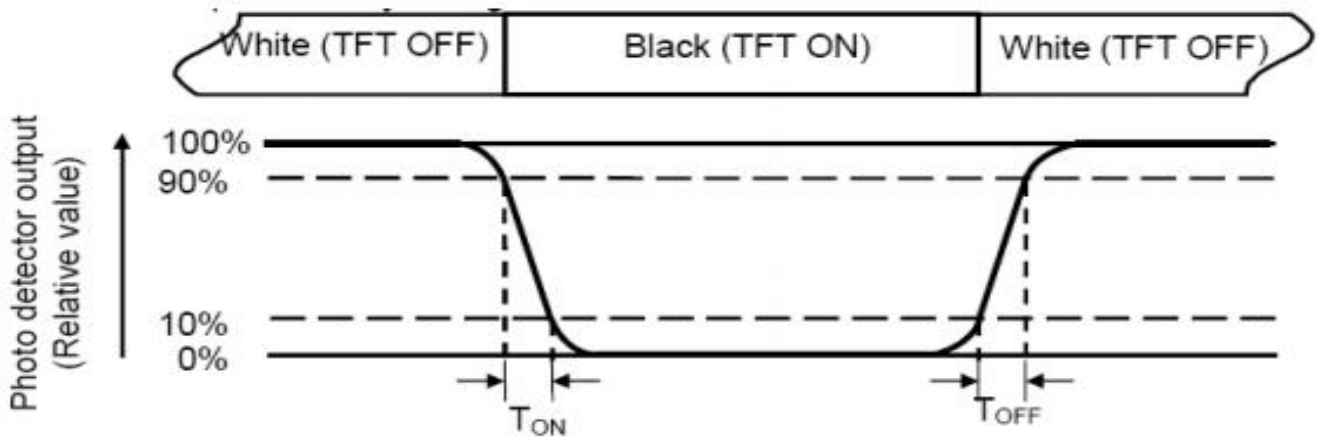
Fig 2 Definition of viewing angle

Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.3-a/b

Note 7: Surface luminance is the luminance with all pixels displaying white.

L_v = Average Surface Luminance with all white pixels ($P_1, P_2, P_3, \dots, P_n$)

For more information see FIG.3-a/b

Note 8:

H,V : Active area(see Figure a)

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7)50cm distance or test spot position : see Figure a.

measurement instrument : TOPCON's luminance meter SR-3A or BM-7 or compatible (see Figure 1).

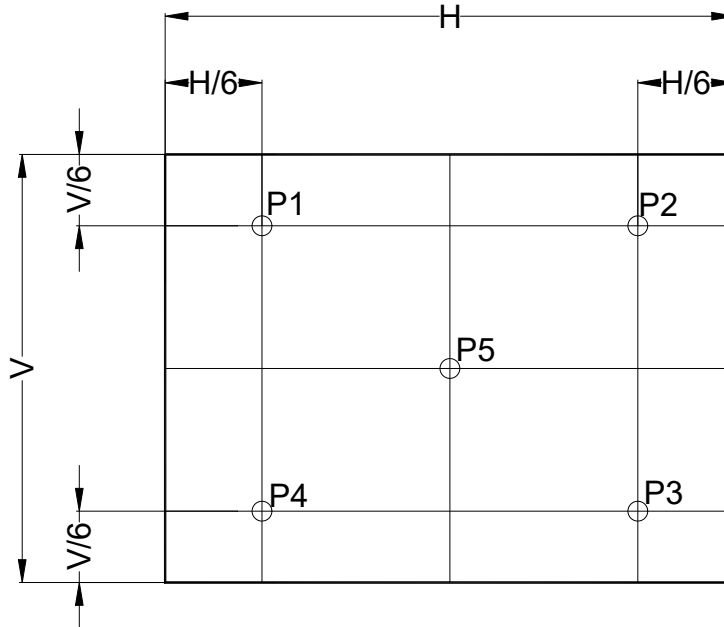


Fig. 3-a Definition of points

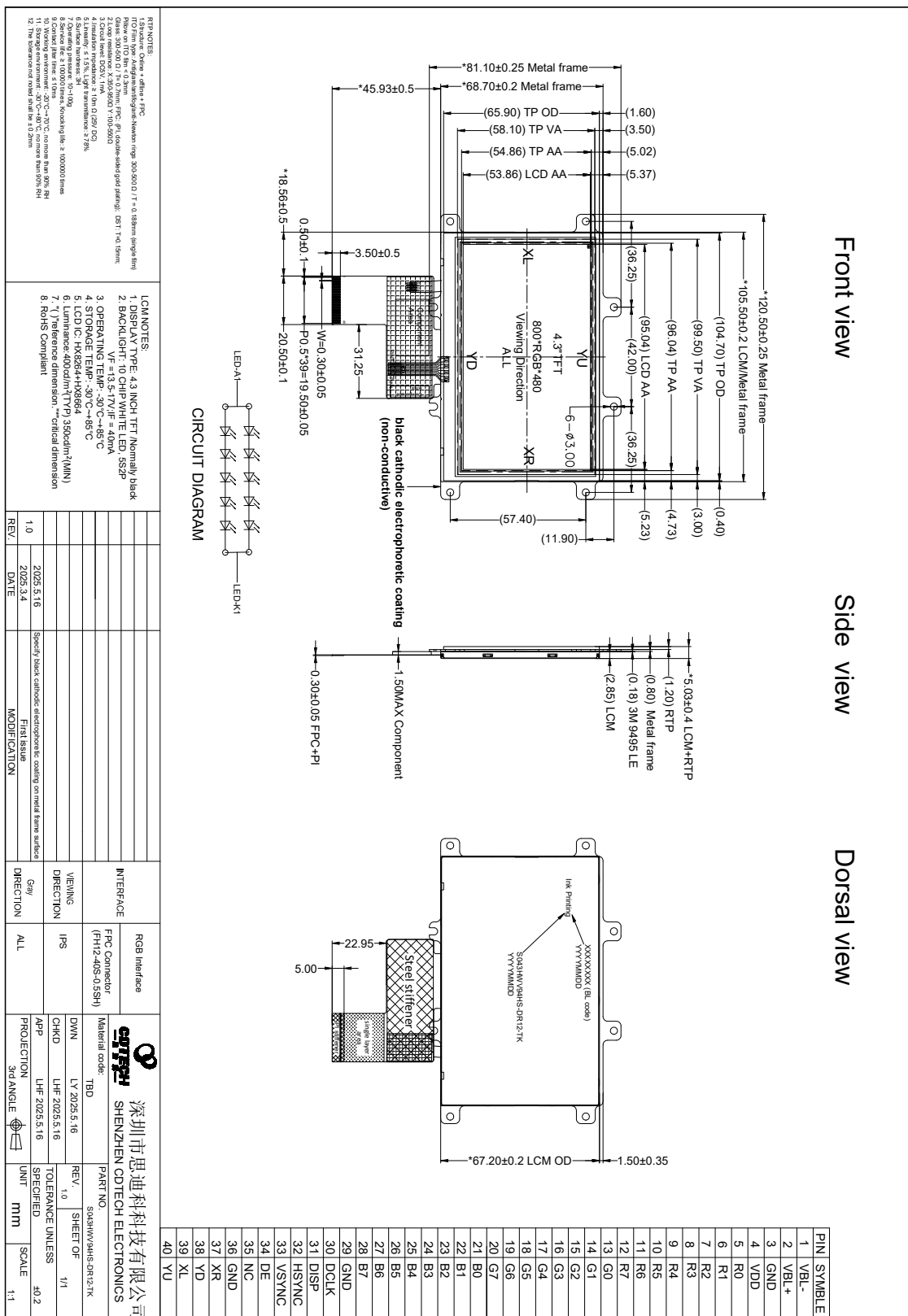
7. Reliability Test Items

Test Item	Test Conditions
High Temperature Storage	Ta= +85℃ 96hrs
Low Temperature Storage	Ta= -30℃ 96hrs
High Temperature Operation	Ta= +85℃ 96hrs
Low Temperature Operation	Ta= -30℃ 96hrs
High Temperature and Humidity Storage	Ta= +60℃, 90% RH 96hrs
Thermal Shock (Non-operation)	-30℃/30 min ~ +85℃/30 min for 20 cycles Start with cold temperature end with high temperature
Electro Static Discharge	Contact = ± 4 kV, class B Air = ± 8 kV, class B R=330Ω,C=150pF
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces

Notes: The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

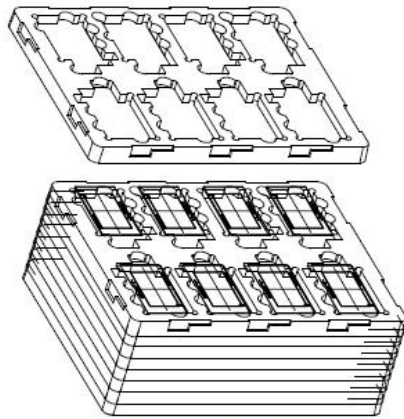
- 1). Air bubble in the LCD
- 2). Seal leak or Glass crack
- 3). Non display or abnormal display
- 4). Brightness reduction >50%

Web: www.cdtech-lcd.com

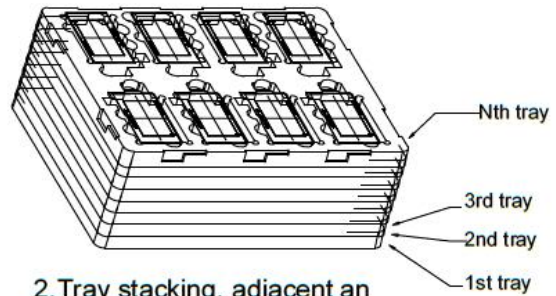


9. Packing

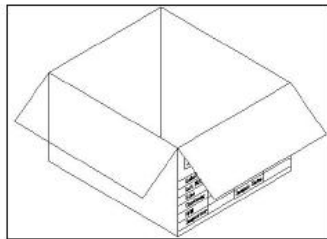
Packing Method



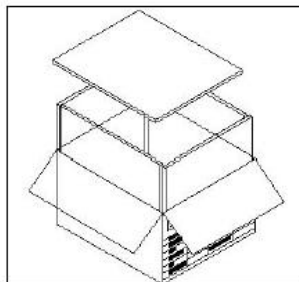
1. Put LCD module into tray cavity



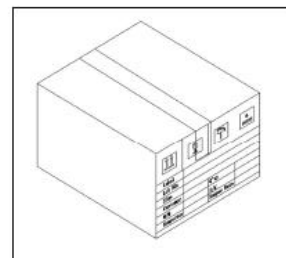
2. Tray stacking, adjacent an upper lower layer with a 180-degree rotation



4. put the tray stack into carton



5. 6 sides of white foams inside the box



6. Carton sealing with adhesive tape

10. Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2. The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time, the recommend condition is :

Temperature : 0℃ ~40℃ Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

10.4 Packaging instructions

When the customers using trays, they have to stack the adjacent trays in a 180° staggered to prevent pressure that could cause product damage.